

# BLF7G22L-160; BLF7G22LS-160

Power LDMOS transistor

Rev. 2.1 — 2 November 2011

Product data sheet

## 1. Product profile

### 1.1 General description

160 W LDMOS power transistor for base station applications at frequencies from 2000 MHz to 2200 MHz.

**Table 1. Typical performance**

*Typical RF performance at  $T_{case} = 25\text{ °C}$  in a common source class-AB production test circuit.*

Mode of operation	f (MHz)	$I_{Dq}$ (mA)	$V_{DS}$ (V)	$P_{L(AV)}$ (W)	$G_p$ (dB)	$\eta_D$ (%)	ACPR (dBc)
2-carrier W-CDMA	2110 to 2170	1300	28	43	18.0	30	-32 <sup>[1]</sup>

[1] Test signal: 3GPP; test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF; carrier spacing 5 MHz.

### 1.2 Features and benefits

- Excellent ruggedness
- High efficiency
- Low  $R_{th}$  providing excellent thermal stability
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

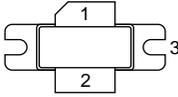
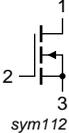
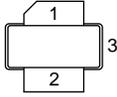
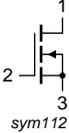
### 1.3 Applications

- RF power amplifiers for W-CDMA base stations and multi carrier applications in the 2000 MHz to 2200 MHz frequency range



## 2. Pinning information

**Table 2. Pinning**

Pin	Description	Simplified outline	Graphic symbol
<b>BLF7G22L-160 (SOT502A)</b>			
1	drain		 sym112
2	gate		
3	source		
<b>BLF7G22LS-160 (SOT502B)</b>			
1	drain		 sym112
2	gate		
3	source		

[1] Connected to flange.

## 3. Ordering information

**Table 3. Ordering information**

Type number	Package		
	Name	Description	Version
BLF7G22L-160	-	flanged LDMOST ceramic package; 2 mounting holes; 2 leads	SOT502A
BLF7G22LS-160	-	earless flanged LDMOST ceramic package; 2 leads	SOT502B

## 4. Limiting values

**Table 4. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-0.5	+13	V
$I_D$	drain current		-	36	A
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature		-	200	°C

## 5. Thermal characteristics

**Table 5. Thermal characteristics**

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	$T_{case} = 80\text{ °C}; P_L = 55\text{ W}$	0.29	K/W

## 6. Characteristics

**Table 6. Characteristics**

$T_j = 25\text{ °C}$ ; unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}$ ; $I_D = 2.16\text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10\text{ V}$ ; $I_D = 216\text{ mA}$	1.5	1.9	2.3	V
$I_{DSS}$	drain leakage current	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 28\text{ V}$	-	-	4.5	$\mu\text{A}$
$I_{DSX}$	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$ ; $V_{DS} = 10\text{ V}$	34	-	-	A
$I_{GSS}$	gate leakage current	$V_{GS} = 11\text{ V}$ ; $V_{DS} = 0\text{ V}$	-	-	450	nA
$g_{fs}$	forward transconductance	$V_{DS} = 10\text{ V}$ ; $I_D = 10.8\text{ A}$	-	20	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}$ ; $I_D = 7.56\text{ A}$	-	0.06	-	$\Omega$

## 7. Test information

**Table 7. Application information**

Mode of operation: 2-carrier W-CDMA; PAR 8.4 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 PDPCH;  $f_1 = 2112.5\text{ MHz}$ ;  $f_2 = 2117.5\text{ MHz}$ ;  $f_3 = 2162.5\text{ MHz}$ ;  $f_4 = 2167.5\text{ MHz}$ ; RF performance at  $V_{DS} = 28\text{ V}$ ;  $I_{Dq} = 1300\text{ mA}$ ;  $T_{case} = 25\text{ °C}$ ; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$G_p$	power gain	$P_{L(AV)} = 43\text{ W}$	16.5	18.0	-	dB
$RL_{in}$	input return loss	$P_{L(AV)} = 43\text{ W}$	-	-15	-6.5	dB
$\eta_D$	drain efficiency	$P_{L(AV)} = 43\text{ W}$	27	30	-	%
$ACPR_{5M}$	adjacent channel power ratio (5 MHz)	$P_{L(AV)} = 43\text{ W}$	-	-32	-28	dBc

**Table 8. Application information**

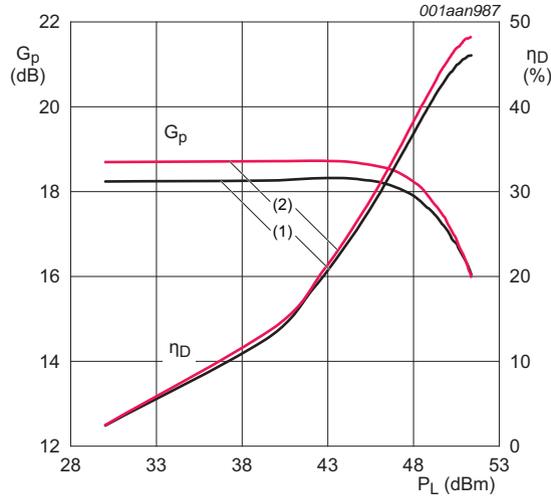
Mode of operation: 1-carrier W-CDMA; PAR 7.2 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 PDPCH;  $f = 2167.5\text{ MHz}$ ; RF performance at  $V_{DS} = 28\text{ V}$ ;  $I_{Dq} = 1300\text{ mA}$ ;  $T_{case} = 25\text{ °C}$ ; unless otherwise specified; in a class-AB production test circuit.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$PAR_O$	output peak-to-average ratio	$P_{L(AV)} = 100\text{ W}$ ; at 0.01 % probability on CCDF	3.9	4.15	-	dB

### 7.1 Ruggedness in class-AB operation

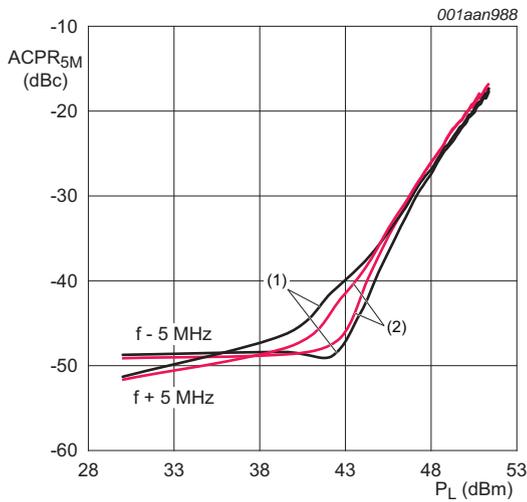
The BLF7G22L-160 and BLF7G22LS-160 are capable of withstanding a load mismatch corresponding to VSWR = 10 : 1 through all phases under the following conditions:  $V_{DS} = 28\text{ V}$ ;  $I_{Dq} = 1300\text{ mA}$ ;  $P_L = 160\text{ W}$ ;  $f = 2110\text{ MHz}$ .

7.2 2-Carrier W-CDMA 5 MHz



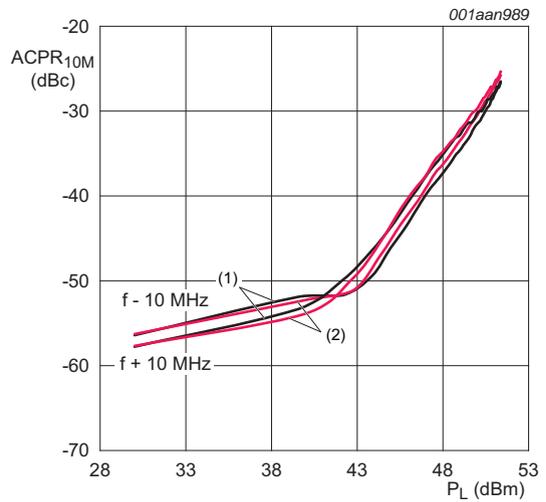
$V_{DS} = 28$  V;  $I_{Dq} = 1300$  mA.  
 (1)  $f = 2110$  MHz  
 (2)  $f = 2170$  MHz

Fig 1. Power gain and drain efficiency as function of load power; typical values



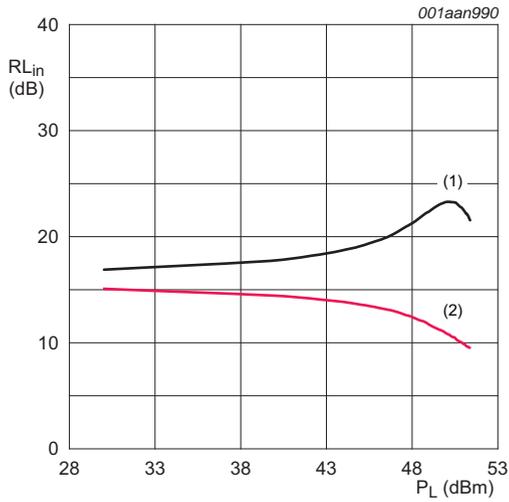
$V_{DS} = 28$  V;  $I_{Dq} = 1300$  mA.  
 (1)  $f = 2110$  MHz  
 (2)  $f = 2170$  MHz

Fig 2. Adjacent channel power ratio ( $\pm 5$  MHz) as a function of load power; typical values



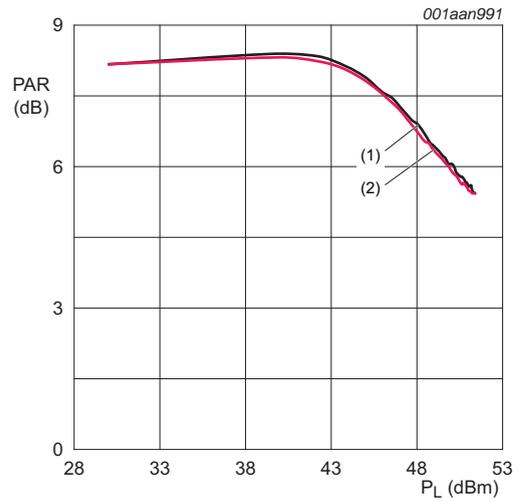
$V_{DS} = 28$  V;  $I_{Dq} = 1300$  mA.  
 (1)  $f = 2110$  MHz  
 (2)  $f = 2170$  MHz

Fig 3. Adjacent channel power ratio ( $\pm 10$  MHz) as a function of load power; typical values



$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

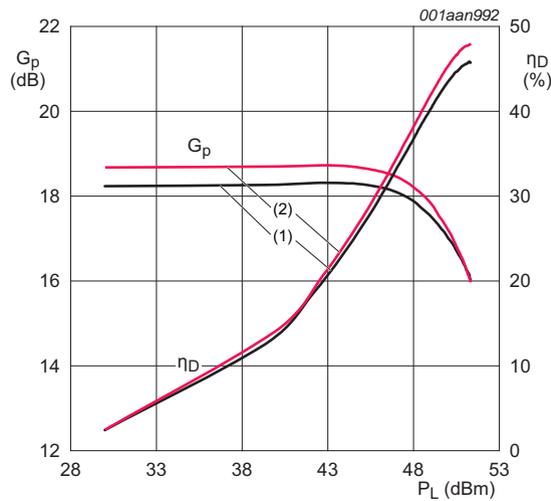
**Fig 4. Input return loss as function of load power; typical values**



$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

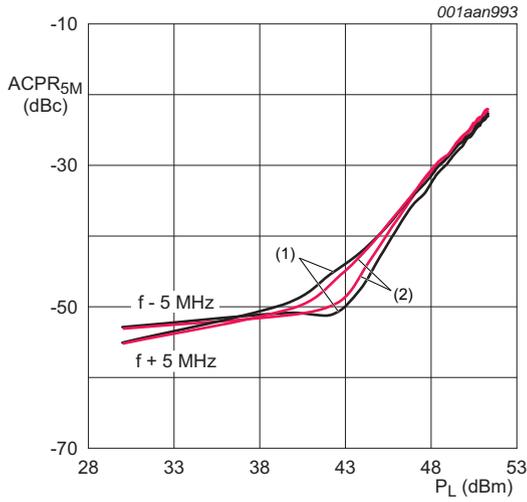
**Fig 5. Peak-to-average power ratio as function of load power; typical values**

### 7.3 2-Carrier W-CDMA 10 MHz



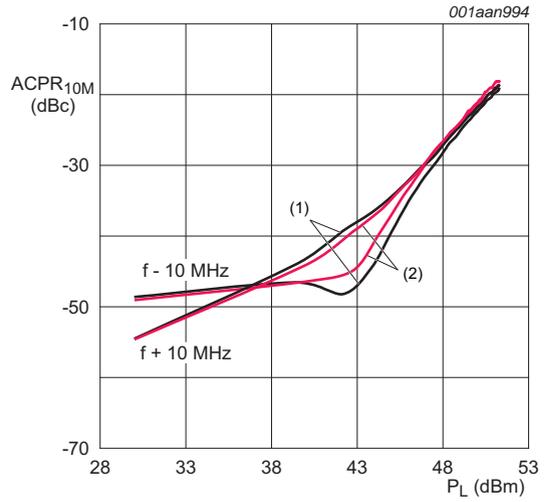
$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

**Fig 6. Power gain and drain efficiency as function of load power; typical values**



$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

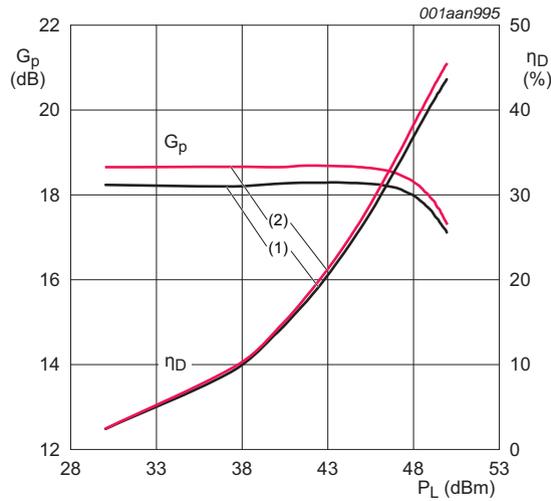
**Fig 7. Adjacent channel power ratio ( $\pm 5\text{ MHz}$ ) as a function of load power; typical values**



$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

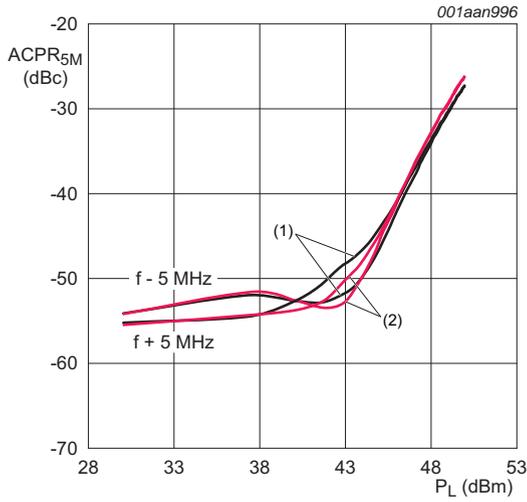
**Fig 8. Adjacent channel power ratio ( $\pm 10\text{ MHz}$ ) as a function of load power; typical values**

## 7.4 1-Carrier W-CDMA



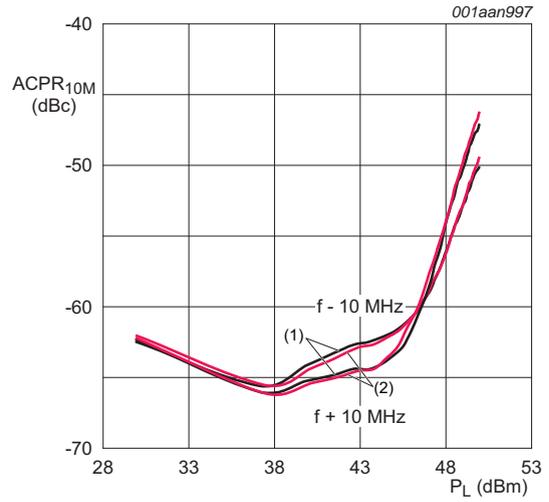
$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

**Fig 9. Power gain and drain efficiency as function of load power; typical values**



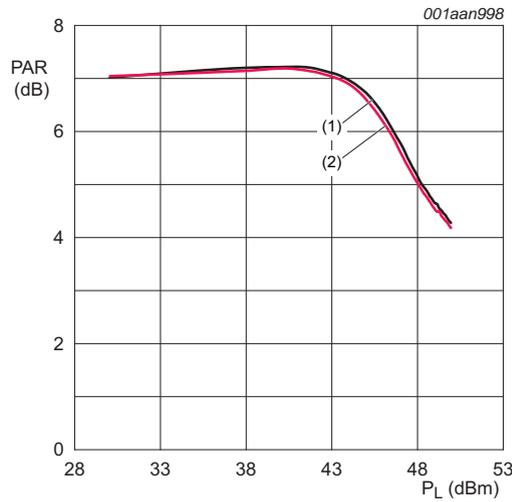
$V_{DS} = 28\text{ V}; I_{DQ} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

**Fig 10. Adjacent channel power ratio ( $\pm 5\text{ MHz}$ ) as a function of load power; typical values**



$V_{DS} = 28\text{ V}; I_{DQ} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

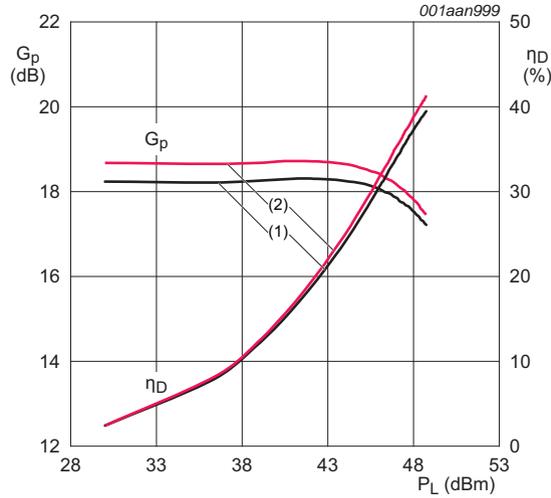
**Fig 11. Adjacent channel power ratio ( $\pm 10\text{ MHz}$ ) as a function of load power; typical values**



$V_{DS} = 28\text{ V}; I_{DQ} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

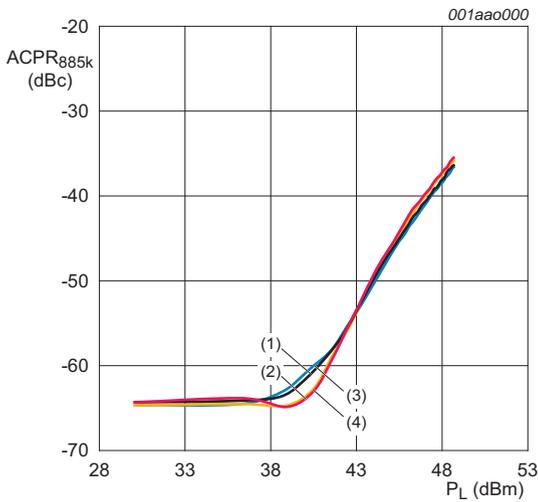
**Fig 12. Peak-to-average power ratio as function of load power; typical values**

7.5 IS-95



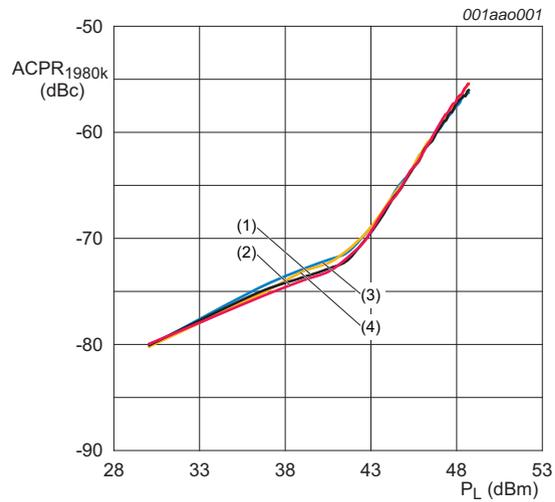
$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}$   
 (2)  $f = 2170\text{ MHz}$

Fig 13. Power gain and drain efficiency as function of load power; typical values



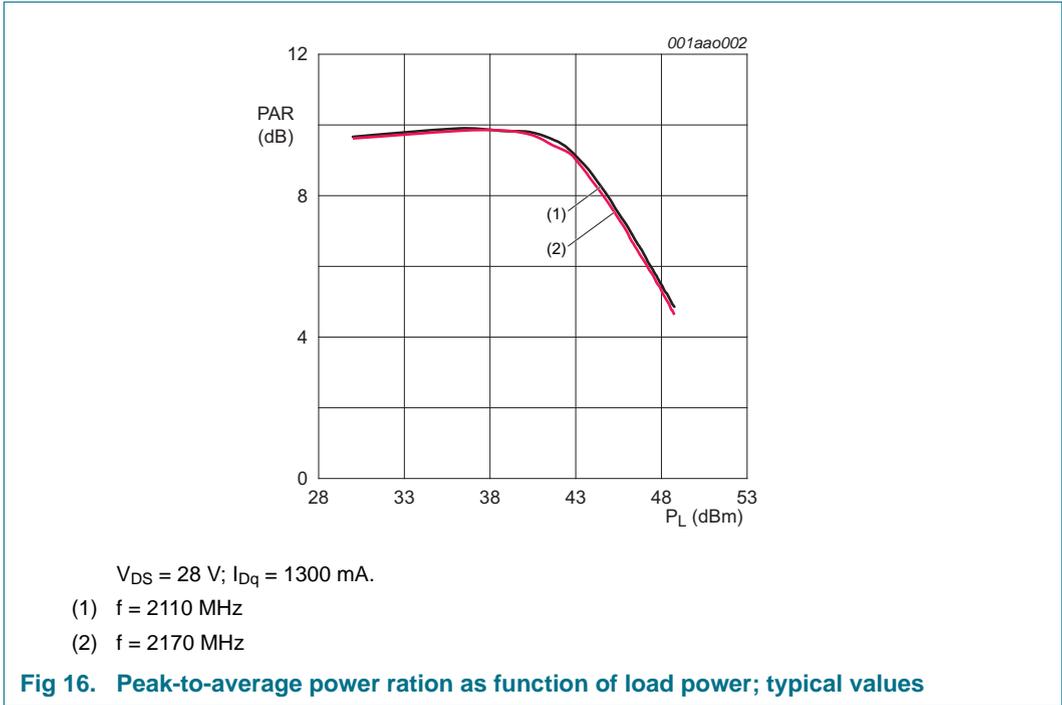
$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}; f + 885\text{ kHz}$   
 (2)  $f = 2170\text{ MHz}; f + 885\text{ kHz}$   
 (3)  $f = 2110\text{ MHz}; f - 885\text{ kHz}$   
 (4)  $f = 2170\text{ MHz}; f - 885\text{ kHz}$

Fig 14. Adjacent channel power ratio ( $\pm 5\text{ MHz}$ ) as a function of load power; typical values

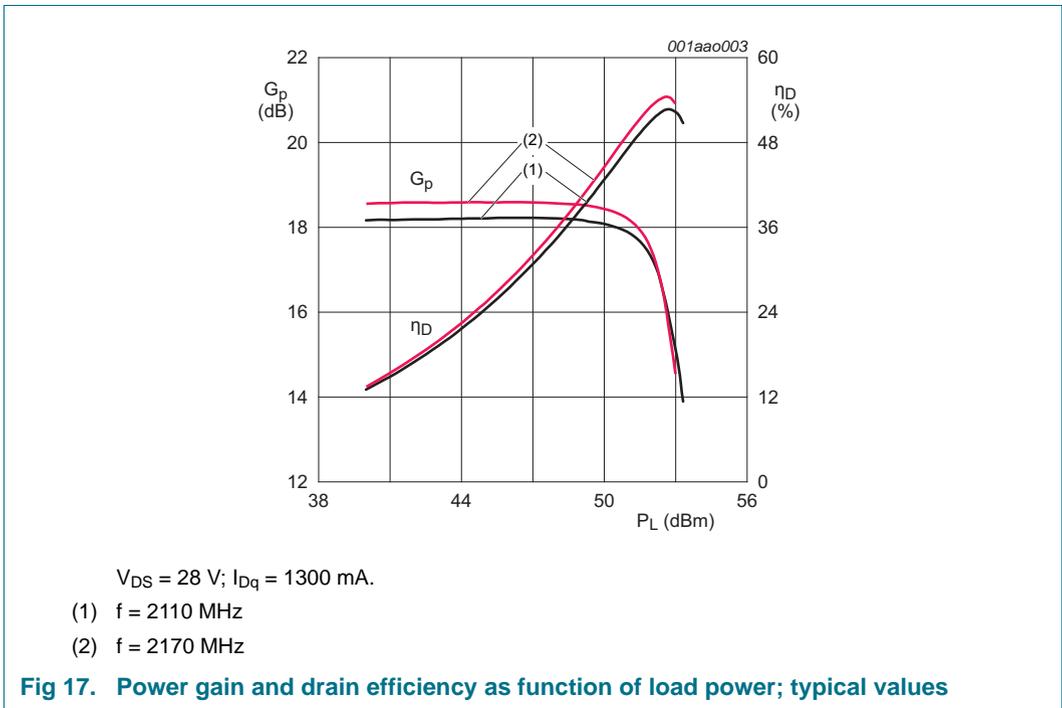


$V_{DS} = 28\text{ V}; I_{Dq} = 1300\text{ mA}.$   
 (1)  $f = 2110\text{ MHz}; f + 1980\text{ kHz}$   
 (2)  $f = 2170\text{ MHz}; f + 1980\text{ kHz}$   
 (3)  $f = 2110\text{ MHz}; f - 1980\text{ kHz}$   
 (4)  $f = 2170\text{ MHz}; f - 1980\text{ kHz}$

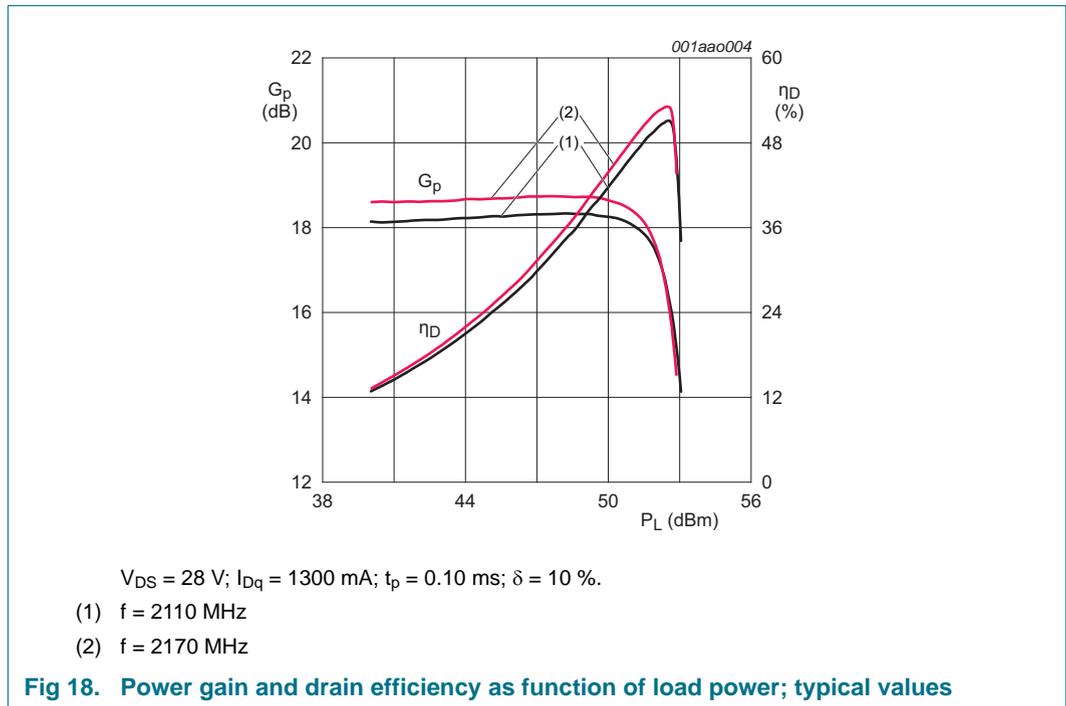
Fig 15. Adjacent channel power ratio ( $\pm 10\text{ MHz}$ ) as a function of load power; typical values



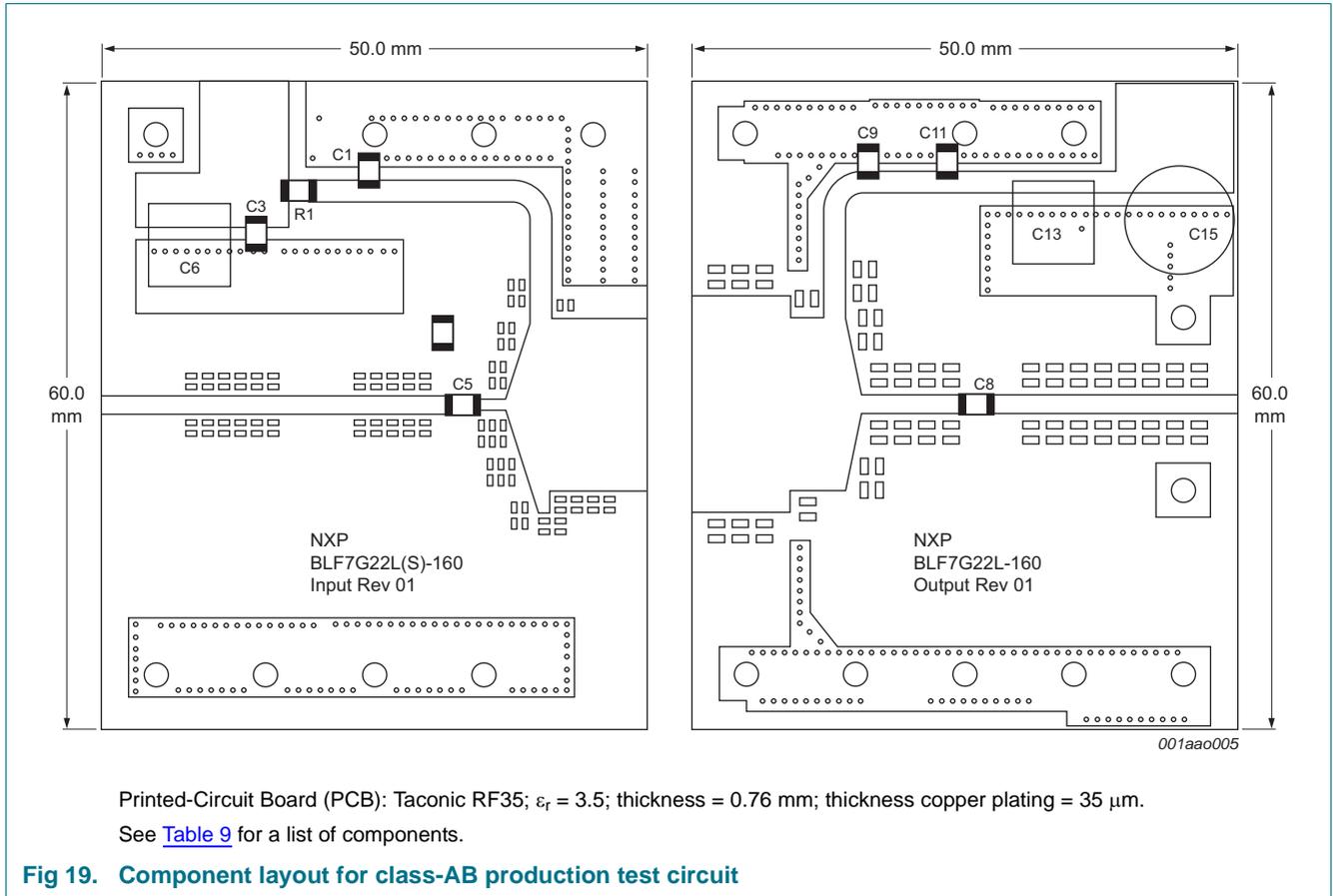
7.6 CW



7.7 CW-pulsed



7.8 Test circuit



**Table 9. List of components**

For test circuit see [Figure 19](#).

Component	Description	Value	Remarks
C1, C5, C8, C9	multilayer ceramic chip capacitor	68 pF	[1]
C3, C11	multilayer ceramic chip capacitor	820 pF	[2]
C6, C13	multilayer ceramic chip capacitor	10 $\mu\text{F}$	[3]
C15	electrolytic capacitor	470 $\mu\text{F}$ ; 63 V	
R1	SMD resistor	12 $\Omega$	Philips 1206

[1] American Technical Ceramics type 800B or capacitor of same quality.

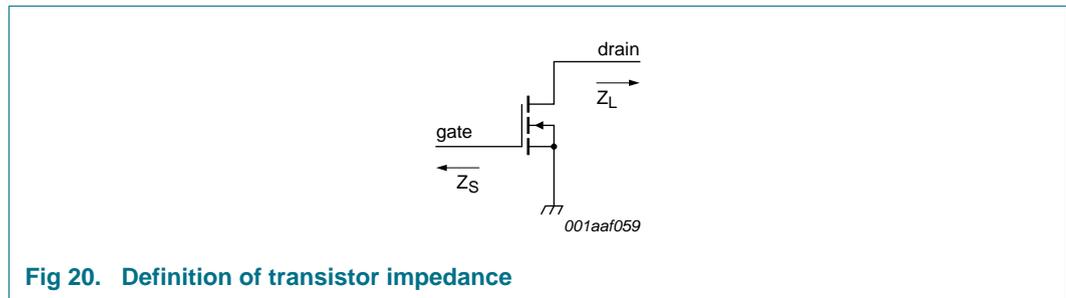
[2] American Technical Ceramics type 100A or capacitor of same quality.

[3] TDK or capacitor of same quality.

**7.9 Impedance information**

**Table 10. Typical impedance**  
*Typical values unless otherwise specified.*

<b>f</b> <b>MHz</b>	<b>Z<sub>S</sub></b> <b>Ω</b>	<b>Z<sub>L</sub></b> <b>Ω</b>
2050	1.39 – j4.13	1.41 – j3.80
2080	1.67 – j3.93	1.38 – j3.63
2110	2.01 – j3.89	1.35 – j3.45
2140	2.28 – j4.09	1.33 – j3.28
2170	2.27 – j4.47	1.31 – j3.12
2200	1.92 – j4.76	1.28 – j2.95
2230	1.42 – j4.75	1.26 – j2.79



**Fig 20. Definition of transistor impedance**

8. Package outline

Flanged LDMOST ceramic package; 2 mounting holes; 2 leads

SOT502A

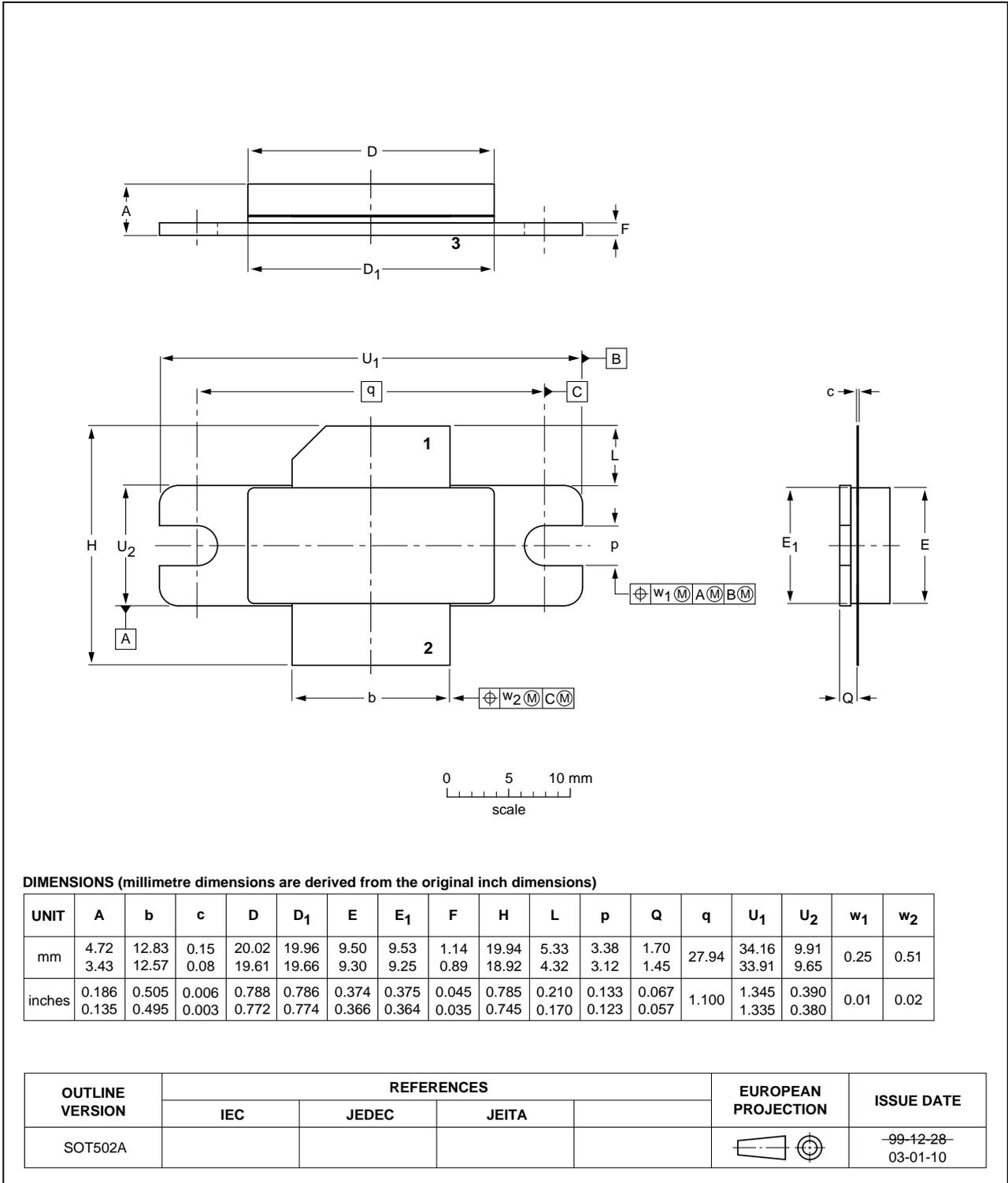


Fig 21. Package outline SOT502A

Earless flanged LDMOST ceramic package; 2 leads

SOT502B

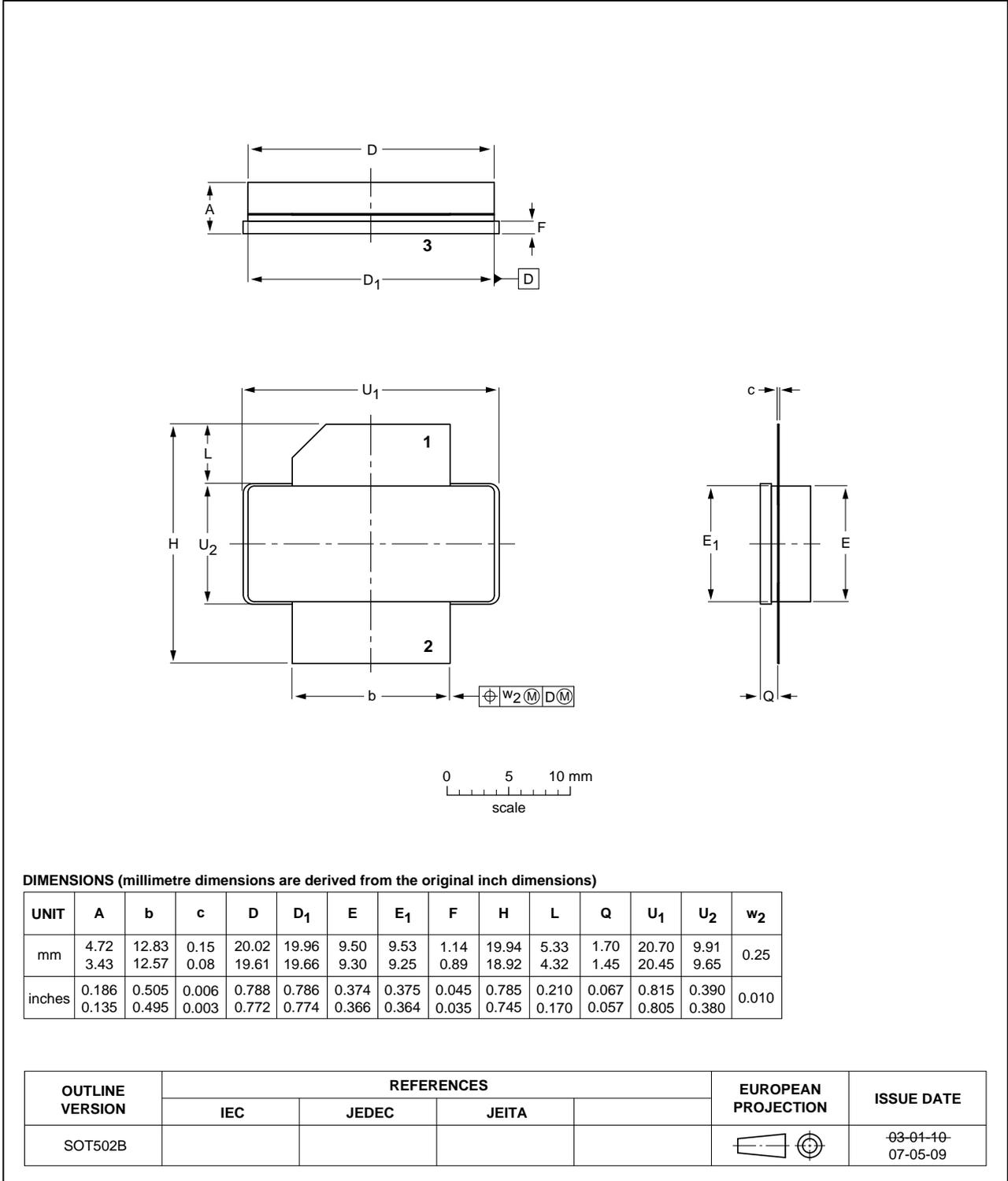


Fig 22. Package outline SOT502B

## 9. Abbreviations

**Table 11. Abbreviations**

Acronym	Description
3GPP	Third Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical CHannel
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
LDMOST	Laterally Diffused Metal Oxide Semiconductor Transistor
PAR	Peak-to-Average power Ratio
PDPCH	transmission Power of the Dedicated Physical CHannel
RF	Radio Frequency
SMD	Surface Mounted Device
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

## 10. Revision history

**Table 12. Revision history**

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF7G22L-160_7G22LS-160 v.2.1	20111102	Product data sheet	-	BLF7G22L-160_7G22LS-160 v.2
Modifications:	<ul style="list-style-type: none"> <li>• <a href="#">Table 3</a>: amended package descriptions</li> </ul>			
BLF7G22L-160_7G22LS-160 v.2	20111020	Product data sheet	-	BLF7G22L-160_7G22LS-160 v.1
Modifications:	<ul style="list-style-type: none"> <li>• The status of this document has been changed to Product data sheet</li> <li>• <a href="#">Table 7 on page 3</a>: the minimum value for <math>\eta_D</math> has been changed</li> </ul>			
BLF7G22L-160_7G22LS-160 v.1	20110427	Preliminary data sheet	-	-

## 11. Legal information

### 11.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 11.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 11.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or

malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

## 11.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 12. Contact information

---

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 13. Contents

---

<b>1</b>	<b>Product profile</b> . . . . .	<b>1</b>
1.1	General description . . . . .	1
1.2	Features and benefits . . . . .	1
1.3	Applications . . . . .	1
<b>2</b>	<b>Pinning information</b> . . . . .	<b>2</b>
<b>3</b>	<b>Ordering information</b> . . . . .	<b>2</b>
<b>4</b>	<b>Limiting values</b> . . . . .	<b>2</b>
<b>5</b>	<b>Thermal characteristics</b> . . . . .	<b>2</b>
<b>6</b>	<b>Characteristics</b> . . . . .	<b>3</b>
<b>7</b>	<b>Test information</b> . . . . .	<b>3</b>
7.1	Ruggedness in class-AB operation . . . . .	3
7.2	2-Carrier W-CDMA 5 MHz . . . . .	4
7.3	2-Carrier W-CDMA 10 MHz . . . . .	5
7.4	1-Carrier W-CDMA . . . . .	6
7.5	IS-95 . . . . .	8
7.6	CW . . . . .	9
7.7	CW-pulsed . . . . .	10
7.8	Test circuit . . . . .	11
7.9	Impedance information . . . . .	12
<b>8</b>	<b>Package outline</b> . . . . .	<b>13</b>
<b>9</b>	<b>Abbreviations</b> . . . . .	<b>15</b>
<b>10</b>	<b>Revision history</b> . . . . .	<b>15</b>
<b>11</b>	<b>Legal information</b> . . . . .	<b>16</b>
11.1	Data sheet status . . . . .	16
11.2	Definitions . . . . .	16
11.3	Disclaimers . . . . .	16
11.4	Trademarks . . . . .	17
<b>12</b>	<b>Contact information</b> . . . . .	<b>17</b>
<b>13</b>	<b>Contents</b> . . . . .	<b>18</b>

---

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

---

© NXP B.V. 2011.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 2 November 2011

Document identifier: BLF7G22L-160\_7G22LS-160