### **F98 Series**

### Resin-Molded Chip, High CV Undertab





#### **FEATURES**

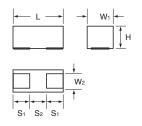
- Compliant to the RoHS2 directive 2011/65/EU
- SMD face down design
- Small and low profile





#### **APPLICATIONS**

- Smartphone
- Mobile phone
- Wireless module
- Hearing aid

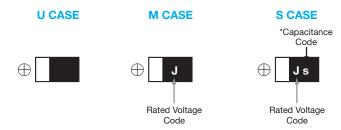


### **CASE DIMENSIONS:** millimeters (inches)

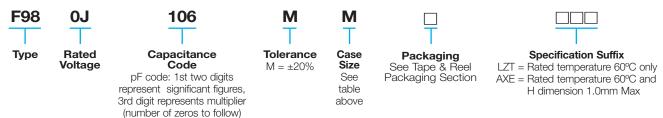
Code	L	W <sub>1</sub>	W <sub>2</sub>	Н	S <sub>1</sub>	S <sub>2</sub>
М	1.60 <sup>+0.20</sup> <sub>-0.10</sub>	0.85 <sup>+0.20</sup> <sub>-0.10</sub>	0.65±0.10	0.80±0.10*3	0.50±0.10	0.60±0.10
	(0.063 <sup>+0.008</sup> <sub>-0.004</sub> )	(0.033 <sup>+0.008</sup> <sub>-0.004</sub> )	(0.026±0.004)	(0.031±0.004)	(0.020±0.004)	(0.024±0.004)
s	2.00 <sup>+0.20</sup> <sub>-0.10</sub>	1.25 <sup>+0.20</sup> <sub>-0.10</sub>	0.90±0.10	0.80±0.10	0.50±0.10	1.00±0.10
	(0.079 <sup>+0.008</sup> <sub>-0.004</sub> )	(0.049 <sup>+0.008</sup> <sub>-0.004</sub> )	(0.035±0.004)	(0.031±0.004)	(0.020±0.004)	(0.039±0.004)
U	1.10±0.05	0.60±0.05	0.35±0.05	0.55±0.05	0.30±0.05	0.50±0.05
	(0.043±0.002)	(0.024±0.002)	(0.014±0.002)	(0.022±0.002)	(0.012±0.002)	(0.020±0.002)

<sup>\*3</sup> F980J107MMAAXE: 1.0mm Max.

#### **MARKING**



#### **HOW TO ORDER**



#### **TECHNICAL SPECIFICATIONS**

Category Temperature Range:	-55 to +125°C
Rated Temperature:	+85°C
Capacitance Tolerance:	±20% at 120Hz
Dissipation Factor:	Refer to next page
ESR 100kHz:	Refer to next page
Leakage Current:	Refer to next page
	Provided that:
	After 5 minute's application of rated voltage, leakage current at 85°C
	10 times or less than 20°C specified value.
	After 5 minute's application of rated voltage, leakage current at 125°C
	12.5 times or less than 20°C specified value.

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# CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capacitance		Rated Voltage						
μF	Code	4V (0G)	6.3V (0J)	10V (1A)	16V (1C)	20V (1D)	25V (1E)	*Cap Code
1.0	105				М	М	M	-
2.2	225			M/U	М			_
4.7	475	U	M/U	M/U**	М			_
10	106	U	M/U**	M	S			а
22	226	М	М	M**/S				J
33	336	М	М	M**/S				n
47	476	М	M/S	S				S
68	686	M/S						W
100	107	M/S	M*4/S					А
220	227	S						J

Available Ratings

We can consider the type of compliance to AEC-Q200.

Please contact to your local AVX sales office when these series are being designed in your application.

### **RATINGS & PART NUMBER REFERENCE**

AVX Part No.	Case Size	Capacitance (µF)	Rated Voltage (V)	*2 DCL (µA)	DF (%) @ 120Hz	ESR (Ω) @ 100kHz	*1 ∆C/C (%)
4 Volt							
F980G475MUA	U	4.7	4	0.5	20	20	±30
F980G106MUA	Ū	10	4	0.8	25	20	±30
F980G226MMA	М	22	4	0.9	15	7.5	±30
F980G336MMA	М	33	4	1.3	30	4	±30
F980G476MMA	М	47	4	1.9	40	8	±30
F980G686MMA	М	68	4	27.2	50	10	±30
F980G686MSA	S	68	4	2.7	30	4	±30
F980G107MMA	М	100	4	80.0	60	10	±30
F980G107MSA	S	100	4	4.0	35	4	±30
F980G227MSA	S	220	4	132	80	5	±30
			3 Volt				
F980J475MMA	М	4.7	6.3	0.5	20	7.5	±30
F980J475MUA	U	4.7	6.3	0.6	20	20	±30
F980J106MMA	М	10	6.3	0.6	8	6	±30
F980J106MUALZT	U	10	6.3	6.3	30	30	±30
F980J226MMA	М	22	6.3	1.4	20	6	±30
F980J336MMA	М	33	6.3	4.2	35	8	±30
F980J476MMA	М	47	6.3	29.6	45	10	±30
F980J476MSA	S	47	6.3	3.0	25	6	±30
F980J107MMAAXE	М	100	6.3	126	80	10	±30
F980J107MSA	S	100	6.3	63.0	50	8	±30
500 / A 005 A 44			Volt	0.5			
F981A225MMA	М	2.2	10	0.5	6	7.5	±30
F981A225MUA	U	2.2	10	0.5	15	15	±30
F981A475MMA	М	4.7	10	0.5	6	6	±30
F981A475MUALZT	U	4.7	10	4.7	25	25	±30
F981A106MMA	М	10	10	1.0	20	7.5	±30
F981A226MMALZT	M	22	10	11.0	30	8	±30
F981A226MSA	S	22	10	2.2	20	4	±30
F981A336MMALZT	M	33	10	33.0	45	8	±30
F981A336MSA	S	33	10	3.3	30	6	±30
F981A476MSA	S	47	10	9.4	35	5	±30
16 Volt   F981C105MMA   M   1   16   0.5   6   10   ±30							
F981C105MMA	M	2.2					
F981C225MMA	M	4.7	16 16	0.5	6 12	10 12	±30 ±30
F981C106MSA	S	10	16	1.6	18	4	±30
1 90 TO TOUIVISA	J		) Volt	1.0	10	4	±30
F981D105MMA	М	1	20	0.5	6	10	±30
AMINIODI DI DOLLA	IVI		5 Volt	0.0	U	10	TOU
F981E105MMA	М	1	25	0.5	8	10	±30

<sup>\*2:</sup> Leakage Current
After 5 minute's application of rated voltage,
leakage current at 20°C.

<sup>\*</sup>Codes under development – subject to change

<sup>\*4</sup> Rated temperature 60°C and H dimension 1.0mm Max only. Please contact AVX when you need detail spec.

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### **QUALIFICATION TABLE**

TEST	F98 series (Temperature range -55°C to +125°C) Condition						
1551							
	At 40°C, 90 to 95% R.H., 500 hours (No voltage applied)						
Damp Heat	Capacitance Change Refer to page 68 (*1)						
(Steady State)	Dissipation Factor 150% or less of initial specified value						
	Leakage Current						
	-55°C / +125°C, 30 minutes each, 5 cycles						
Temperature Cycles	Capacitance Change Refer to page 68 (*1)						
iomporataro <b>c</b> yclos	Dissipation Factor 150% or less of initial specified value						
	Leakage Current						
	10 seconds reflow at 260°C, 5 seconds immersion at 260°C.						
Resistance to	Capacitance Change Refer to page 68 (*1)						
Soldering Heat	Dissipation Factor Initial specified value or less						
	Leakage Current Initial specified value or less						
	After application of surge in series with a 1kΩ resistor at the rate of 30 seconds ON, 30 seconds OFF,						
	for 1000 successive test cycles at 85°C, capacitors shall meet the characteristic requirements in the table above.						
Surge	Capacitance Change Refer to page 68 (*1)						
	Dissipation Factor 150% or less of initial specified value						
	Leakage Current						
	After 1000 hours' application of rated voltage in series with a $3\Omega$ resistor at 85°C,						
	capacitors shall meet the characteristic requirements in the table above.						
Endurance	Capacitance Change Refer to page 68 (*1)						
	Dissipation Factor 150% or less of initial specified value						
	Leakage Current						
	After applying the pressure load of 5N for 10±1 seconds horizontally to the center of capacitor side body 🙇 🚊 🗝						
Shear Test	which has no electrode and has been soldered beforehand on a substrate, there shall be found neither 5N (0.51kg·1)						
	exfoliation nor its sign at the terminal electrode.						
	Keeping a capacitor surface-mounted on a substrate upside down and supporting the substrate at						
Terminal Strength	both of the opposite bottom points 45mm apart from the center of capacitor, the pressure strength is						
	applied with a specified jig at the center of substrate so that the substrate may bend by 1mm as						
	illustrated. Then, there shall be found no remarkable abnormality on the capacitor terminals.						