

HLMP-HG74/75, HLMP-HM74/75, HLMP-HB74/75

Red, Green and Blue

5 mm Standard Oval LEDs



Data Sheet



Lead (Pb) Free
RoHS 6 fully
compliant



Description

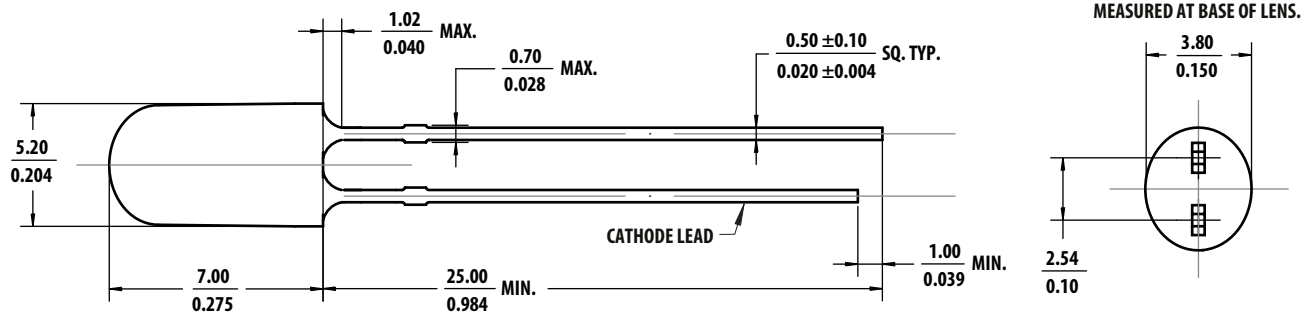
These Precision Optical Performance Oval LEDs are specifically designed for full color/video and passenger information signs. The oval shaped radiation pattern and high luminous intensity ensure that these devices are excellent for wide field of view outdoor applications where a wide viewing angle and readability in sunlight are essential. The package epoxy contains UV inhibitor to reduce the effects of long term exposure to direct sunlight.

Applications

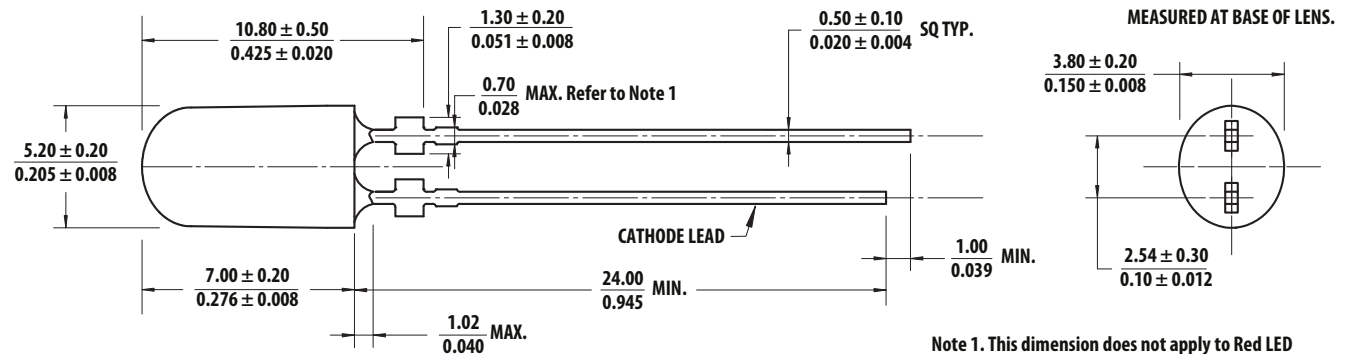
- Full Color Signs

Package Dimensions

Package Drawing A



Package Drawing B



Caution: InGaN devices are Class 1C HBM ESD sensitive per JEDEC Standard. Please observe appropriate precautions during handling and processing. Refer to Application Note AN-1142 for additional details.

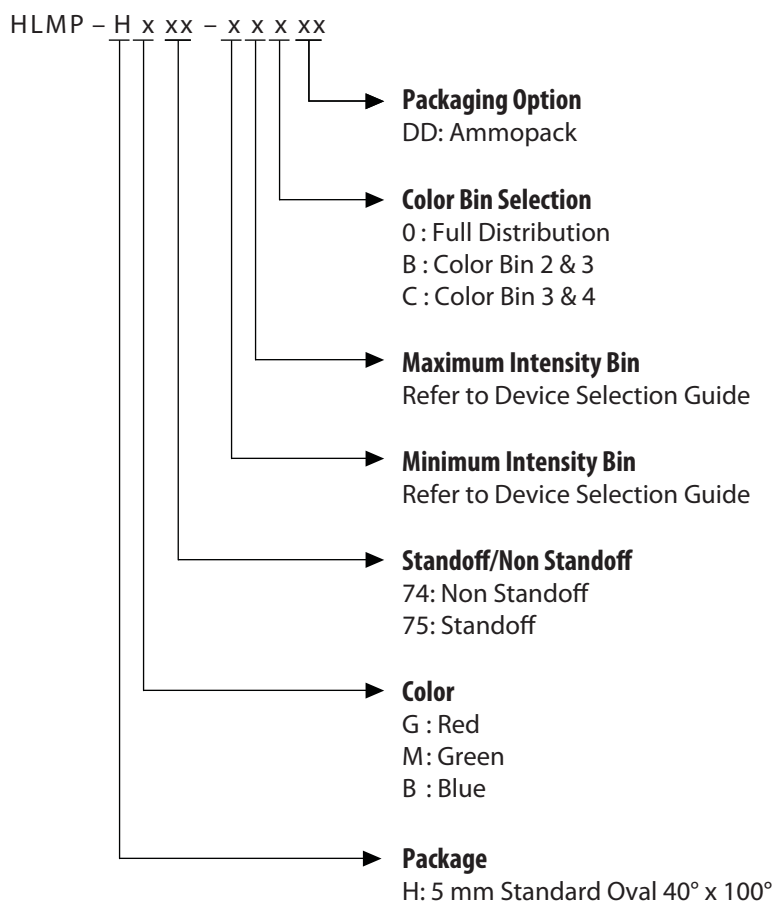
Device Selection Guide

Part Number	Color and Dominant Wavelength λ_d (nm) Typ [3]	Luminous Intensity I _v (mcd) at 20 mA [1,2,5]		Standoff	Typical Viewing Angle (°) [4]	Package Drawing
		Min	Max			
HLMP-HG74-XY0DD	Red 626	1660	2400	No	40 x 100	A
HLMP-HG75-XY0DD	Red 626	1660	2400	Yes		B
HLMP-HM74-34BDD	Green 530	4200	6050	No	40 x 100	A
HLMP-HM75-34BDD	Green 530	4200	6050	Yes		B
HLMP-HM74-34CDD	Green 530	4200	6050	No	40 x 100	A
HLMP-HM75-34CDD	Green 530	4200	6050	Yes		B
HLMP-HB74-UVBDD	Blue 470	960	1380	No	40 x 100	A
HLMP-HB75-UVBDD	Blue 470	960	1380	Yes		B
HLMP-HB74-UVCDD	Blue 470	960	1380	No	40 x 100	A
HLMP-HB75-UVCDD	Blue 470	960	1380	Yes		B

Notes:

1. The luminous intensity is measured on the mechanical axis of the lamp package and it is tested with pulsing condition.
2. The optical axis is closely aligned with the package mechanical axis.
3. Dominant wavelength, λ_d , is derived from the CIE Chromaticity Diagram and represents the color of the lamp.
4. $\theta_{1/2}$ is the off-axis angle where the luminous intensity is half the on-axis intensity.
5. Tolerance for each bin limit is $\pm 15\%$.

Part Numbering System



Absolute Maximum Ratings

$T_J = 25^\circ\text{C}$

Parameter	Red	Green/ Blue	Unit
DC Forward Current ^[1]	50	30	mA
Peak Forward Current	100 ^[2]	100 ^[3]	mA
Power Dissipation	120	114	mW
LED Junction Temperature	130	110	°C
Operating Temperature Range	-40 to +100	-40 to +85	°C
Storage Temperature Range		-40 to +100	°C

Notes:

1. Derate linearly as shown in Figures 4 and 8.
2. Duty Factor 30%, frequency 1 KHz.
2. Duty Factor 10%, frequency 1 KHz.

Electrical / Optical Characteristics

$T_J = 25^\circ\text{C}$

Parameter	Symbol	Min.	Typ.	Max.	Units	Test Conditions
Forward Voltage	V_F				V	$I_F = 20\text{ mA}$
Red		1.8	2.1	2.4		
Green		2.8	3.2	3.8		
Blue		2.8	3.2	3.8		
Reverse Voltage ^[3]	V_R				V	
Red		5				$I_R = 100\ \mu\text{A}$
Green and Blue		5				$I_R = 10\ \mu\text{A}$
Dominant Wavelength ^[1]	λ_d				nm	$I_F = 20\text{ mA}$
Red		618	626	630		
Green		523	530	535		
Blue		464	470	476		
Peak Wavelength	λ_{PEAK}				nm	Peak of Wavelength of Spectral Distribution at $I_F = 20\text{ mA}$
Red			634			
Green			521			
Blue			464			
Thermal Resistance	$R\theta_{J-PIN}$		240		°C/W	LED Junction-to-Pin
Luminous Efficacy ^[2]	η_V				lm/W	Emitted Luminous Power/ Emitted Radiant Power
Red			218			
Green			538			
Blue			65			

Notes:

1. The dominant wavelength is derived from the chromaticity diagram and represents the color of the lamp.
2. The radiant intensity, I_e in watts per steradian, may be found from the equation $I_e = I_V/\eta_V$ where I_V is the luminous intensity in candelas and η_V is the luminous efficacy in lumens/watt.
3. Indicates product final testing condition. Long term reverse bias is not recommended.

AllnGaP Red

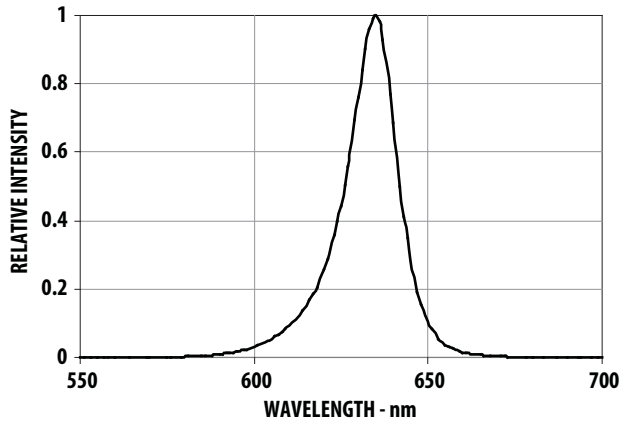


Figure 1. Relative Intensity vs Wavelength

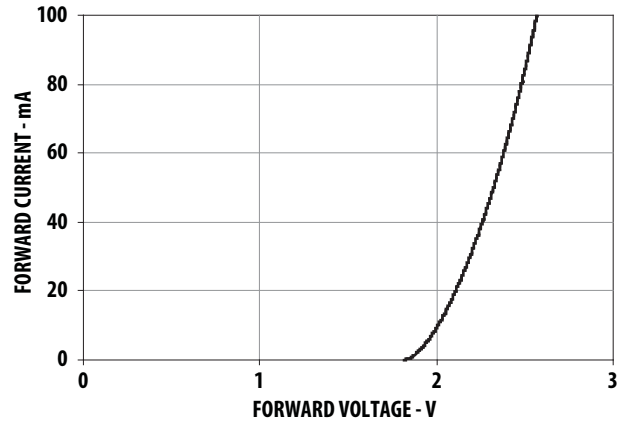


Figure 2. Forward Current vs Forward Voltage

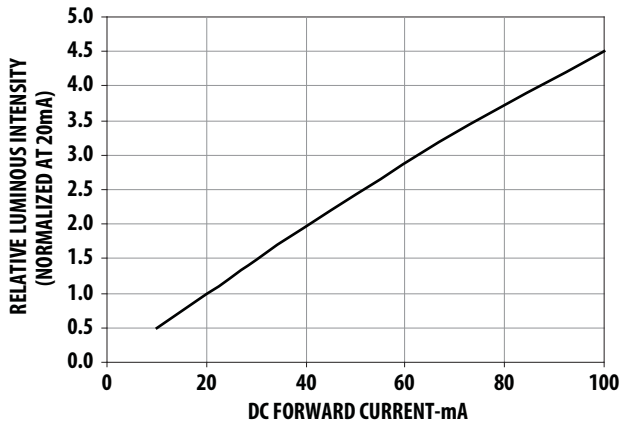


Figure 3. Relative Intensity vs Forward Current

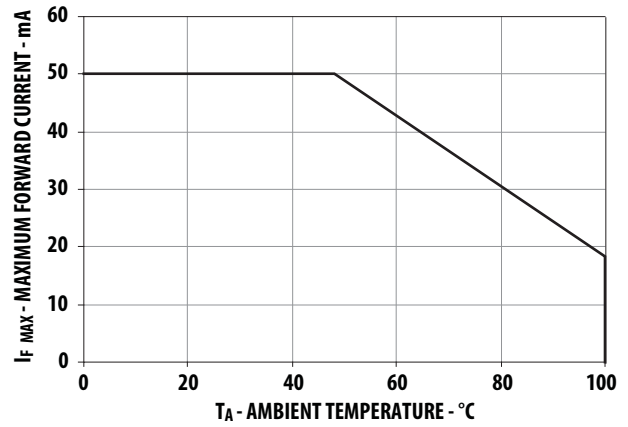


Figure 4. Maximum Forward Current vs Ambient Temperature

InGaN Green and Blue

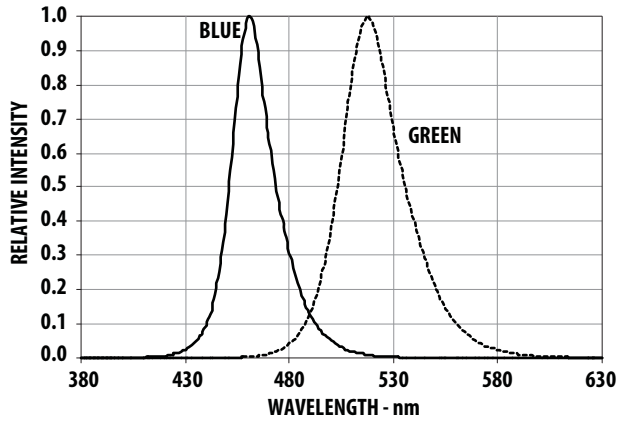


Figure 5. Relative Intensity vs Wavelength

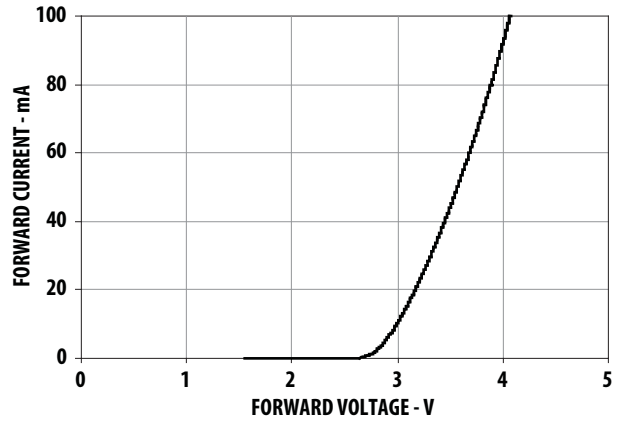


Figure 6. Forward Current vs Forward Voltage

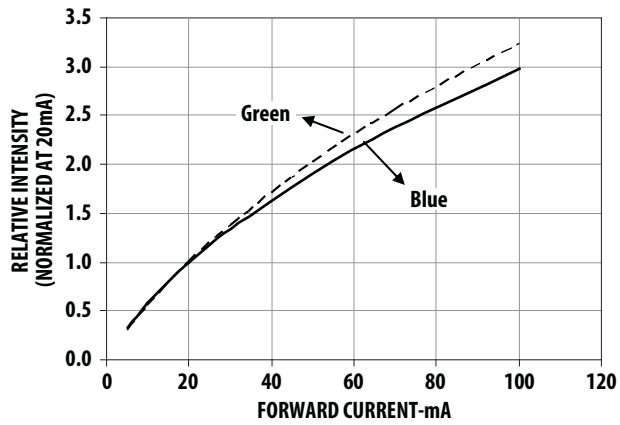


Figure 7. Relative Intensity vs Forward Current

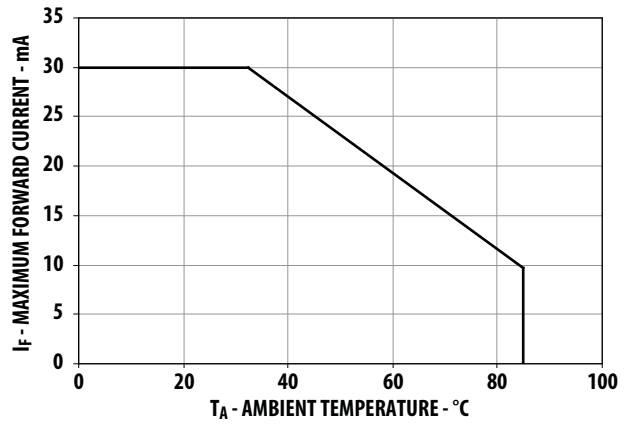


Figure 8. Maximum Forward Current vs Ambient Temperature

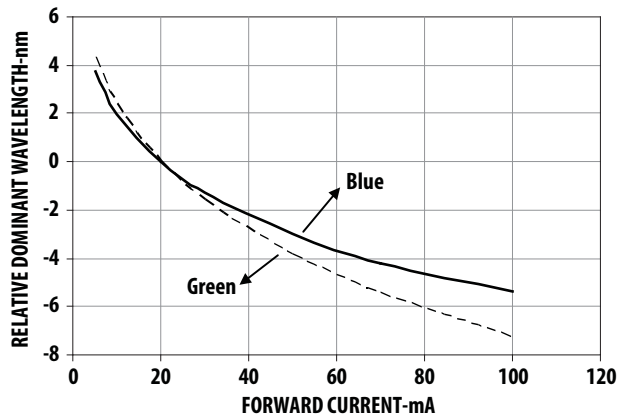


Figure 9. Relative Dominant Wavelength vs Forward Current

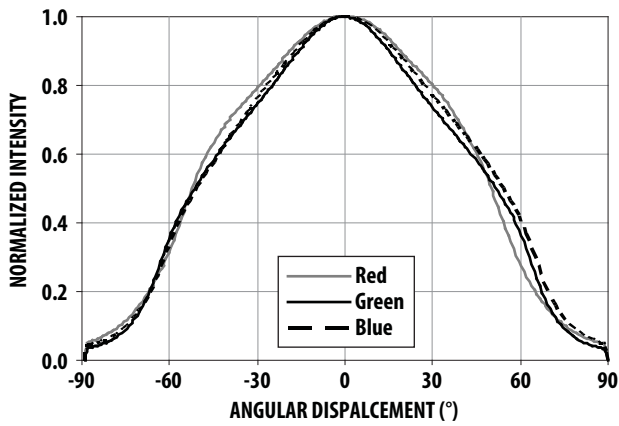


Figure 10. Radiation pattern-Major Axis

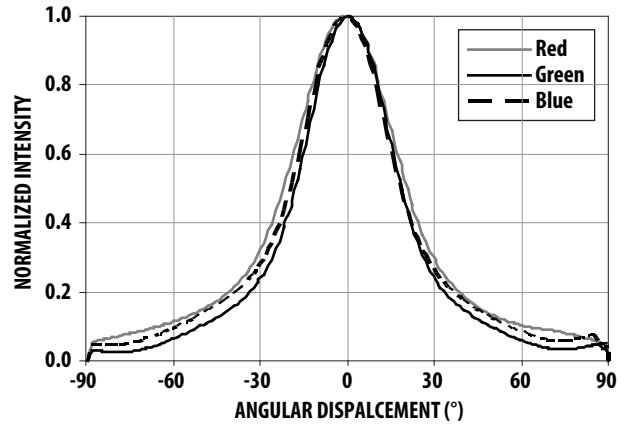


Figure 11. Radiation pattern-Minor Axis

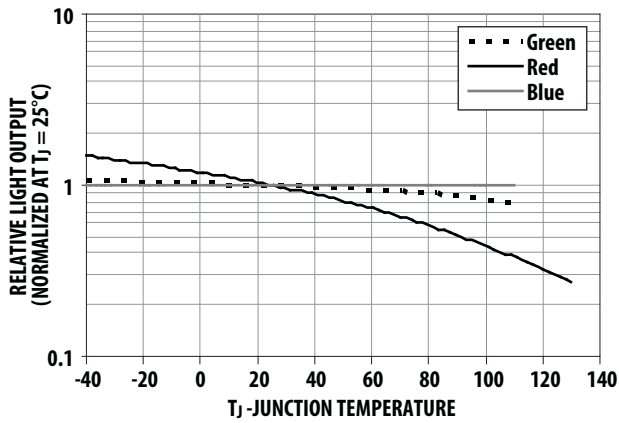


Figure 12. Relative Light Output vs Junction Temperature

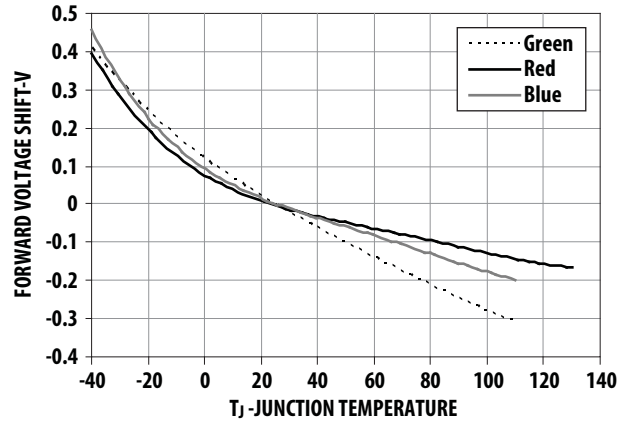


Figure 13. Forward Voltage Shift vs Junction Temperature

Intensity Bin Limit Table (1.2: 1 Iv Bin Ratio)

Bin	Intensity (mcd) at 20 mA	
	Min	Max
U	960	1150
V	1150	1380
W	1380	1660
X	1660	1990
Y	1990	2400
Z	2400	2900
1	2900	3500
2	3500	4200
3	4200	5040
4	5040	6050

Tolerance for each bin limit is ±15%

V_F Bin Table (V at 20 mA)

Bin ID	Min	Max
VD	1.8	2.0
VA	2.0	2.2
VB	2.2	2.4

Notes:

1. Tolerance for each bin limit is ±0.05 V
2. V_F binning only applicable to Red color.

Red Color Range

Min Dom	Max Dom					
618.0	630.0	x	0.6872	0.3126	0.6890	0.2943
		y	0.6690	0.3149	0.7080	0.2920

Tolerance for each bin limit is ± 0.5 nm

Green Color Bin Table

Bin	Min Dom	Max Dom					
2	523	527	x	0.0979	0.1450	0.1711	0.1305
			y	0.8316	0.7319	0.7218	0.8189
3	527	531	x	0.1305	0.1711	0.1967	0.1625
			y	0.8189	0.7218	0.7077	0.8012
4	531	535	x	0.1625	0.1967	0.2210	0.1929
			y	0.8012	0.7077	0.6920	0.7816

Tolerance for each bin limit is ± 0.5 nm

Blue Color Bin Table

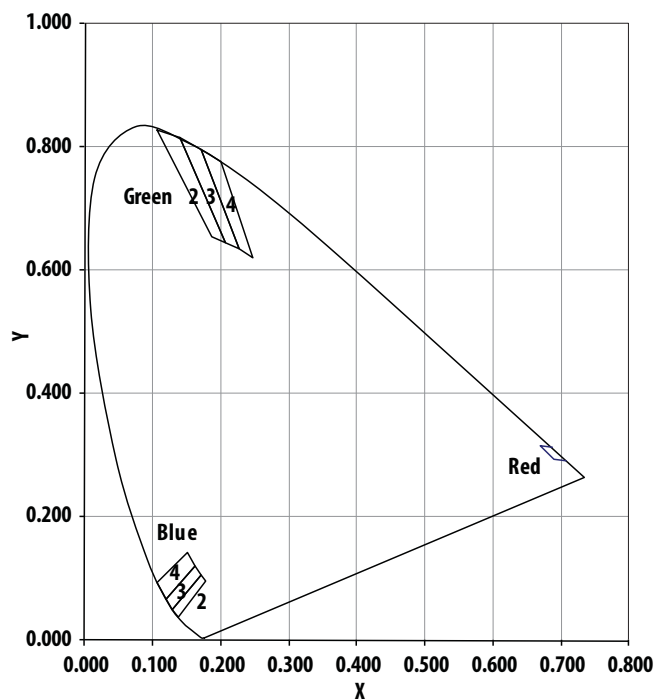
Bin	Min Dom	Max Dom					
2	464	468	x	0.1374	0.1766	0.1699	0.1291
			y	0.0374	0.0966	0.1062	0.0495
3	468	472	x	0.1291	0.1699	0.1616	0.1187
			y	0.0495	0.1062	0.1209	0.0671
4	472	476	x	0.1187	0.1616	0.1517	0.1063
			y	0.0671	0.1209	0.1423	0.0945

Tolerance for each bin limit is ± 0.5 nm

Note:

1. All bin categories are established for classification of products. Products may not be available in all bin categories. Please contact your Avago representative for further information.

Avago Color Bin on CIE 1931 Chromaticity Diagram



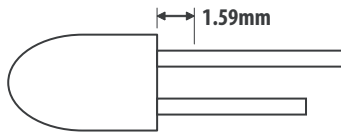
Precautions:

Lead Forming:

- The leads of an LED lamp may be preformed or cut to length prior to insertion and soldering on PC board.
- For better control, it is recommended to use proper tool to precisely form and cut the leads to applicable length rather than doing it manually.
- If manual lead cutting is necessary, cut the leads after the soldering process. The solder connection forms a mechanical ground which prevents mechanical stress due to lead cutting from traveling into LED package. This is highly recommended for hand solder operation, as the excess lead length also acts as small heat sink.

Soldering and Handling:

- Care must be taken during PCB assembly and soldering process to prevent damage to the LED component.
- LED component may be effectively hand soldered to PCB. However, it is only recommended under unavoidable circumstances such as rework. The closest manual soldering distance of the soldering heat source (soldering iron's tip) to the body is 1.59 mm. Soldering the LED using soldering iron tip closer than 1.59 mm might damage the LED.



- ESD precaution must be properly applied on the soldering station and personnel to prevent ESD damage to the LED component that is ESD sensitive. Do refer to Avago application note AN 1142 for details. The soldering iron used should have grounded tip to ensure electrostatic charge is properly grounded.
- Recommended soldering condition:

	Wave Soldering [1, 2]	Manual Solder Dipping
Pre-heat temperature	105° C Max.	–
Preheat time	60 sec Max	–
Peak temperature	260° C Max.	260° C Max.
Dwell time	5 sec Max.	5 sec Max

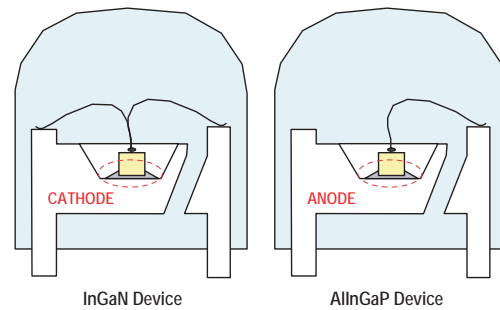
Note:

1. Above conditions refers to measurement with thermocouple mounted at the bottom of PCB.
 2. It is recommended to use only bottom preheaters in order to reduce thermal stress experienced by LED.
- Wave soldering parameters must be set and maintained according to the recommended temperature and dwell time. Customer is advised to perform daily check on the soldering profile to ensure that it is always conforming to recommended soldering conditions.

Note:

1. PCB with different size and design (component density) will have different heat mass (heat capacity). This might cause a change in temperature experienced by the board if same wave soldering setting is used. So, it is recommended to re-calibrate the soldering profile again before loading a new type of PCB.
2. Avago Technologies' AllnGaP high brightness LED are using high efficiency LED die with single wire bond as shown below. Customer is advised to take extra precaution during wave soldering to ensure that the maximum wave temperature does not exceed 260° C and the solder contact time does not exceeding 5 sec. Over-stressing the LED during soldering process might cause premature failure to the LED due to delamination.

Avago Technologies LED configuration



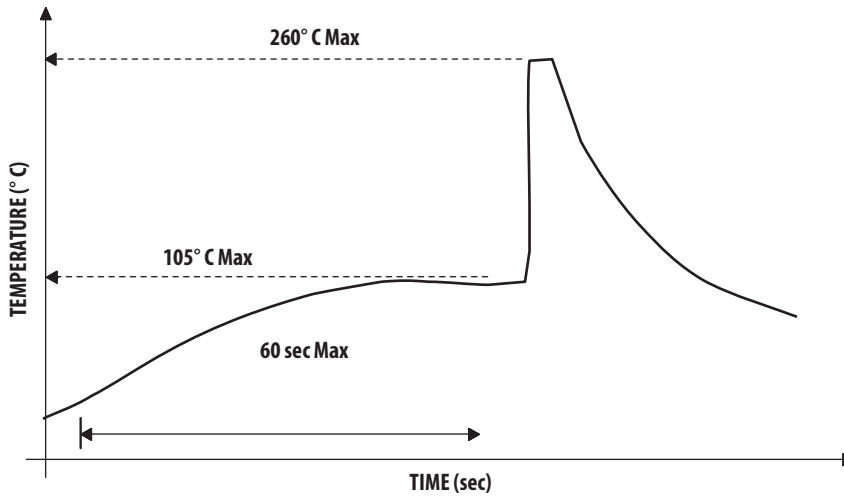
- Any alignment fixture that is being applied during wave soldering should be loosely fitted and should not apply weight or force on LED. Non metal material is recommended as it will absorb less heat during wave soldering process.
- At elevated temperature, LED is more susceptible to mechanical stress. Therefore, PCB must allowed to cool down to room temperature prior to handling, which includes removal of alignment fixture or pallet.
- If PCB board contains both through hole (TH) LED and other surface mount components, it is recommended that surface mount components be soldered on the top side of the PCB. If surface mount need to be on the bottom side, these components should be soldered using reflow soldering prior to insertion the TH LED.
- Recommended PC board plated through holes (PTH) size for LED component leads.

LED component lead size	Diagonal	Plated through hole diameter
0.45 x 0.45 mm (0.018x 0.018 inch)	0.636 mm (0.025 inch)	0.98 to 1.08 mm (0.039 to 0.043 inch)
0.50 x 0.50 mm (0.020x 0.020 inch)	0.707 mm (0.028 inch)	1.05 to 1.15 mm (0.041 to 0.045 inch)

- Over-sizing the PTH can lead to twisted LED after clinching. On the other hand under sizing the PTH can cause difficulty inserting the TH LED.

Refer to application note AN5334 for more information about soldering and handling of high brightness TH LED lamps.

Example of Wave Soldering Temperature Profile for TH LED



Recommended solder:
 Sn63 (Leaded solder alloy)
 SAC305 (Lead free solder alloy)

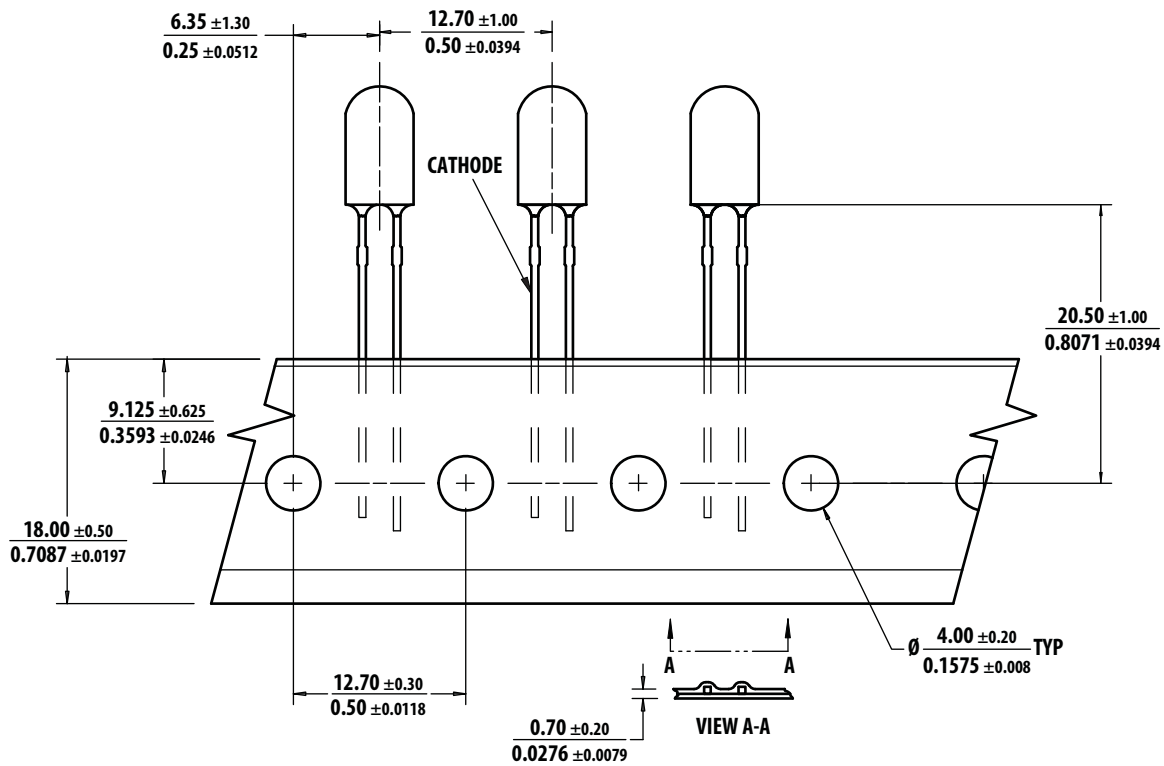
Flux: Rosin flux

Solder bath temperature: 255°C ± 5°C
 (maximum peak temperature = 260°C)

Dwell time: 3.0 sec - 5.0 sec
 (maximum = 5 sec)

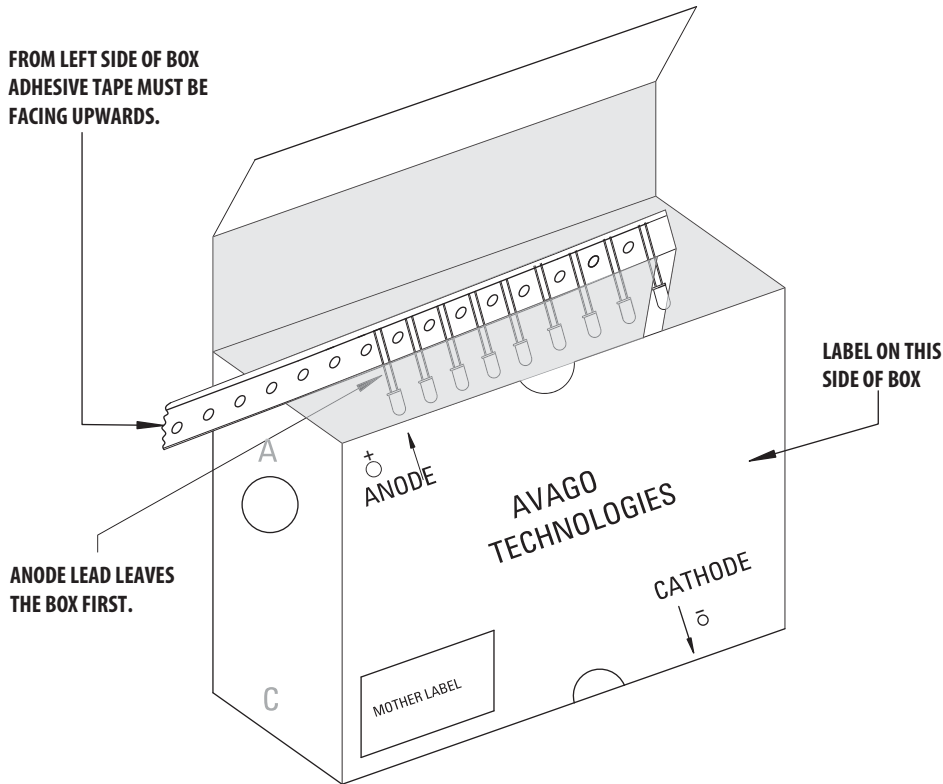
Note: Allow for board to be sufficiently cooled to room temperature before exerting mechanical force.

Ammo Packs Drawing



Note: All dimensions in millimeters (inches)

Packaging Box for Ammo Packs













Note: For InGaN device, the ammo pack packaging box contain ESD logo

Packaging Label

(i) Avago Mother Label: (Available on packaging box of ammo pack and shipping box)

AVAGO TECHNOLOGIES	
STANDARD LABEL LS0002	
RoHS Compliant	
e3 max temp 260C	
(1P) Item: <u>Part Number</u>	(Q) QTY: <u>Quantity</u>
(1T) Lot: <u>Lot Number</u>	CAT: <u>Intensity Bin</u>
LPN:	BIN: <u>Refer to below information</u>
(9D)MFG Date: <u>Manufacturing Date</u>	
<hr/>	
(P) Customer Item:	
(V) Vendor ID:	(9D) Date Code: <u>Date Code</u>
<hr/>	
DeptID:	Made In: <u>Country of Origin</u>

(ii) Avago Baby Label (Only available on bulk packaging)

 Avago TECHNOLOGIES		RoHS Compliant e3 max temp 260C
Lamps Baby Label		
(1P) PART #: Part Number 		
(1T) LOT #: Lot Number 		
(9D)MFG DATE: Manufacturing Date 	QUANTITY: Packing Quantity 	
C/O: Country of Origin		
Customer P/N: 	CAT: Intensity Bin 	
Supplier Code: 	BIN: Refer to below information 	
	DATECODE: Date Code 	

Acronyms and Definition:

BIN:

- (i) Color bin only or VF bin only
 (Applicable for part number with color bins but without VF bin OR part number with VF bins and no color bin)
 OR
- (ii) Color bin incorporated with VF Bin
 (Applicable for part number that have both color bin and VF bin)

Example:

- (i) Color bin only or VF bin only
 BIN: 2 (represent color bin 2 only)
 BIN: VB (represent VF bin "VB" only)
- (ii) Color bin incorporate with VF Bin
 BIN: 2VB

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