



SINGLE 2 INPUT POSITIVE NOR GATE

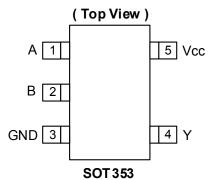
Description

The 74LVC1G02Q is an automotive compliant single 2-input positive NOR gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output preventing damaging current backflow when the device is powered down.

The gate performs the positive Boolean function:

$$Y = \overline{A + B}$$
 or $Y = \overline{A} \cdot \overline{B}$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to 125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- I_{OFF} Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per AEC-Q100
 - Exceeds 2000V Human Body Model (AEC Q100-002)
 - Exceeds 1000V Charged Device Model (AEC Q100-011)
- Latch-Up Exceeds 100mA (AEC Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G02Q is suitable for automotive applications requiring specific change control and is AEC-Q100 qualified, has a grade 1 -40°C to 125°C temperature rating, is PPAP capable, and is manufactured in IATF16949:2016 certified facilities.

Applications

- Voltage Level Shifting
- General Purpose Logic
- Power-Down Signal Isolation
- Wide Array of Products such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High-Reliability Networking/Communications
 - Industrial/Agricultural Equipment

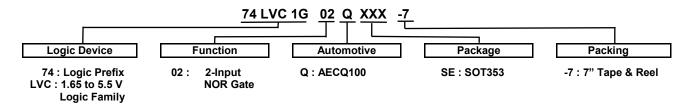
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



1G : One Gate

Ordering Information (Note 4)



Part Number	Part Number Package Package		Package	7" Tape and Reel		
Fait Nullibei	Code	(Notes 5 & 6)	Size	Quantity	Part Number Suffix	
74LVC1G02QSE-7	SE	SOT353	2.0mm × 2.0mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7	

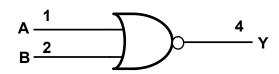
Notes:

- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.
 5. Pad layout as shown in Diodes Inc. suggested pad layouts, which can be found on our website at see http://www.diodes.com/package-outlines.html.
 6. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

Pin Descriptions

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Υ
Н	Х	L
Х	Н	L
L	L	Н



Absolute Maximum Ratings (Notes 7 & 8)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or I _{OFF} State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I <0	-50	mA
lok	Output Clamp Current	-50	mA
Io	Continuous Output Current	±50	mA
I _{CC,} I _{GND}	Continuous current through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

- 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range..

Recommended Operating Conditions (Note 9)

Symbol		Parameter	Min	Max	Unit	
V	Operating Voltage	Operating	1.65	5.5	V	
V _{CC}	Operating Voltage	Data Retention Only	1.5	_	V	
		V _{CC} = 1.65V to 1.95V	0.65 × V _{CC}	_		
	Lligh Lavel Innut Valtage	V _{CC} = 2.3V to 2.7V	1.7	_	.,	
V _{IH}	High-Level Input Voltage	V _{CC} = 3V to 3.6V	2	_	V	
		V _{CC} = 4.5V to 5.5V	0.7 × V _{CC}	_	1	
		V _{CC} = 1.65V to 1.95V	_	0.35 × V _{CC}		
.,		V _{CC} = 2.3V to 2.7V	_	0.7	1 ,,	
V_{IL}	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V	
		V _{CC} = 4.5V to 5.5V	_	0.3 × V _{CC}	1	
VI		Input Voltage	0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		V _{CC} = 1.65V	_	-4		
		V _{CC} = 2.3V	_	-8	1	
	High Lavel Output Compant	V _{CC} = 2.7V	_	-12		
Іон	High-Level Output Current		_	-16	- mA	
		V _{CC} = 3V	_	-24		
		V _{CC} = 4.5V	_	-32		
		V _{CC} = 1.65V	_	4		
		V _{CC} = 2.3V	_	8	1	
,	Law Lavel Output Current	V _{CC} = 2.7V	_	12	mA	
I _{OL}	Low-Level Output Current	V - 2V	_	16	IIIA	
		V _{CC} = 3V	_	24		
		V _{CC} = 4.5V	_	32		
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$		20		
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	_	10	ns/V	
	T G G	$V_{CC} = 5V \pm 0.5V$	_	5	1	
T _A	Operating Free-Air Temperature	_	-40	+125	°C	

Note: 9. Unused inputs should be held at V_{CC} or Ground.

July 2019



Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Symbol	Parameter	Test Conditions		V _{CC}	-40°	C to +125	°C	Unit
Symbol	Faranietei	Test Co			Min	Тур	Max	Unit
			$I_{OH} = -100 \mu A$	1.65V to 5.5V	V _{CC} - 0.1	_	_	
			$I_{OH} = -4mA$	1.65V	0.95	_	_	
V	High Loyal Output Valtage	\/ = \/ 05\/	$I_{OH} = -8mA$	2.3V	1.7	_	_	V
V _{OH}	High Level Output Voltage	$V_I = V_{IH} \text{ or } V_{IL}$	I _{OH} = -12mA	2.7V	1.9	_	_	V
			$I_{OH} = -24mA$	3V	2.0	_	_	
			I _{OH} = -32mA	4.5V	3.4	_	_	
			$I_{OL} = 100 \mu A$	1.65V to 5.5V	_	_	0.10	V
		V _I = V _{IH} or V _{IL}	$I_{OL} = 4mA$	1.65V	_	_	0.70	
.,			I_{OL} = 8mA	2.3V	_	_	0.45	
V_{OL}	Low Level Output Voltage		I _{OL} = 12mA	2.7V	_	_	0.60	
			I _{OL} = 24mA	3V	_	_	0.80	
			I _{OL} = 32mA	4.5V	_	_	0.80	
II	Input Current	V _I = 5.5V or GN	1D	0 to 5.5V	_	±0.1	±1	μA
I _{OFF}	Power Down Leakage Current	$V_1 \text{ or } V_0 = 5.5V$		0V	_	_	±2	μA
Icc	Supply Current	V _I = 5.5V or GND I _O =0		5.5V	_	0.1	4	μΑ
Δl _{CC}	Additional Supply Current	One input at V _{CC} –0.6V Other inputs at V _{CC} or GND		3V to 5.5V	_	_	500	μA
Cı	Input Capacitance	V_i = GND to V_C	C	3.3V	_	5.0	_	pF

Package Characteristics

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
$\theta_{\sf JA}$	Thermal Resistance Junction-to-Ambient	SOT353	Note 10		371	-	°C/W
θЈС	Thermal Resistance Junction-to-Case	SOT353	Note 10	_	143		°C/W

Note: 10. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

Figure 1 Typical Values at T_A = +25°C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	Vcc	T _A = -4	10°C to 12	5°C	Unit			
Faranietei	Input	Output	V CC	Min	Тур	Max	Oille			
	2.5V ± 0.2V A or B Y 2.7V 3.3V ± 0.3V		1.8V ± 0.15V	1.0	3.2	10.5				
		A or B	A or B		A or B Y	2.5V ± 0.2V	0.5	2.2	7.0	
t_{PD}				A or B		A or B Y	2.7V	0.5	2.5	7.0
		$3.3V \pm 0.3V$	0.5	2.1	6.0					
		5.0V ± 0.5V	0.5	1.7	5.5					

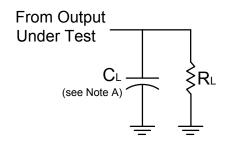
Operating Characteristics

 $T_A = +25^{\circ}C$

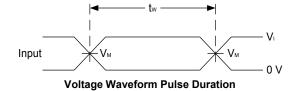
	Parameter	Test Conditions	V _{CC} = 1.8V Typ	V _{CC} = 2.5V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{pd}	Power Dissipation Capacitance	f = 10 MHz	14	14	14	14	pF

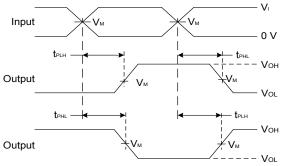


Measurement Information



Vcc	Inputs		V _M	CL	RL
VCC	Vı	t _r /t _f	V MI)L	INL
1.8V ± 0.15V	V _{CC}	≤2ns	V _{CC} /2	30pF	1ΚΩ
2.5V ± 0.2V	V _{CC}	≤2ns	V _{CC} /2	30pF	500Ω
2.7V	V _{CC}	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	V _{CC}	≤2.5ns	V _{CC} /2	50pF	500Ω





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1 Load Circuit and Voltage Waveforms

Notes:

A. Includes test lead and test apparatus capacitance.
B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
C. Inputs are measured separately one transition per measurement.

D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Marking Information

SOT353

(Top View)



 $\begin{array}{ccc} \underline{XXX} & : & \text{Identification Code} \\ \underline{\underline{Y}} & : & \text{Year 0~9} \\ \underline{\underline{W}} & : & \text{Week: A~Z 1~26 week} \end{array}$

a~z 27~52 week z represents week 52 and 53

: A~ Z: Internal Code

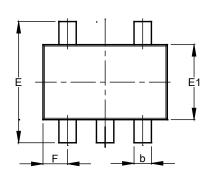
SOT353

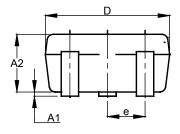
Part Number	Package	Identification Code
74LVC1G02QSE-7	SOT353	UTQ

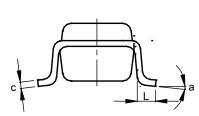


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.







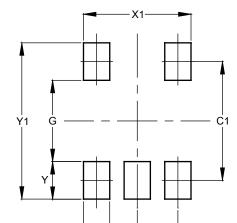
SOT353

SOT353

SOT353				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2 500



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