



DSM80101M

NPN TRANSISTOR WITH DUAL SERIES SWITCHING DIODE

Features

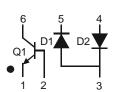
- Integrates one NPN Transistor (Q1) and two Switching Diodes (D1, D2) in a Single Compact Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

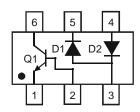
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.01 grams (Approximate)



Top View



Device Schematic



Top View Pin Configuration

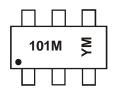
Ordering Information (Note 4)

| Part Number | Case | Packaging |
|-------------|-------|-------------------|
| DSM80101M-7 | SOT26 | 3,000/Tape & Reel |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



101M = Product Type Marking Code (See Electrical Characteristics Table) YM = Date Code Marking Y = Year (ex: B = 2014) M = Month (ex: 9 = September)

Date Code Key

| Year | 2014 | | 2015 | 2016 | | 2017 | 2018 | | 2019 | 2020 | | 2021 |
|-------|------|-----|------|------|-----|------|------|-----|------|------|-----|------|
| Code | В | | С | D | | E | F | | G | Н | | I |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



Maximum Ratings – Q1 (@ $T_A = +25$ °C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--|---------------------|-------|------|
| Collector-Base Voltage | V _{CBO} | 80 | V |
| Collector-Emitter Voltage | V _{CEO} | 80 | V |
| Emitter-Base Voltage | V _{EBO} | 6.0 | V |
| Continuous Collector Current | I _{C(MAX)} | 500 | mA |
| Peak Pulse Collector Current @ DC Increment for I _C ; I _B = 300mA; Test Duration >10s for each Step | I _{CM} | 0.8 | А |
| Base Current | I _B | 200 | mA |

Maximum Ratings – D1, D2 (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--|--|-------|------|
| Non-Repetitive Peak Reverse Voltage | V _{RM} | 100 | V |
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 75 | V |
| RMS Reverse Voltage | V _{R(RMS)} | 53 | V |
| Forward Continuous Current (Note 5) | I _{FM} | 300 | mA |
| Average Rectified Output Current (Note 5) | I _O | 200 | mA |
| Non-Repetitive Peak Forward Surge Current @ t = 1.0μs | I _{FSM} | 20 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|--|-----------------------------------|-------------|------|
| Power Dissipation (Note 5) | P _D | 600 | mW |
| Thermal Resistance, Junction to Ambient Air (Note 5) | $R_{	hetaJA}$ | 208 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -65 to +150 | °C |

Electrical Characteristics – Q1 (@T_A = +25°C, unless otherwise specified.)

| Characteristic (Note 6) | Symbol | Min | Typical | Max | Unit | Test Condition |
|--------------------------------------|-------------------|-----|---------|-----|------|-----------------------------|
| Collector-Base Breakdown Voltage | BV _{CBO} | 80 | _ | - | ٧ | $Ic = 100\mu A, IE = 0$ |
| Collector-Emitter Breakdown Voltage | BV _{CEO} | 80 | _ | - | V | Ic = 1.0mA, IB = 0 |
| Emitter-Base Breakdown Voltage | BV _{EBO} | 6.0 | _ | - | V | $IE = 100 \mu A$, $IC = 0$ |
| Collector Cutoff Current | I _{CBO} | _ | _ | 100 | nA | VCB = 80V, IE = 0 |
| Collector-Emitter Saturation Voltage | $V_{CE(SAT)}$ | _ | _ | 0.3 | V | Ic = 100mA, IB = 10mA |
| DC Current Transfer Ratio | h _{FE} | 120 | 180 | 350 | | Ic = 10mA, VcE = 1.0V |

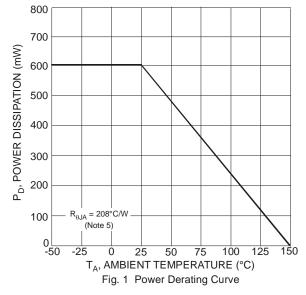
Electrical Characteristics - D1, D2 (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Max | Unit | Test Condition | | |
|------------------------------------|-----------------|-----|-------|-------|--|----|----|
| Reverse Breakdown Voltage (Note 6) | $V_{(BR)R}$ | 75 | | V | $I_R = 100\mu A$ | | |
| | | _ | 0.715 | V | $I_F = 5.0 \text{mA}$ | | |
| Forward Voltage | \/_ | _ | 0.855 | | I _F = 10mA | | |
| Toward Vollage | V_{F} | _ | 1.0 | | $I_F = 50mA$ | | |
| | | _ | 1.25 | | I _F = 150mA | | |
| Leakage Current (Note 6) | I _R | _ | 0.1 | μA | V _R = 75V | | |
| Leakage Current (Note 0) | | IR | ıĸ | IR IR | _ | 25 | nA |
| Total Capacitance | C _T | | 2.0 | pF | $V_R = 0V$, $f = 1.0MHz$ | | |
| Reverse Recovery Time | t _{rr} | _ | 4 | ns | $I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$ | | |

Notes: 5. Device mounted on FR-4 PC board with recommended pad layout, which can be found on our website at http://www.diodes.com.

6. Short duration pulse test used to minimize self-heating effect.





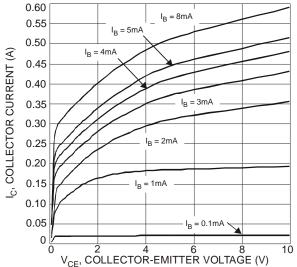
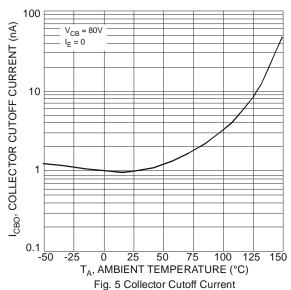


Fig. 3 Typical Collector Current vs. Collector-Emitter Voltage



vs. Ambient Temperature

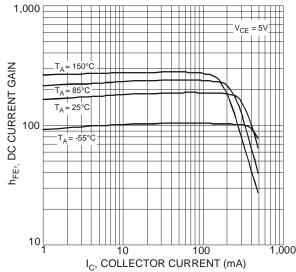


Fig. 2 Typical DC Current Gain vs. Collector Current

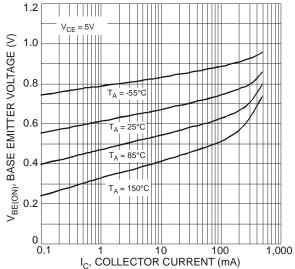


Fig. 4 Typical Base Emitter Voltage vs. Collector Current

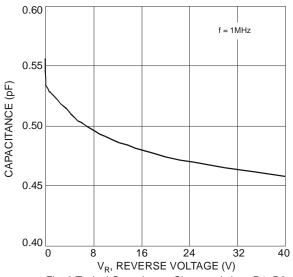


Fig. 6 Typical Capacitance Characteristics - D1, D2



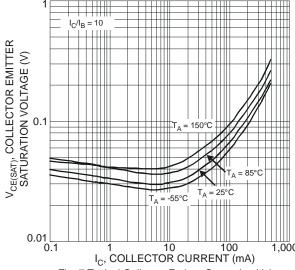
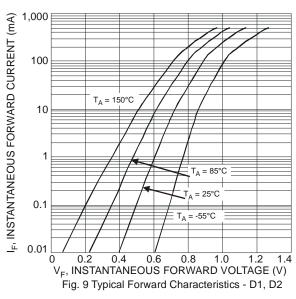


Fig. 7 Typical Collector Emitter Saturation Voltage vs. Ambient Temperature



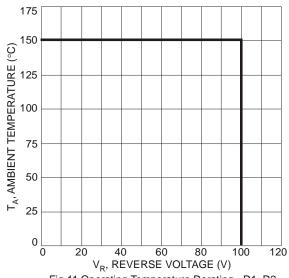
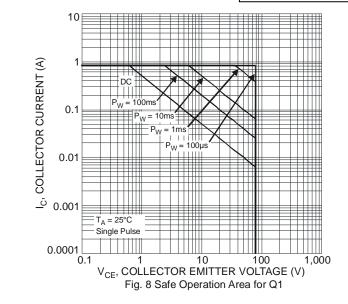
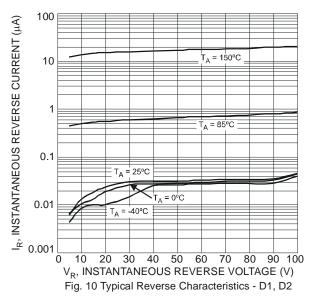


Fig 11 Operating Temperature Derating - D1, D2 $\,$

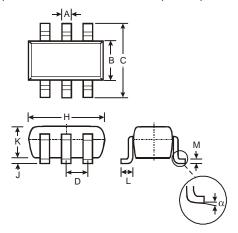






Package Outline Dimensions

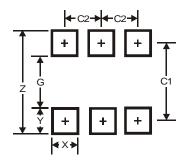
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



| SOT26 | | | | | | |
|-------|----------------------|------|------|--|--|--|
| Dim | Min | Max | Тур | | | |
| Α | 0.35 | 0.50 | 0.38 | | | |
| В | 1.50 | 1.70 | 1.60 | | | |
| O | 2.70 | 3.00 | 2.80 | | | |
| D | _ | | 0.95 | | | |
| Η | 2.90 | 3.10 | 3.00 | | | |
| 7 | 0.013 | 0.10 | 0.05 | | | |
| K | 1.00 | 1.30 | 1.10 | | | |
| ١ | 0.35 | 0.55 | 0.40 | | | |
| M | 0.10 | 0.20 | 0.15 | | | |
| α | 0° | 8° | | | | |
| All D | All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



| Dimensions | Value (in mm) |
|------------|---------------|
| Z | 3.20 |
| G | 1.60 |
| Х | 0.55 |
| Y | 0.80 |
| C1 | 2.40 |
| C2 | 0.95 |



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