APPLICA	BLE STAN	DARD						
/ 11 1 210/1	OPERATING			RAGE				
RATING	TEMPERATURE RANGE VOLTAGE		APPLIC.		IPERATURE RANGE	-10°C TO + 60°C(N	C TO + 60°C (NOTE2)	
					INECTOR	DF9*-31S-1V (**)		
	CURRENT							
			SPECIFICA <sup>-</sup>	ΤΙΟ	NS			
IT	EM		TEST METHOD		REQ	UIREMENTS	QT	АТ
CONSTR	UCTION				1			ı
GENERAL EX	AMINATION	VISUALLY AND BY MEASURING INSTRUMENT.			ACCORDING TO DRAWING.			Χ
MARKING		CONFIRMED VISUALLY.						Х
ELECTR	IC CHARA	CTERIS	STICS					
CONTACT F	RESISTANCE	100	m A (DC OR 1000 Hz).		50mΩ MAX.		Х	_
CONTACT RESISTANCE INSULATION RESISTANCE VOLTAGE PROOF MECHANICAL CHA		100V DC.			500MΩ MIN.			_
VOLTAGE P	ROOF	250V AC FOR 1 min.			NO FLASHOVER OR BREAKDOWN.			_
MECHAN	IICAL CHA	RACTE	RISTICS		1			
		30TIMES	INSERTIONS AND EXTRACTIONS.		① CONTACT RES	SISTANCE: 50mΩ MAX.		
OPERATION					② NO DAMAGE, CRACK OR LOOSENESS OF PARTS.			_
VIBRATION		FREQUENCY 10 TO 55 Hz, SINGLE AMPLITUDE 0.75 mm, AT 2 h, FOR 3 DIRECTIONS.			1 NO ELECTRICAL DISCONTINUITY OF 1µs. 2 NO DAMAGE, CRACK OR LOOSENESS OF PARTS.			_
SHOCK		<u>,</u>			<ol> <li>NO ELECTRICAL DISCONTINUITY OF 1μs.</li> <li>NO DAMAGE, CRACK OR LOOSENESS OF PARTS.</li> </ol>			
ENI/IDO	NAENITAI		ACTERISTICS		(2) NO DAMAGE, CRAC	K OR LOOSENESS OF PARTS.	Х	_
RAPID CHA			TURE -65→ 5 TO 35→125→ 5 TO 35°C		① CONTACT DESIG	TANCE: 50mΩ MAX.	1	ı
TEMPERAT		TIME UNDER 5	30→10 TO 15→ 30→10TO15min		② INSULATION RES	ISTANCE: 500 MΩ MIN. K OR LOOSENESS OF PARTS.	Х	-
DAMP HEAT			D AT 40 ± 2 °C, 90 TO 95 %, 96 h.		① CONTACT RESIS		Х	
(STEADY STATE)					<ul> <li>② INSULATION RESISTANCE: 500 MΩ MIN.</li> <li>③ NO DAMAGE, CRACK OR LOOSENESS OF PARTS.</li> </ul>			_
HEAT RESIS	STANCE OF	(1)REFLO	W SOLDERING			OF CASE OF EXCESSIVE		
SOLDERING	3	《PREHEA 150 TC	V AREA》 0°C, 220°C FOR 60 SECONDS MAX. ATING AREA》 0 180°C 90∼120 SECONDS. UM TWICE ACTION IS ALLOWED UNDER	THE	LOOSENESS OF THE	ETERMINALS.	X	_
		(2) MANUA SOLDE SOLDE	CONDITION. AL SOLDELING RING IRON TEMPERATURE 380°C RING TIME : WITHIN 3 SECONDS. RENGTH ON CONTACT.					
SOLDERAB	ILITY	DURATIO	ING TEMPARATURE:245±5°C DN OF IMMERSION : ING FOR 3±0.5 SECONDS			COATING OF SOLDER SHALL 1 OF 95% OF THE SURFACE D.	Х	_
_			TURE RISE BY CURRENT.		I		<u> </u>	

NOTE2:STORAGEIS DEFINED AS LONG-TERM STORAGE OF UNUSED PRODUCTS.

APPLY OPERATION TEMPERATURE RANGE TO PRODUCTS MOUNTED ON PCB WITHOUT POWER SUPLLY.

UNLESS OTHERWISE SPECIFIED, REFER TO JIS C 5402.

	COUN	DESCRIPTION OF REVISIONS	DESIGNED		CHECKED	DATE	
$\Delta$							
				APPROVED	WR. FUKUCHI	20191024	
				CHECKED	TS. MIYAZAKI	20191024	
				DESIGNED	RH. KAGAMI	20191024	
				DRAWN	RH. KAGAMI	20191024	
Note	Note QT:Qualification Test AT:Assurance Test X:Applicable Test			IG NO.	ELC-160612-22-00		
Н	RS	SPECIFICATION SHEET	PART NO.		DF9C-31P-1V(22)		
		HIROSE ELECTRIC CO., LTD.	CODE NO.	CL540	0-0238-2-22	1/1	