

	CPC1219Y	Units
Blocking Voltage	60	V _P
Load Current	200	mA
Max R _{ON}	16	Ω
Input Voltage to operate	5-12	V

Features

- 100% Solid State
- Voltage-controlled operation
- Designed for use in security systems complying with EN50130-4
- Small 4-Pin SIP Package
- · Arc-Free With No Snubbing Circuits
- 2500V_{rms} Input/Output Isolation
- No EMI/RFI Generation
- · Immune to radiated EM fields
- Auto Pick & Place, Wave Solderable

Applications

- Security
 - · Passive Infrared Detectors (PIR)
 - Data Signalling
- Sensor Circuitry
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - · Electronic Switching
 - I/O Subsystems
 - Energy Meters
- Medical Equipment—Patient/Equipment Isolation
- Aerospace
- Industrial Controls

Description

The CPC1219 is a voltage-controlled, single-pole, normally closed (1-Form-B) optically coupled solid state relay configuration in a 4-pin Single In-line Package (SIP). Clare's patented OptoMOS architecture makes available the optically coupled technology necessary to activate the output's efficient MOSFET switches while providing a 2500V_{rms} input-to-output isolation barrier. Control of the isolated output is accomplished by means of a highly effective GaAlAs infrared LED at the input. An internal resistor in series with the LED enables the input voltage-controlled operation.

Because the input is solid state there is no need for snubbers or "catch" diodes to suppress the inductive flyback transient voltage normally associated with EMR coils.

Approvals

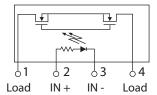
- UL recognized file #E76270
- EN/IEC 60950-1 compliant

Ordering Information

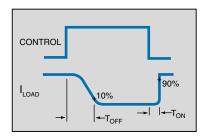
Part #	Description	
CPC1219Y	4-Pin SIP (25/tube)	

Pin Configuration

CPC1219 Pinout



Switching Characteristics of Normally Closed (Form B) Devices











Absolute Maximum Ratings (@ 25° C)

Parameter	Ratings	Units	
Blocking Voltage	60	V_{P}	
Reverse Input Voltage	5	V	
Input Control Voltage	15	V	
Input Power Dissipation	225	mW	
Total Power Dissipation 1	800	mW	
Isolation Voltage Input to Output	2500	V _{rms}	
Operational Temperature	-40 to +85	°C	
Storage Temperature	-40 to +125	°C	

¹ Derate Linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

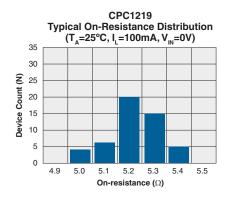
Electrical Characteristics

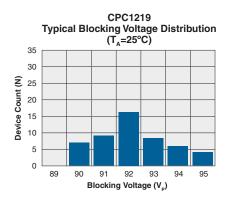
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics @ 25°C						<u>'</u>
Load Current, Continuous 1	-	IL	-	-	200	mA
Peak Load Current	t≤10ms	I _{LPK}	-	-	400	mA
On-Resistance ²	I _L =200mA	R _{ON}	-	-	16	Ω
Off-State Leakage Current	V _L =60V, V _{IN} =5V	I _{LEAK}	-	-	1	μΑ
Switching Speeds						
Turn-On (Output Closed)	$V_{IN} = 5V, V_{L} = 10V$	T_{ON}	-	-	5	ms
Turn-Off (Output Open)		T _{OFF}	-	-	5	ms
Output Capacitance	V _{IN} =5V, V _L =50V, f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics @ 25°C						'
Input Control Voltage						
Output Open			-	-	3.75	V
Output Closed	$I_L = 200 \text{mA}$	V_{IN}	1	-	-	V
Recommended Operating Range			5	-	12	V
Reverse Input Current	V _{IN} = -5V	I _R	-	-	10	μА
Input Resistor	-	-	900	1000	1100	Ω
Common Characteristics @ 25°C	;			-		
Capacitance Input to Output	-	-	-	1	-	pF

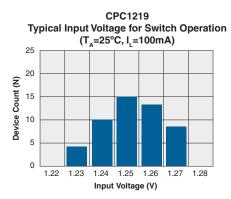
Load current derates linearly from 200mA @ 25°C to 125mA @80°C.
 Measurement taken within 1 second of on time.

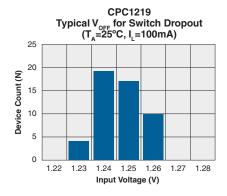


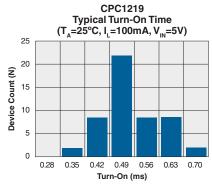
PERFORMANCE DATA*

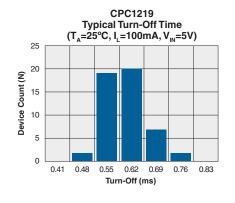


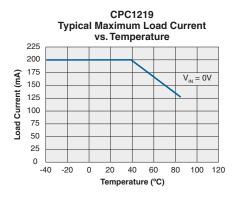


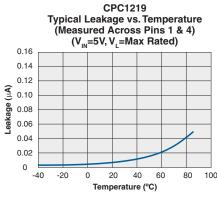


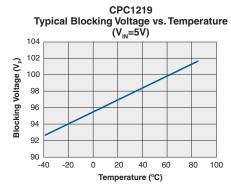


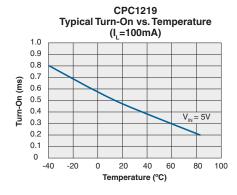


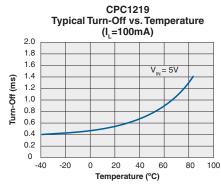


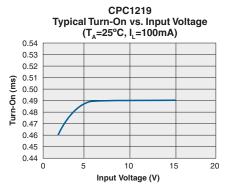








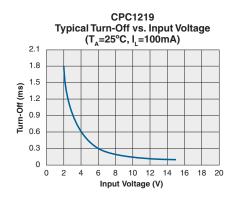


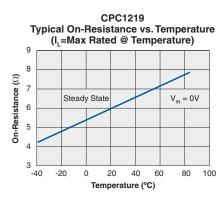


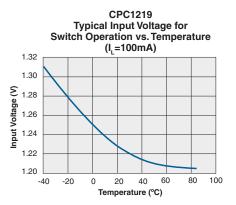
^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

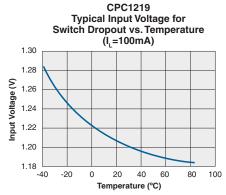


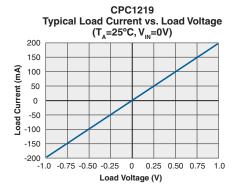
PERFORMANCE DATA*

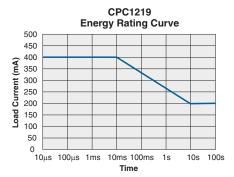












^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

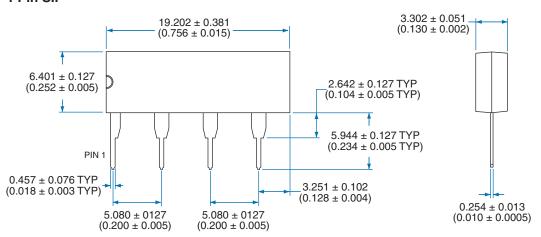






MECHANICAL DIMENSIONS

4-Pin SIP



NOTES:

- 1. Leadframe thickness does not include plating. (1000 microinches maximum)
- 2. Pin location tolerances are non-accumulative.

Dimensions: mm (inches)

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