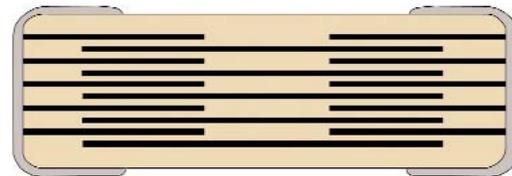


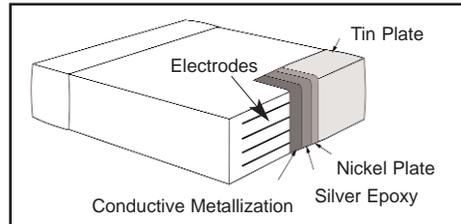
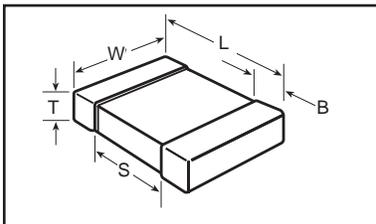
**Fail-Safe Floating Electrode MLCC with Flexible Termination  
FF-CAP / X7R Dielectric**



**Floating (Cascading) Electrode Internal Design**



**Outline Drawing**



**Product Description**

The FF-CAP incorporates two existing KEMET technologies; Floating Electrode (cascading electrode design) and Flexible Termination. The floating electrode component of these capacitors yields improved voltage and ESD performance over standard designs, and also mitigates the risk of low IR or short circuit failures associated with mechanical flex cracks. The flexible termination component incorporates a measure of flexibility to the capacitor, shifting flex stress away from the ceramic body and into the termination area.

The combination of these technologies ensures an increased measure of protection from board flex, offering up to 5mm of flex-bend capability. This provides for an enhanced level of mechanical flex crack protection for low to mid capacitance part types.

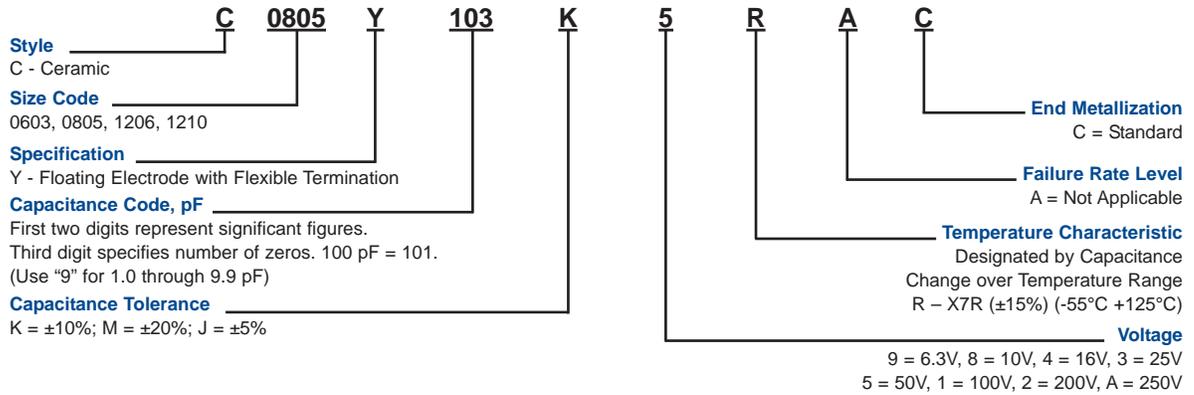
**Dimensions – Millimeters (Inches)**

EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation
0603	1608	1.8 (.071) ± 0.15 (.006)	0.8 (.032) ± 0.15 (.006)	0.35 (.014) ± 0.15 (.006)	0.70 (.028)
0805	2012	2.2 (.087) ± 0.20 (.008)	1.25 (.049) ± 0.20 (.008)	0.05 (.02) ± 0.25 (.010)	0.75 (.030)
1206	3216	3.4 (.134) ± 0.20 (.008)	1.6 (.063) ± 0.20 (.008)	0.50 (.02) ± .25 (.010)	N/A
1210	3225	3.4 (.134) ± 0.20 (.008)	2.5 (.098) ± 0.20 (.008)	0.50 (.02) ± .25 (.010)	N/A

Refer to standard thickness dimensions and table located in the F3102 SMT catalog on pages 73, 74, and 77.



## Ordering Information



## Electrical Parameters

As detailed in the KEMET Surface Mount Catalog F3102 for X7R, with following specific requirements based on room temperature (25°C) parameters:

- Operating Range: -55°C to +125°C, with no-bias capacitance shift limited to  $\pm 15\%$  over that range.
- Insulation Resistance (IR) measured after 2 minutes at rated voltage @ 25°C: Limit is 1,000 megohm microfarads or 100 gigohm, whichever is less.
- Capacitance and Dissipation Factor (DF) measured at 1kHz and 1 Vrms.  
DF Limits are:

50 - 250 Volts	2.5%
16 - 25 Volts	3.5%
6.3 - 10 Volts	5.0%

## Soldering Process

These components are suitable for reflow and wave soldering. All parts incorporate the standard KEMET barrier layer of pure nickel, with an overplate of pure tin to provide excellent solderability as well as resistance to leaching.

## Marking

These chips will be supplied unmarked. If required, they can be laser-marked as an extra option. Details on the marking format are included in KEMET Surface Mount catalog F3102.

## Qualification/Certification

AEC-Q200 Rev. C - Automotive  
RoHS 6 - 100% tin termination

***In general, the information in the KEMET Surface Mount catalog F3102 applies to these capacitors. The information in this bulletin supplements that in the catalog.***