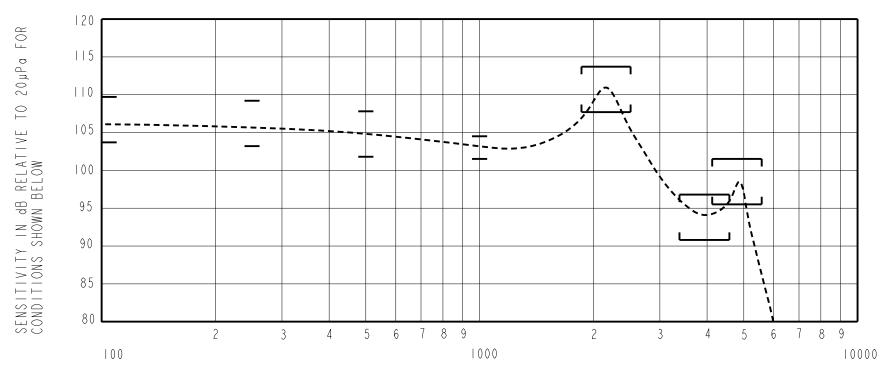


#### CONSTANT VOLTAGE DRIVE CONDITIONS



FREQUENCY IN HERTZ

#### **ACOUSTICAL**

SENSITIVITY

DEVICE WILL PRODUCE THE SPL LISTED BELOW UNDER TEST CONDITIONS DESCRIBED IN TABLE 4. NOMINAL SENSITIVITY AT IKHZ IS dB RELATIVE TO  $20\mu P\alpha$ . ALL OTHER VALUES IN dB RELATIVE TO THE SENSITIVITY AT IkHz.

•				
LIMIT TYPE	FREQUENCY (Hz)	MINIMUM	NOMINAL	MAXIMUM
REL	100	+0.7	+ 3 . 7	+6.7
REL	250	+0.2	+ 3 . 2	+6.2
REL	500	-1.2	+1.8	+4.8
REF	1000	-1.5	103.0	+1.5
PEAK I	1860 - 2510	+4.7	+7.7	+10.7
VALLEY I	3380 - 4580	-12.2	-9.2	-6.2
PEAK 2	4125 - 5580	-7.5	- 4 . 5	-1.5

TABLE I

TOTAL HARMONIC DISTORTION

DEVICE WILL NOT EXCEED TOTAL HARMONIC DISTORTION LEVELS LISTED BELOW.

FREQUENCY (Hz)	AC DRIVE (Vrms)	DC BIAS (V)	LIMIT (%)
1/3 PEAKI (TYP. 728)	0.158	0	3
1/2 PEAKI (TYP. 1082)	0.158	0	3
1/3 PEAKI (TYP. 728)	0.446	0	8
1/2 PEAKI (TYP. 1082)	0.446	0	8

TABLE 2

MAXIMUM OUTPUT LEVEL

POWER (mW)	500 Hz SPL (dB)	REQUIRED VOLTAGE (Vrms)	Peak SPL (dB)	REQUIRED VOLTAGE (Vrms)
10	118.1	0.98	128.4	1.83

TABLE 3

## **ELECTRICAL**

DC RESISTANCE @ 20°C	76.0 Ohms ± 10%
IMPEDANCE @ 500 Hz	109 Ohms ± 15%
IMPEDANCE @ I kHz	170 Ohms ± 15%
INDUCTANCE @ 500 Hz	21.4 mH TYPICAL
CAPACITANCE @ 10 MHz	2.8 pF TYPICAL

TABLE 5

ISOLATION: CASE WILL BE ELECTRICALLY ISOLATED FROM THE COIL CIRCUIT.

#### **MECHANICAL**

PORT LOCATION: 12S SOLDER TYPE: SAC305

TEMPERATURE

OPERATING: SENSITIVITY WILL NOT VARY MORE THAN

+1/-3 dB AT 500 Hz FROM -17°C TO 63°C

STORAGE: -40°C TO 63°C

SHOCK RESISTANCE: 90% SURVIVAL RATE WITH THD @ 1/3 PEAK FREQUENCY LESS THAN 10%, THD @ 1/2 PEAK FREQUENCY LESS THAN 20% AND LESS THAN 3dB CHANGE IN SENSITIVITY AT 1kHz WHEN SUBJECTED TO 14,100 G

### **TEST CONDITIONS**

NOMINAL SOURCE VOLTAGE	0.158 Vrms, 0 mA DC BIAS
SOURCE IMPEDANCE	<   Ohm
TUBING	10 mm [.394"] LONG X   mm [.039"]   .D. (" TE")
COUPLER CAVITY	2 CM <sup>3</sup> , SIMULATED ANSI S3.7 TYPE HA-3 (IEC 60318-5)
MAXIMUM DRIVE VOLTAGE	2.19 Vrms

TABLE 4

Revision	C.O. #	Implementation Date	RELEASE LEVEL		REVISION
A	C10113459	5-21-12	Active		Α
WHEN TEST LIMITS ARE USED TO ESTABLISH INCOMING INSPECTION ACCEPTANCE/REJECTION CRITERIA, CORRELATION OF TEST EQUIPMENT WITH KNOWLES IS ALSO REQUIRED FOR FILMINATION OF FOULPMENT AND TEST METHOD VARIATION				DR. BY	DATE 5 - 2   -   2
ELIMINATION OF EGGINGENT AND TEST METHOD THINKING					DATE
TITLE:	RF	CFIVFR	CR-31502-000	GJP	5-23-12
				APP. BY	DATE
	PERFORMAN	NCE SPECIFICATION	SHT 2.1	GJP	5-23-12

# **KNOWLES ELECTRONICS** ITASCA, ILLINOIS U.S.A.