

General Description

The MAX9117-MAX9120 nanopower comparators in space-saving SC70 packages feature Beyond-the-Rails™ inputs and are guaranteed to operate down to +1.6V. The MAX9117/MAX9118 feature an on-board 1.252V ±1.75% reference and draw an ultra-low supply current of only 600nA, while the MAX9119/MAX9120 (without reference) require just 350nA of supply current. These features make the MAX9117-MAX9120 family of comparators ideal for all 2-cell battery-monitoring/management applications.

The unique design of the output stage limits supply-current surges while switching, virtually eliminating the supply glitches typical of many other comparators. This design also minimizes overall power consumption under dynamic conditions. The MAX9117/MAX9119 have a push-pull output stage that sinks and sources current. Large internal-output drivers allow rail-to-rail output swing with loads up to 5mA. The MAX9118/MAX9120 have an open-drain output stage that makes them suitable for mixed-voltage system design. All devices are available in the ultra-small 5-pin SC70 package.

Applications

2-Cell Battery Monitoring/Management Ultra-Low-Power Systems Mobile Communications Notebooks and PDAs Threshold Detectors/Discriminators Sensing at Ground or Supply Line Telemetry and Remote Systems Medical Instruments

Selector Guide

PART	INTERNAL REFERENCE	OUTPUT TYPE	SUPPLY CURRENT (nA)
MAX9117	Yes	Push-Pull	600
MAX9118	Yes	Open-Drain	600
MAX9119	No	Push-Pull	350
MAX9120	No	Open-Drain	350

Typical Application Circuit appears at end of data sheet.

Beyond-the-Rails is a trademark of Maxim Integrated Products, Inc.

Features

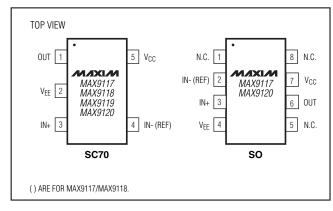
- ♦ Space-Saving SC70 Package (Half the Size of SOT23)
- **♦ Ultra-Low Supply Current** 350nA Per Comparator (MAX9119/MAX9120) 600nA Per Comparator with Reference (MAX9117/MAX9118)
- ♦ Guaranteed to Operate Down to +1.6V
- ♦ Internal 1.252V ±1.75% Reference (MAX9117/MAX9118)
- ♦ Input Voltage Range Extends 200mV **Beyond-the-Rails**
- ♦ CMOS Push-Pull Output with ±5mA Drive Capability (MAX9117/MAX9119)
- ♦ Open-Drain Output Versions Available (MAX9118/MAX9120)
- **♦** Crowbar-Current-Free Switching
- ♦ Internal Hysteresis for Clean Switching
- ♦ No Phase Reversal for Overdriven Inputs

Ordering Information

PART	PIN- PACKAGE	TOP MARK	PKG CODE
MAX9117EXK-T	5 SC70-5	ABW	X5-1
MAX9117ESA+	8 SO	_	S8-2
MAX9118EXK-T	5 SC70-5	ABX	X5-1
MAX9119EXK-T	5 SC70-5	ABY	X5-1
MAX9120EXK-T	5 SC70-5	ABZ	X5-1
MAX9120ESA+	8 SO	_	S8-2

Note: All devices specified for over -40°C to +85°C operating temperature range.

Pin Configurations



MIXIM

Maxim Integrated Products 1

⁺Denotes lead-free package.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{CC} to V _{EE})+6V
Voltage Inputs (IN+, IN-, REF)(VEE - 0.3V) to (VCC + 0.3V)
Output Voltage
MAX9117/MAX9119(VEE - 0.3V) to (VCC + 0.3V)
MAX9118/MAX9120(V _{EE} - 0.3V) to +6V
Current Into Input Pins±20mA
Output Current±50mA
Output Short-Circuit Duration

Continuous Power Dissipation ($T_A = +70^{\circ}C$)	
5-Pin SC70 (derate 2.5mW/°C above +70°C)	200mW
8-Pin SO (derate 5.88mW/°C above +70°C)	471mW
Operating Temperature Range40°C	C to +85°C
Junction Temperature	+150°C
Storage Temperature Range65°C	to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX9117/MAX9118 (with REF)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{IN+} = V_{REF}, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
0 1 1/1 5	.,	Inferred from the PSRR	T _A = +25°C	1.6		5.5 V		
Supply Voltage Range	Vcc	test	$T_A = T_{MIN}$ to T_{MAX}	1.8		5.5	\ \	
		$V_{CC} = 1.6V$	$T_A = +25^{\circ}C$		0.60	1		
Supply Current	Icc		$T_A = +25^{\circ}C$		0.68	1.3	μΑ	
		$V_{CC} = 5V$	$T_A = T_{MIN}$ to T_{MAX}			1.6		
IN+ Voltage Range	V _{IN+}	Inferred from output sw	ring test	V _{EE} - 0.2		V _{CC} + 0.2	V	
learnet Offe et Welle er	\/	(NI-+- Q)	$T_A = +25^{\circ}C$		1	5	>/	
Input Offset Voltage	Vos	(Note 2)	TA = TMIN to TMAX			10	mV	
Input-Referred Hysteresis	V _{HB}	(Note 3)			4		mV	
Input Bias Current	IB	$T_A = +25^{\circ}C$			0.15	1	nA	
Input bias Current	ıB	TA = TMIN to TMAX				2	ПА	
Davier Coursely Dairation Datie	DODD	VCC = 1.6V to 5.5V, T _A = +25°C			0.1	1	mV/V	
Power-Supply Rejection Ratio	PSRR	$V_{CC} = 1.8V \text{ to } 5.5V, T_A$			1			
		MAX9117, V _{CC} = 5V, ISOURCE = 5mA	T _A = +25°C		190	400		
			$T_A = T_{MIN}$ to T_{MAX}			500]	
Output Voltage Swing High	VCC - VOH	1447/04/17	$V_{CC} = 1.6V, T_A = +25^{\circ}C$		100 200		mV	
		MAX9117, ISOURCE = 1mA	$V_{CC} = 1.8V$, $T_A = T_{MIN}$ to T_{MAX}			300		
		\/	T _A = +25°C		190	400		
		$V_{CC} = 5V$, $I_{SINK} = 5mA$	$T_A = T_{MIN}$ to T_{MAX}			500		
Output Voltage Swing Low	Vol		$V_{CC} = 1.6V, T_A = +25^{\circ}C$		100	200	mV	
			$V_{CC} = 1.8V$, $T_A = T_{MIN}$ to T_{MAX}			300		
Output Leakage Current	ILEAK	MAX9118 only, $V_O = 5.5V$			0.002	1	μΑ	
		Coursing V- V	$V_{CC} = 5V$		35			
Output Short-Circuit Current	1	Sourcing, $V_O = V_{EE}$	Vcc = 1.6V		3		mA	
	Isc	Sinking Vo - Vos	Vcc = 5V		35			
		Sinking, $V_O = V_{CC}$	V _{CC} = 1.6V		3			
High-to-Low Propagation Delay	top	$V_{CC} = 1.6V$			16		116	
(Note 4)	tpD-	V _{CC} = 5V			14		μs	

ELECTRICAL CHARACTERISTICS—MAX9117/MAX9118 (with REF) (continued)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{IN+} = V_{REF}, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	SYMBOL CONDITIONS			TYP	MAX	UNITS
		MAYO117 only	V _{CC} = 1.6V		15		
		MAX9117 only	$V_{CC} = 5V$		40]
Low-to-High Propagation Delay (Note 4)	t _{PD+}		$V_{CC} = 1.6V, R_{PULLUP}$ = 100k Ω	16			μs
		MAX9118 only	$V_{CC} = 5V$, $R_{PULLUP} = 100k\Omega$		45		
Rise Time	tRISE	MAX9117 only, C _L =	= 15pF		1.6		μs
Fall Time	tfall	$C_L = 15pF$			0.2		μs
Power-Up Time	ton				1.2		ms
Deference Voltage	V	T _A = +25°C		1.230	1.252	1.274	V
Reference Voltage	V _{REF}	$T_A = T_{MIN}$ to T_{MAX}		1.196		1.308]
Reference Voltage Temperature Coefficient	TC _{REF}				100		ppm/ °C
Deference Output Voltage Naise	Г	BW = 10Hz to 100kHz		1.1			m)/m :=
Reference Output Voltage Noise	E _N	BW = 10Hz to 100kH	BW = 10Hz to 100kHz, CREF = 1nF		0.2		mV _{RMS}
Reference Line Regulation	ΔV _{REF} / ΔV _{CC}	$V_{CC} = 1.6V \text{ to } 5.5V$			0.25		mV/V
Reference Load Regulation	ΔV _{REF} /	Δl _{OUT} = 10nA			±1		mV/ nA

ELECTRICAL CHARACTERISTICS—MAX9119/MAX9120 (without REF)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = 0V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS	
Cupply Voltage Dange	Vac	Inferred from the	T _A = +25°C	1.6		5.5	_	
Supply Voltage Range	Vcc	PSRR test	$T_A = T_{MIN}$ to T_{MAX}	1.8		5.5	V	
		$V_{CC} = 1.6V, T_A = +25^\circ$	°C		0.35	0.80		
Supply Current	Icc	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	T _A = +25°C		0.45	0.80	μΑ	
		Vcc = 5V	$T_A = T_{MIN}$ to T_{MAX}			1.2		
Input Common-Mode Voltage Range	V _{CM}	Inferred from the CMRR test		V _{EE} - 0.2		V _{CC} + 0.2	V	
	Vos	-0.2V ≤ V _{CM} ≤	T _A = +25°C		1	5		
Input Offset Voltage		(V _{CC} + 0.2V) (Note 2)	TA = TMIN to TMAX			10	mV	
Input-Referred Hysteresis	V _{HB}	-0.2V ≤ V _{CM} ≤ (V _{CC} +	0.2V) (Note 3)		4		mV	
Input Dies Current	1-	T _A = +25°C			0.15	1	.α Λ	
Input Bias Current	Ι _Β	$T_A = T_{MIN}$ to T_{MAX}				2	nA	
Input Offset Current	los				75		рА	
Dayyar Cymphy Daigatian Datia	DCDD	V _{CC} = 1.6V to 5.5V, T _A = +25°C			0.1	1	ma\/\/	
Power-Supply Rejection Ratio	PSRR	V _{CC} = 1.8V to 5.5V, T _A = T _{MIN} to T _{MAX}			•	1	mV/V	
Common-Mode Rejection Ratio	CMRR	$(V_{EE} - 0.2V) \le V_{CM} \le (V_{EE} - 0.2V)$	V _{CC} + 0.2V)		0.5	3	mV/V	

ELECTRICAL CHARACTERISTICS—MAX9119/MAX9120 (without REF) (continued)

 $(V_{CC} = +5V, V_{EE} = 0V, V_{CM} = 0V, T_A = -40^{\circ}C \text{ to } +85^{\circ}C, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$ (Note 1)

PARAMETER	SYMBOL	COND	ITIONS	MIN	TYP	MAX	UNITS	
		MAX9119 only, V _{CC} =	T _A = +25°C		190	400		
		5V, ISOURCE = 5mA	$T_A = T_{MIN}$ to T_{MAX}			500		
Output Voltage Swing High	V _{CC} - V _{OH}	MAX9119 only,	$V_{CC} = 1.6V,$ $T_{A} = +25^{\circ}C$		100	200	mV	
		ISOURCE = 1mA	$V_{CC} = 1.8V,$ $T_{A} = T_{MIN} \text{ to } T_{MAX}$			300		
		$V_{CC} = 5V$,	T _A = +25°C		190	400		
		I _{SINK} = 5mA	$T_A = T_{MIN}$ to T_{MAX}			500		
Output Voltage Swing Low	V _{OL}	Journal Trad	$V_{CC} = 1.6V,$ $T_{A} = +25^{\circ}C$		100	200 mV	mV	
		ISINK = 1mA	$V_{CC} = 1.8V,$ $T_{A} = T_{MIN} \text{ to } T_{MAX}$			300		
Output Leakage Current	ILEAK	MAX9120 only, $V_O = 5$.5V		0.001	1	μΑ	
	Isc	Sourcing, V _O = V _{EE}	$V_{CC} = 5V$		35		mA	
Output Short-Circuit Current			V _{CC} = 1.6V		3			
Output Short-Circuit Current		Sourcing, V _O = V _{CC}	$V_{CC} = 5V$		35		IIIA	
			V _{CC} = 1.6V		3			
High-to-Low Propagation Delay	tpD-		V _{CC} = 1.6V		16		μs	
(Note 4)	4-D-		$V_{CC} = 5V$		14		μо	
		MAX9119 only	V _C C = 1.6V		15			
		Win the Fire of the	$V_{CC} = 5V$		40	40		
Low-to-High Propagation Delay (Note 4)	t _{PD+}	MAX9120 only	$V_{CC} = 1.6V$, $R_{PULLUP} = 100k\Omega$		16		μs	
		WAX9120 Only	$V_{CC} = 5V$, $R_{PULLUP} = 100k\Omega$		45			
Rise Time	trise	MAX9119 only, C _L = 15pF			1.6		μs	
Fall Time	tFALL	$C_L = 15pF$			0.2		μs	
Power-Up Time	ton				1.2		ms	

Note 1: All specifications are 100% tested at T_A = +25°C. Specification limits over temperature (T_A = T_{MIN} to T_{MAX}) are guaranteed by design, not production tested.

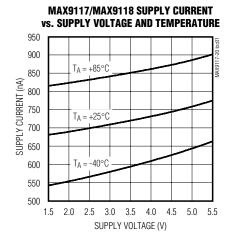
Note 2: VOS is defined as the center of the hysteresis band at the input.

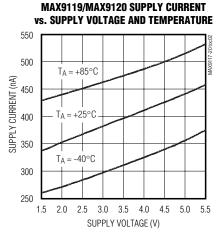
Note 3: The hysteresis-related trip points are defined as the edges of the hysteresis band, measured with respect to the center of the band (i.e., Vos) (Figure 2).

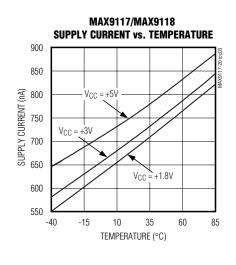
Note 4: Specified with an input overdrive (V_{OVERDRIVE}) of 100mV, and load capacitance of C_L = 15pF. V_{OVERDRIVE} is defined above and beyond the offset voltage and hysteresis of the comparator input. For the MAX9117/MAX9118, reference voltage error should also be added.

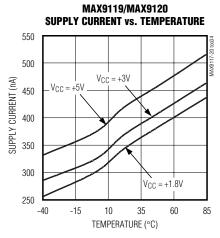
Typical Operating Characteristics

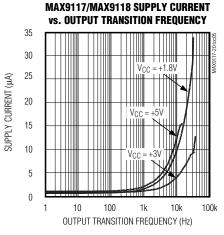
(VCC = +5V, VFF = 0V, CL = 15pF, Voverprive = 100mV, TA = +25°C, unless otherwise noted.)

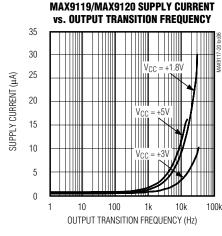


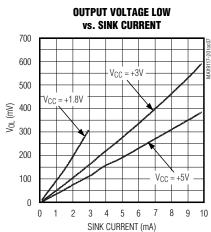


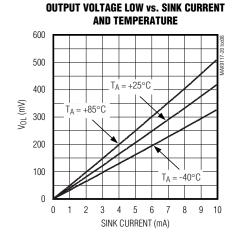


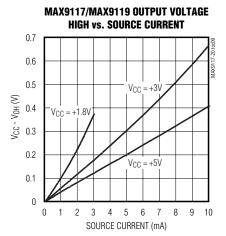






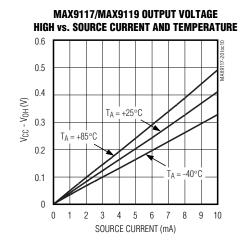


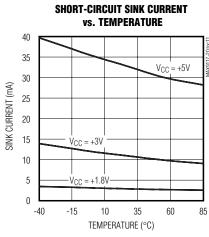


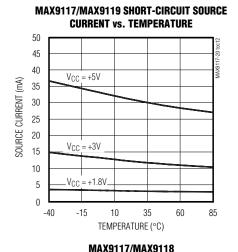


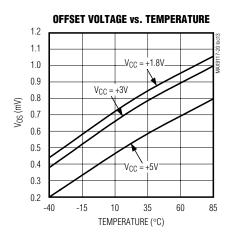
Typical Operating Characteristics (continued)

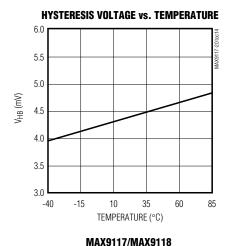
 $(V_{CC} = +5V, V_{EE} = 0V, C_L = 15pF, V_{OVERDRIVE} = 100mV, T_A = +25^{\circ}C, unless otherwise noted.)$

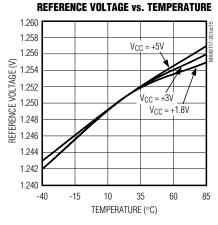


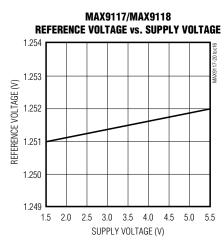


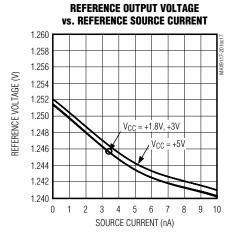


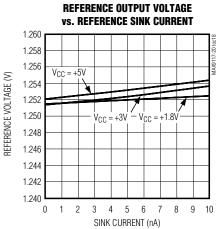








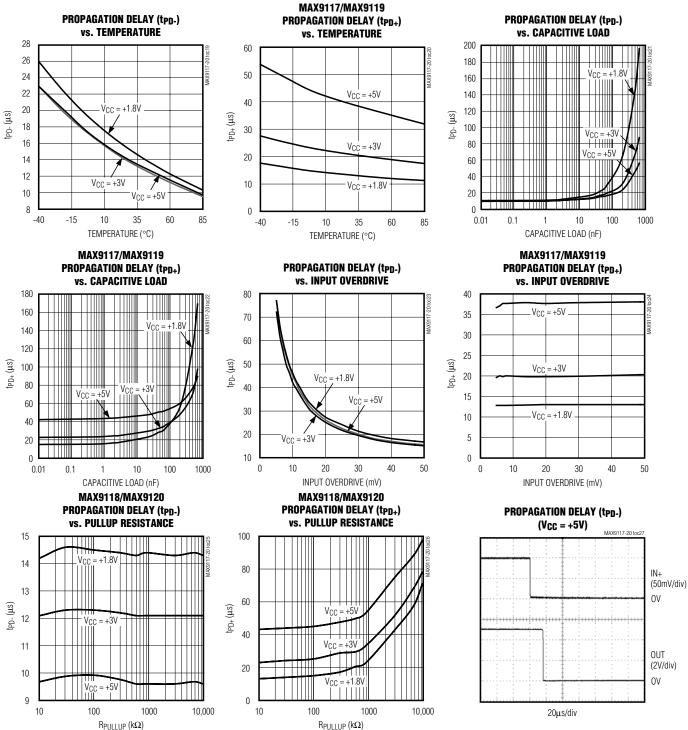




MAX9117/MAX9118

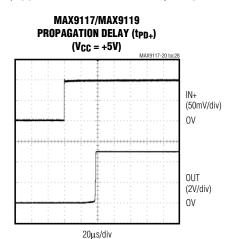
Typical Operating Characteristics (continued)

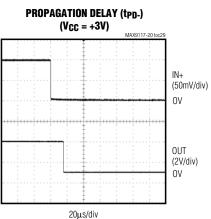
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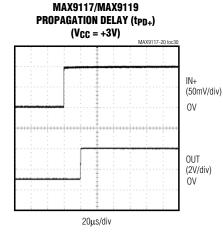


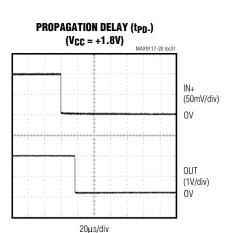
Typical Operating Characteristics (continued)

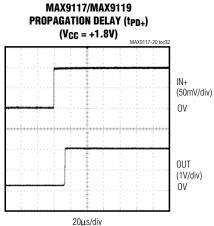
(VCC = +5V, VEE = 0V, CL = 15pF, VOVERDRIVE = 100mV, TA = +25°C, unless otherwise noted.)

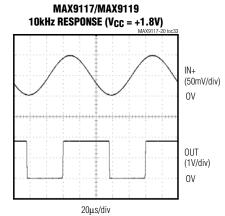


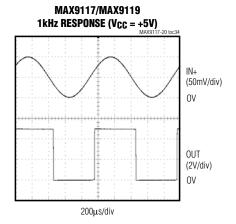


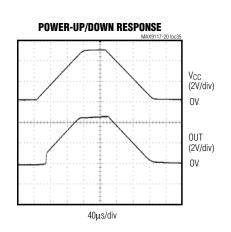




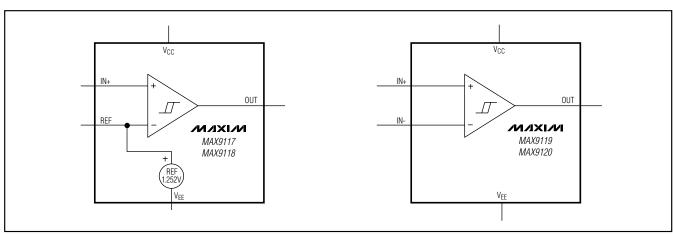








Functional Diagrams



Pin Description

	PII	N				
MAX9	9117/ 9118		9119/ 9120	NAME	FUNCTION	
SC70	so	SC70	so			
1	6	1	6	OUT	Comparator Output	
2	4	2	4	VEE	Negative Supply	
3	3	3	3	IN+	Comparator Noninverting Input	
4	2	_		REF	1.252V Reference	
5	7	5	7	VCC	Positive Supply	
		4	2	IN-	Comparator Inverting Input	
_	1, 5, 8		1, 5, 8	N.C.	No Connection. Not internally connected.	

Detailed Description

The MAX9117/MAX9118 feature an on-board 1.252V ±1.75% reference, yet draw an ultra-low supply current of 600nA. The MAX9119/MAX9120 (without reference) consume just 350nA of supply current. All four devices are guaranteed to operate down to +1.6V. Their common-mode input voltage range extends 200mV beyond-the-rails. Internal hysteresis ensures clean output switching, even with slow-moving input signals. Large internal output drivers allow rail-to-rail output swing with up to ±5mA loads.

The output stage employs a unique design that minimizes supply-current surges while switching, virtually

eliminating the supply glitches typical of many other comparators. The MAX9117/MAX9119 have a push-pull output stage that sinks as well as sources current. The MAX9118/MAX9120 have an open-drain output stage that can be pulled beyond V_{CC} to an absolute maximum of 6V above V_{EE}. These open-drain versions are ideal for implementing wire-OR output logic functions.

Input Stage Circuitry

The input common-mode voltage range extends from V_{EE} - 0.2V to V_{CC} + 0.2V. These comparators operate at any differential input voltage within these limits. Input bias current is typically ± 0.15 nA if the input voltage is between the supply rails. Comparator inputs are protected from overvoltage by internal ESD protection diodes connected to the supply rails. As the input voltage exceeds the supply rails, these ESD protection diodes become forward biased and begin to conduct.

Output Stage Circuitry

The MAX9117-MAX9120 contain a unique breakbefore-make output stage capable of rail-to-rail operation with up to ±5mA loads. Many comparators consume orders of magnitude more current during switching than during steady-state operation. However, with this family of comparators, the supply-current change during an output transition is extremely small. In the Typical Operating Characteristics, the Supply Current vs. Output Transition Frequency graphs show the minimal supply-current increase as the output switching frequency approaches 1kHz. This characteristic reduces the need for power-supply filter capacitors to reduce glitches created by comparator switching currents. In battery-powered applications, this characteristic results in a substantial increase in battery life.

Reference (MAX9117/MAX9118)

The internal reference in the MAX9117/MAX9118 has an output voltage of +1.252V with respect to VEE. Its typical temperature coefficient is 100ppm/°C over the full -40°C to +85°C temperature range. The reference is a PNP emitter-follower driven by a 120nA current source (Figure 1). The output impedance of the voltage reference is typically 200k Ω , preventing the reference from driving large loads. The reference can be bypassed with a low-leakage capacitor. The reference is stable for any capacitive load. For applications requiring a lower output impedance, buffer the reference with a low-input-leakage op amp, such as the MAX4162.

Applications Information

Low-Voltage, Low-Power Operation

The MAX9117–MAX9120 are ideally suited for use with most battery-powered systems. Table 1 lists a variety of battery types, capacities, and approximate operating times for the MAX9117–MAX9120, assuming nominal conditions.

Internal Hysteresis

Many comparators oscillate in the linear region of operation because of noise or undesired parasitic feedback. This tends to occur when the voltage on one input is equal or very close to the voltage on the other input. The MAX9117–MAX9120 have internal hysteresis to counter parasitic effects and noise.

The hysteresis in a comparator creates two trip points: one for the rising input voltage (V_{THR}) and one for the falling input voltage (V_{THF}) (Figure 2). The difference between the trip points is the hysteresis (V_{HB}). When the comparator's input voltages are equal, the hysteresis effectively causes one comparator input to move quickly past the other, thus taking the input out of the

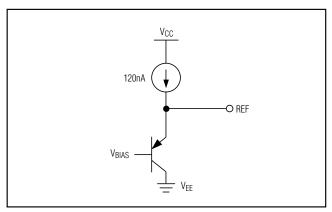


Figure 1. MAX9117/MAX9118 Voltage Reference Output Equivalent Circuit

region where oscillation occurs. Figure 2 illustrates the case in which IN- has a fixed voltage applied, and IN+ is varied. If the inputs were reversed, the figure would be the same, except with an inverted output.

Additional Hysteresis (MAX9117/MAX9119)

The MAX9117/MAX9119 have a 4mV internal hysteresis band (VHB). Additional hysteresis can be generated with three resistors using positive feedback (Figure 3). Unfortunately, this method also slows hysteresis response time. Use the following procedure to calculate resistor values.

Select R3. Leakage current at IN is under 2nA, so the current through R3 should be at least 0.2µA to minimize errors caused by leakage current. The current through R3 at the trip point is (VREF - VOUT) / R3. Considering the two possible output states in solving for R3 yields two formulas: R3 = VREF / IR3 or R3 = (VCC - VREF) / IR3. Use the smaller of the two resulting resistor values. For example, when using the

Table 1. Battery Applications Using MAX9117-MAX9120

BATTERY TYPE	RECHARGEABLE	V _{FRESH} (V)	V _{END-OF-LIFE} (V) CAPACITY, AA SIZE (MA-h) MAX9117/MAX9118 OPERATING TIME (hr)		MAX9119/MAX9120 OPERATING TIME (hr)	
Alkaline (2 Cells)	No	3.0	1.8	2000	2.5 x 10 ⁶	5 x 10 ⁶
Nickel-Cadmium (2 Cells)	Yes	2.4	1.8	750	937,500	1.875 x 10 ⁶
Lithium-Ion (1 Cell)	Yes	3.5	2.7	1000	1.25 x 10 ⁶	2.5 x 10 ⁶
Nickel-Metal- Hydride (2 Cells)	Yes	2.4	1.8	1000	1.25 x 10 ⁶	2.5 x 10 ⁶

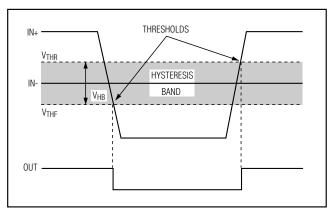


Figure 2. Threshold Hysteresis Band

MAX9117 (VREF = 1.252V) and VCC = +5V, and if we choose IR3 = 1 μ A, then the two resistor values are 1.2M Ω and 3.8M Ω . Choose a 1.2M Ω standard value for R3.

- 2) Choose the hysteresis band required (V_{HB}). For this example, choose 50mV.
- 3) Calculate R1 according to the following equation:

$$R1 = R3 (V_{HB} / V_{CC})$$

For this example, insert the values:

$$R1 = 1.2M\Omega (50mV / 5V) = 12k\Omega$$

- 4) Choose the trip point for V_{IN} rising (V_{THR}) such that V_{THR} > V_{REF} × (R1 + R3) / R3, (V_{THR} is the trip point for V_{IN} rising). This is the threshold voltage at which the comparator switches its output from low to high as V_{IN} rises above the trip point. For this example, choose 3V.
- 5) Calculate R2 as follows:

R2 = 1 / [V_{THR} / (V_{REF} × R1) - (1 / R1) - (1 / R3)]
R2 = 1 / [3.0V / (1.252V × 12k
$$\Omega$$
) - (1 / 12k Ω) - (1 / 1.2M Ω)] = 8.655k Ω

For this example, choose an $8.66k\Omega$ standard 1% value.

6) Verify the trip voltages and hysteresis as follows:

$$V_{IN}$$
 rising: $V_{THR} = V_{REF} \times R1 [(1 / R1) + (1 / R2) + (1 / R3)] = 3V$

$$V_{IN}$$
 falling: $V_{THF} = V_{THR} - (R1 \times V_{CC} / R3) = 2.95V$
Hysteresis = $V_{THR} - V_{THF} = 50 \text{mV}$

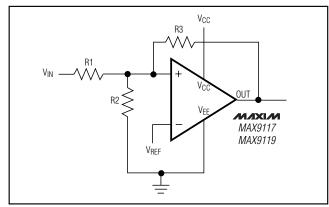


Figure 3. MAX9117/MAX9119 Additional Hysteresis

Additional Hysteresis (MAX9118/MAX9120)

The MAX9118/MAX9120 have a 4mV internal hysteresis band. They have open-drain outputs and require an external pullup resistor (Figure 4). Additional hysteresis can be generated using positive feedback, but the formulas differ slightly from those of the MAX9117/MAX9119. Use the following procedure to calculate resistor values.

- 1) Select R3 according to the formulas R3 = V_{REF} / $1\mu A$ or R3 = $(V_{CC} V_{REF})$ / $1\mu A$ R4. Use the smaller of the two resulting resistor values.
- 2) Choose the hysteresis band required (V_{HB}).
- 3) Calculate R1 according to the following equation:

$$R1 = (R3 + R4) (V_{HB} / V_{CC})$$

- 4) Choose the trip point for V_{IN} rising (V_{THR}) (V_{THR} is the trip point for V_{IN} rising). This is the threshold voltage at which the comparator switches its output from low to high as V_{IN} rises above the trip point.
- 5) Calculate R2 as follows:

$$R2 = 1 / \left[\left(\frac{V_{THR}}{V_{REF} \times R1} \right) - \frac{1}{R1} - \frac{1}{R3} \right]$$

6) Verify the trip voltages and hysteresis as follows:

$$V_{IN}$$
 rising: $V_{THR} = V_{REF} \times R1 \left(\frac{1}{R1} + \frac{1}{R2} + \frac{1}{R3} \right)$

V_{IN} falling:

$$V_{THF} = V_{REF} \times R1 \left(\frac{1}{R1} + \frac{1}{R2} + \frac{1}{R3 + R4} \right) - \frac{R1}{R3 + R4} \times V_{CC}$$

Hysteresis = V_{THR} - V_{THF}

Board Layout and Bypassing

Power-supply bypass capacitors are not typically needed, but use 100nF bypass capacitors close to the device's supply pins when supply impedance is high, supply leads are long, or excessive noise is expected on the supply lines. Minimize signal trace lengths to reduce stray capacitance. A ground plane and surface-mount components are recommended. If the REF pin is decoupled, use a new low-leakage capacitor.

Zero-Crossing Detector

Figure 5 shows a zero-crossing detector application. The MAX9119's inverting input is connected to ground, and its noninverting input is connected to a 100mV_{P-P} signal source. As the signal at the noninverting input crosses 0V, the comparator's output changes state.

Logic-Level Translator

The *Typical Application Circuit* shows an application that converts 5V logic to 3V logic levels. The MAX9120 is powered by the +5V supply voltage, and the pullup resistor for the MAX9120's open-drain output is connected to the +3V supply voltage. This configuration allows the full 5V logic swing without creating overvoltage on the 3V logic inputs. For 3V to 5V logic-level translations, simply connect the +3V supply voltage to VCC and the +5V supply voltage to the pullup resistor.

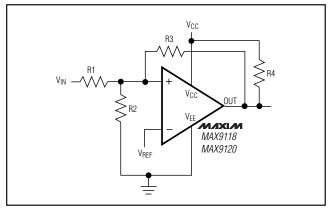
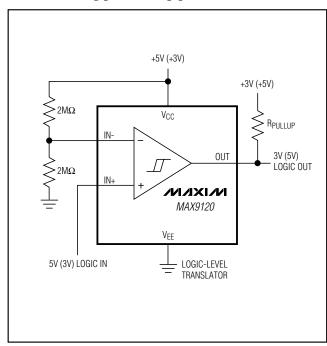


Figure 4. MAX9118/MAX9120 Additional Hysteresis

Typical Application Circuit



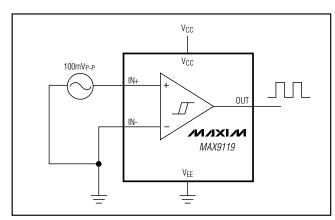


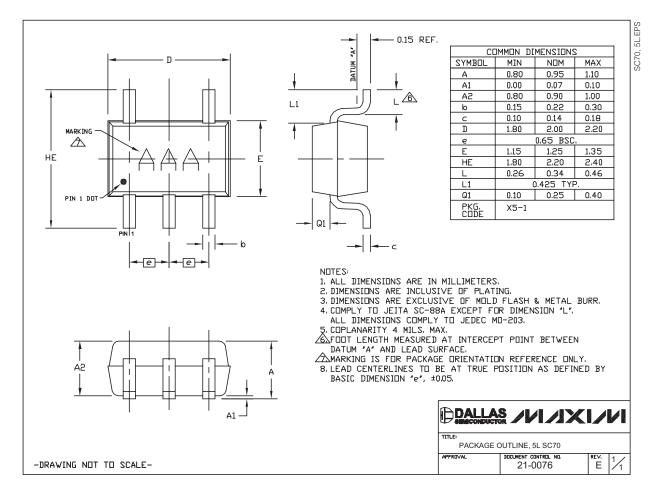
Figure 5. Zero-Crossing Detector

_____Chip Information

TRANSISTOR COUNT: 98

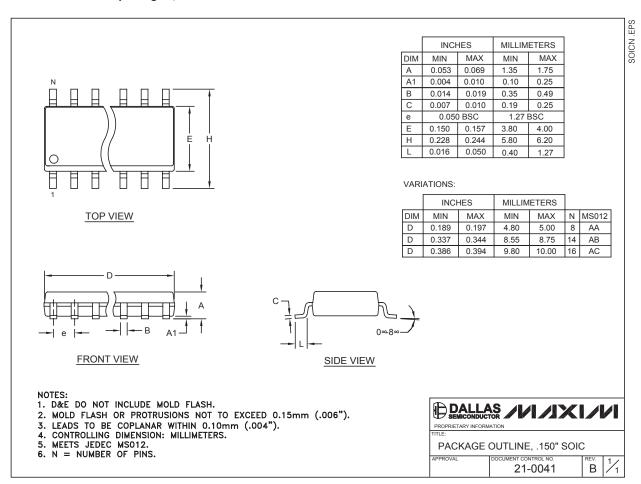
Package Information

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Package Information (continued)

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_Revision History

Pages changed at Rev 4: 1, 2, 9, 13

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