

Features

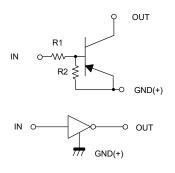
- · Epitaxial Planar Die Construction
- Built-In Biasing Resistors
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- · Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant.See Ordering Information)

Maximum Ratings @ 25°C Unless Otherwise Specified

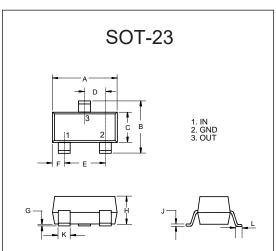
Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V _{CC}		-50		V
Input Voltage	V _{IN}	-12		+5	V
Output Current	Io		-100		mA
Power Dissipation	P _D		200		mW
Junction Temperature	TJ		150		°C
Storage Temperature	T _{stg}	-55		150	°C

Device Marking: 52

Internal Structure

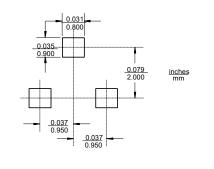


PNP Digital Transistor



DIMENSIONS					
DIM INC		HES	M	М	NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.110	0.120	2.80	3.04	
В	0.083	0.104	2.10	2.64	
С	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
Н	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



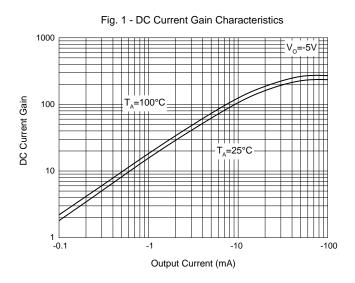


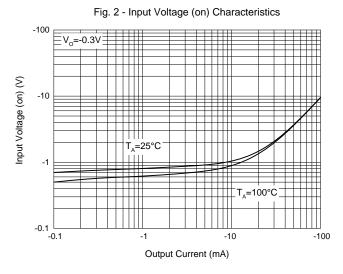
Electrical Characteristics @ 25°C Unless Otherwise Specified

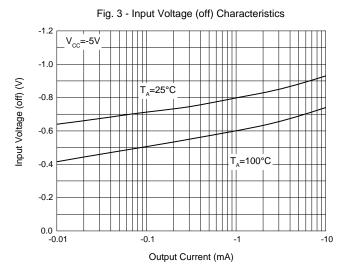
Parameter	Symbol	Min	Тур	Max	Unit	Conditions
lancit Valtaga	$V_{I(off)}$	-0.3			V	V _{CC} =-5V, I _O =-100μA
Input Voltage	V _{I(on)}			-3.0	V	V _O =-0.3V, I _O =-20mA
Output Voltage	V _{O(on)}			-0.3	V	I _O =-10mA,I _I =-0.5mA
Input Current	I _I			-3.8	mA	V _I =-5V
Output Current	I _{O(off)}			-0.5	μA	V _{CC} =-50V, V _I =0
DC Current Gain	G _I	33				V _O =-5V, I _O =-10mA
Input Resistance	R ₁	1.54	2.2	2.86	ΚΩ	
Resistance Ratio	R ₂ /R ₁	3.6	4.5	5.5		
Transition Frequency	f _T		250		MHz	V _{CE} =-10V, I _E =5mA, f=100MHz

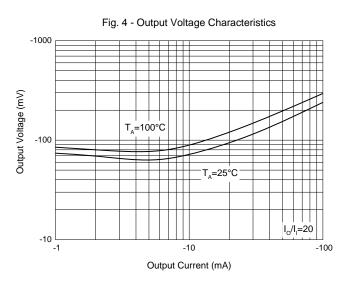


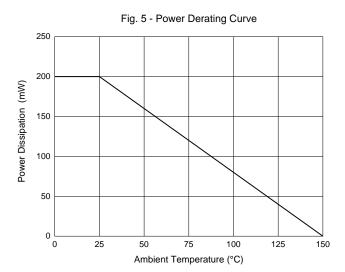
Curve Characteristics













Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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