

Fig. 1

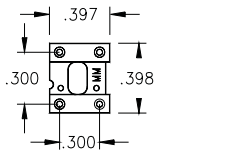


Fig. 2

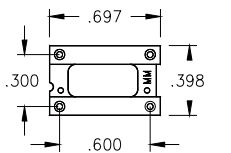


Fig. 3

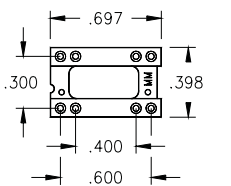
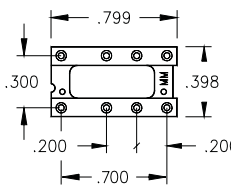
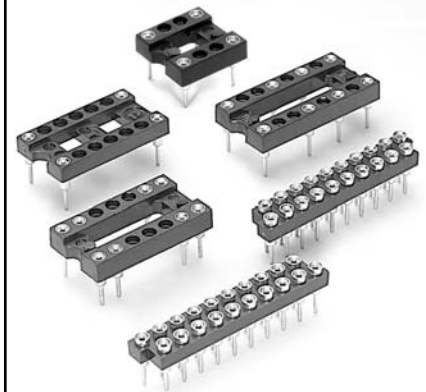


Fig. 4



- Relay sockets accept devices with I/O pins on .100" grid.
- Zig-Zag strip sockets are suitable for IC's and memory chips with staggered double row patterns.
- Standard insulator is molded PBT polyester. High temp PCT is used for RoHS plating options.
- Uses MM# 1001 pin.
See page 134 for details.



Selectively Loaded Sockets For Dual-In-Line Relays

	No. of pins	Ordering Information
Fig. 1	4	110-XX-308-10-001000
Fig. 2	4	110-XX-314-10-001000
Fig. 3	8	110-XX-314-10-002000
Fig. 4	8	110-XX-316-10-003000

Staggered (Zig-Zag) Strip Sockets

Dim 'A'	No. of pins	Insulator Body	Ordering Information
0.747	14	Left, Stackable	410-XX-214-10-001000
0.747	14	Right, Stackable	410-XX-214-10-002000
0.847	16	Left, Stackable	410-XX-216-10-001000
0.847	16	Right, Stackable	410-XX-216-10-002000
1.047	20	Left, Stackable	410-XX-220-10-001000
1.047	20	Right, Stackable	410-XX-220-10-002000
1.247	24	Left, Stackable	410-XX-224-10-001000
1.247	24	Right, Stackable	410-XX-224-10-002000
1.447	28	Left, Stackable	410-XX-228-10-001000
1.447	28	Right, Stackable	410-XX-228-10-002000



XX= Plating Code
See Below

For RoHS compliance, select plating code.
For the all gold option (XX=13) also change part number from X10-XX-XXX-10-00X000 to X10-13-XXX-10-00X100

SPECIFY PLATING CODE XX=	13	93	43
Sleeve (Pin)	10µ" Au	200µ" Sn/Pb	200µ" Sn
Contact (Clip)	30µ" Au	30µ" Au	30µ" Au