

# NL17SZ07

## Single Non-Inverting Buffer with Open Drain Output

The NL17SZ07 is a high performance single inverter with open drain outputs operating from a 1.65 to 5.5 V supply.

The Output stage is open drain with Over Voltage Tolerance. This allows the NL17SZ07 to be used to interface 5.0 V circuits to circuits of any voltage between 0 and +7.0 V.

- Tiny SOT-353 and SOT-553 Packages
- Extremely High Speed:  $t_{PD}$  2.5 ns (typical) at  $V_{CC} = 5$  V
- Designed for 1.65 V to 5.5 V  $V_{CC}$  Operation, CMOS Compatible
- Over Voltage Tolerant Inputs  $V_{IN}$  may be Between 0 and 7.0 V for  $V_{CC}$  Between 0.5 and 5.4 V
- TTL Compatible - Interface Capability with 5.0 V TTL Logic with  $V_{CC} = 2.7$  V to 3.6 V
- LVC MOS Compatible
- 24 mA Output Sink Capability, Pull-up may be between 0 and 7.0 V
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- Chip Complexity: FET = 20

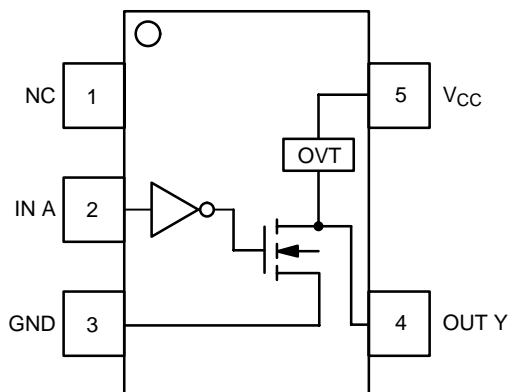


Figure 1. Pinout

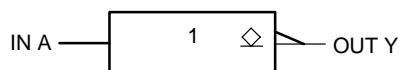


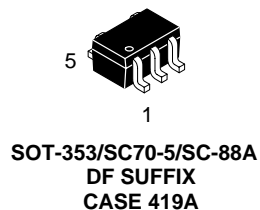
Figure 2. Logic Symbol



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### MARKING DIAGRAMS



d = Date Code



L7 = Device Marking  
D = One Digit Date Code

### PIN ASSIGNMENT

Pin	Function
1	NC
2	IN A
3	GND
4	OUT Y
5	$V_{CC}$

### FUNCTION TABLE

A Input	Y Output
L	L
H	Z

### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

# NL17SZ07

## MAXIMUM RATINGS (Note 1)

Symbol	Characteristics	Value	Unit
V <sub>CC</sub>	DC Supply Voltage	− 0.5 to + 7.0	V
V <sub>I</sub>	DC Input Voltage	− 0.5 ≤ V <sub>I</sub> ≤ + 7.0	V
V <sub>O</sub>	DC Output Voltage Output in Z or LOW State (Note 2)	− 0.5 ≤ V <sub>O</sub> ≤ 7.0	V
I <sub>IK</sub>	DC Input Diode Current V <sub>I</sub> < GND	− 50	mA
I <sub>OK</sub>	DC Output Diode Current V <sub>O</sub> < GND	− 50	mA
I <sub>O</sub>	DC Output Sink Current	± 50	mA
I <sub>CC</sub>	DC Supply Current per Supply Pin	± 100	mA
I <sub>GND</sub>	DC Ground Current per Ground Pin	± 100	mA
T <sub>STG</sub>	Storage Temperature Range	− 65 to + 150	°C
P <sub>D</sub>	Power Dissipation in Still Air SOT-353 SOT-553	186 135	mW
θ <sub>JA</sub>	Thermal Resistance SOT-353 SOT-553	350 496	°C/W
T <sub>L</sub>	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T <sub>J</sub>	Junction Temperature Under Bias	+ 150	°C
I <sub>Latch-Up</sub>	Latch-Up Performance Above V <sub>CC</sub> and Below GND at 85°C (Note 6)	± 500	mA
MSL	Moisture Sensitivity	Level 1	
F <sub>R</sub>	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
ESD	ESD Classification Human Body Model (Note 4) Machine Model (Note 5) Charged Device Model (Note 6)	Class IC Class A N/A	

1. Absolute maximum continuous ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute-maximum-rated conditions is not implied.
2. I<sub>O</sub> absolute maximum rating must be observed.
3. Tested to EIA/JESD22-A114-A, rated to EIA/JESD22-A114-B.
4. Tested to EIA/JESD22-A115-A, rated to EIA/JESD22-A115-A.
5. Tested to JESD22-C101-A.
6. Tested to EIA/JESD78.

## RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage Operating Data Retention Only	1.65 1.5	5.5 5.5	V
V <sub>I</sub>	Input Voltage	0	5.5	V
V <sub>O</sub>	Output Voltage (Z or LOW State)	0	5.5	V
T <sub>A</sub>	Operating Free-Air Temperature	− 40	+ 85	°C
Δt/ΔV	Input Transition Rise or Fall Rate V <sub>CC</sub> = 2.5 V ± 0.2 V V <sub>CC</sub> = 3.0 V ± 0.3 V V <sub>CC</sub> = 5.0 V ± 0.5 V	0 0 0	20 10 5	ns/V

## DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			−40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
V <sub>IH</sub>	High- Level Input Voltage		1.65 to 1.95 2.3 to 5.5	0.7 V <sub>CC</sub> 0.7 V <sub>CC</sub>			0.7 V <sub>CC</sub> 0.7 V <sub>CC</sub>		V
V <sub>IL</sub>	Low- Level Input Voltage		1.65 to 1.95 2.3 to 5.5			0.3 V <sub>CC</sub> 0.3 V <sub>CC</sub>		0.3V <sub>CC</sub> 0.3 V <sub>CC</sub>	V
I <sub>LKG</sub>	Z- State Output Leakage Current	V <sub>IN</sub> = V <sub>IL</sub> V <sub>OUT</sub> = V <sub>CC</sub> or GND	2.3 to 5.5			±5.0		±10.0	μA
V <sub>OL</sub>	Low- Level Output Voltage V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 100 μA	1.65 to 5.5		0.0	0.1		0.1	V
		I <sub>OL</sub> = 4 mA	1.65		0.08	0.24		0.24	
		I <sub>OL</sub> = 8 mA	2.3		0.20	0.3		0.3	
		I <sub>OL</sub> = 12 mA	2.7		0.22	0.4		0.4	
		I <sub>OL</sub> = 16 mA	3.0		0.28	0.4		0.4	
		I <sub>OL</sub> = 24 mA	3.0		0.38	0.55		0.55	
		I <sub>OL</sub> = 32 mA	4.5		0.42	0.55		0.55	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> or V <sub>OUT</sub> = V <sub>CC</sub> or GND	0 to 5.5			±0.1		±1.0	μA
I <sub>OFF</sub>	Power Off- Output Leakage Current	V <sub>OUT</sub> = 5.5 V	0			1		10	μA
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5			1		10	μA

AC ELECTRICAL CHARACTERISTICS t<sub>R</sub> = t<sub>F</sub> = 2.5 ns; C<sub>L</sub> = 50 pF; R<sub>L</sub> = 500 Ω

Symbol	Parameter	Condition	V <sub>CC</sub> (V)	T <sub>A</sub> = 25°C			−40°C ≤ T <sub>A</sub> ≤ 85°C		Unit
				Min	Typ	Max	Min	Max	
t <sub>PZL</sub>	Propagation Delay (Figure 3 and 4)	R <sub>L</sub> = R <sub>1</sub> = 500 Ω, C <sub>L</sub> = 50 pF	1.8 ± 0.15	0.8	5.3	11.6	0.8	12.0	ns
			2.5 ± 0.2	1.2	3.7	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	2.3	3.5	0.5	3.9	
t <sub>PLZ</sub>	Propagation Delay (Figure 3 and 4)	R <sub>L</sub> = R <sub>1</sub> = 500 Ω, C <sub>L</sub> = 50 pF	1.8 ± 0.15	0.8	5.3	11.6	0.8	1.20	ns
			2.5 ± 0.2	1.2	2.8	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.1	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	

## CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Condition	Typical	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	> 2.5	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 7)	10 MHz, V <sub>CC</sub> = 5.5 V, V <sub>I</sub> = 0 V or V <sub>CC</sub>	4.0	pF

7. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no-load dynamic power consumption; P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.

## NL17SZ07

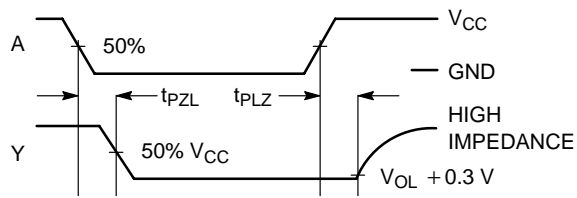


Figure 3. Switching Waveforms

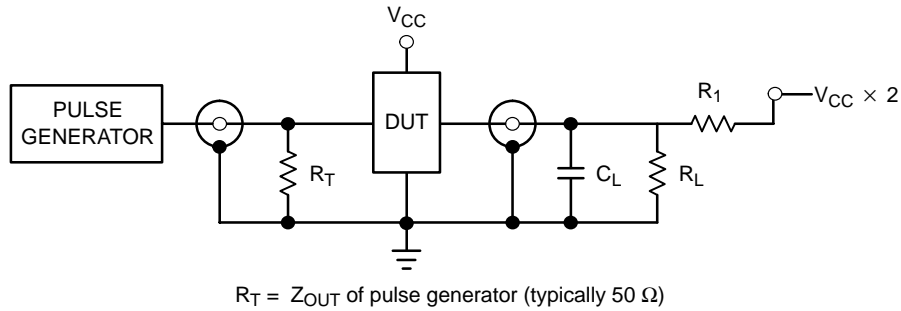


Figure 4. Test Circuit

### DEVICE ORDERING INFORMATION

Device Order Number	Device Nomenclature							Package Type	Tape and Reel Size
	Logic Circuit Indicator	No. of Gates per Package	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape & Reel Suffix		
NL17SZ07DFT2	NL	1	7	SZ	07	DF	T2	SOT-353/ SC70-5/ SC-88A/	178 mm (7") 3000 Unit
NL17SZ07XV5T2	NL	1	7	SZ	07	XV5	T2	SOT-553	178 mm (7") 4000 Unit

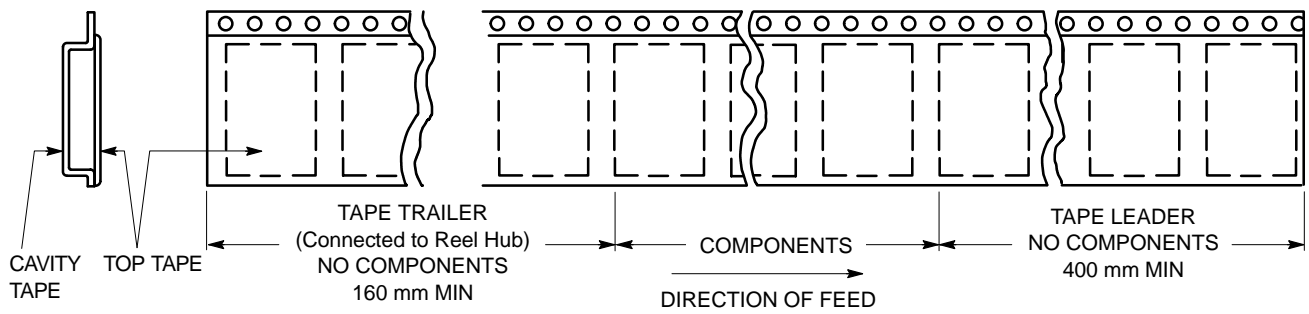
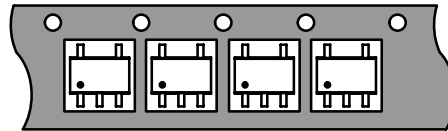
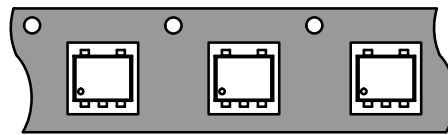


Figure 5. Tape Ends for Finished Goods



"T2" Pin One Opposing Sprocket Hole (3k Reel)

Figure 6. SOT-353/SC70-5/SC-88A Reel Configuration/Orientation



"T2" Pin One Opposing Sprocket Hole (4k Reel)

Figure 7. SOT-553 XV5T2 Reel Configuration/Orientation

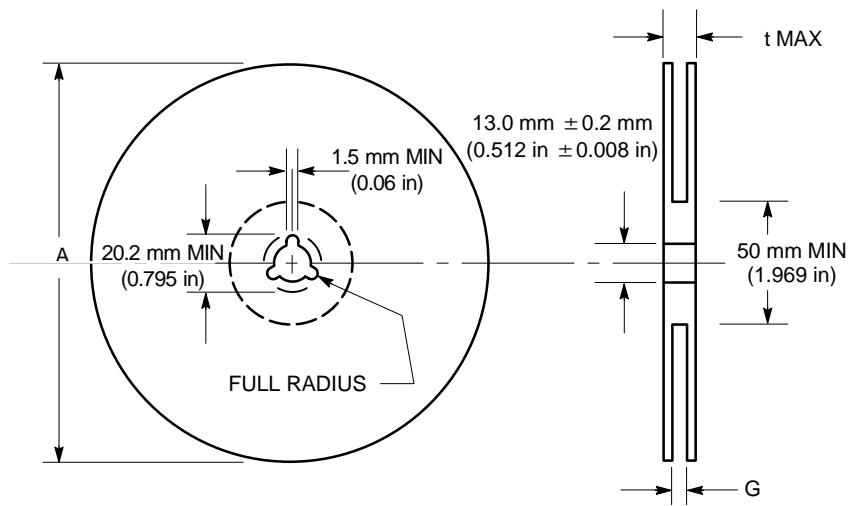


Figure 8. Reel Dimensions

REEL DIMENSIONS†

Tape Size	T and R Suffix	A Max	G	t Max
8 mm	T2	178 mm (7 in)	8.4 mm, + 1.5 mm, -0.0 (0.33 in + 0.059 in, -0.00)	14.4 mm (0.56 in)

†For additional tape and reel information, refer to Brochure BRD8011/D.

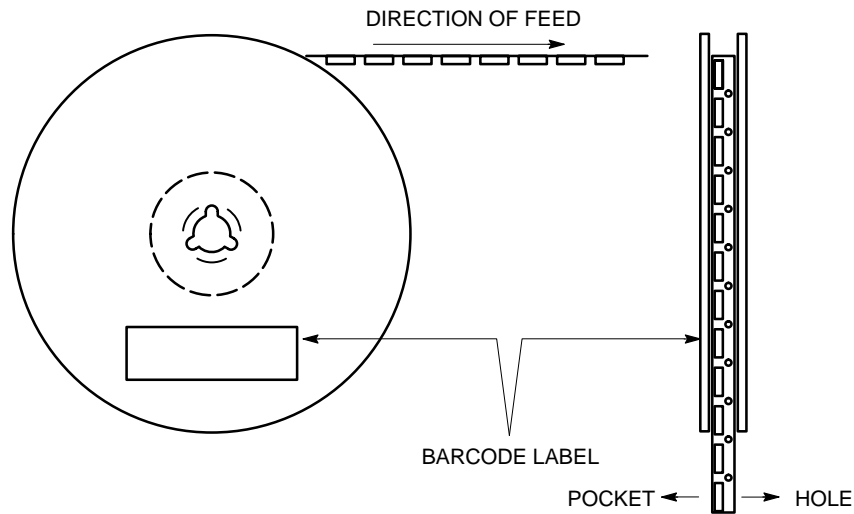
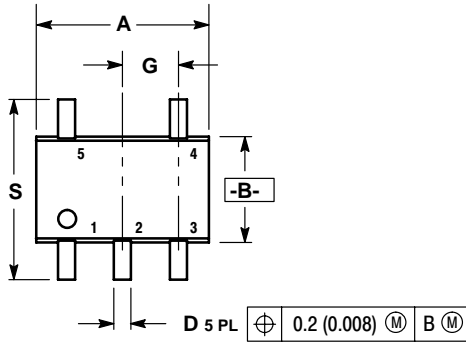


Figure 9. Reel Winding Direction

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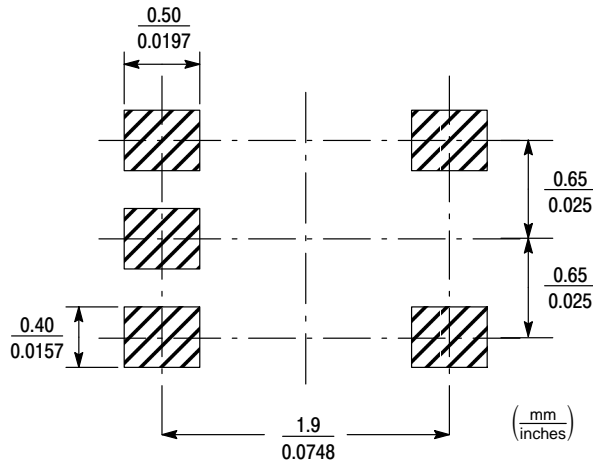
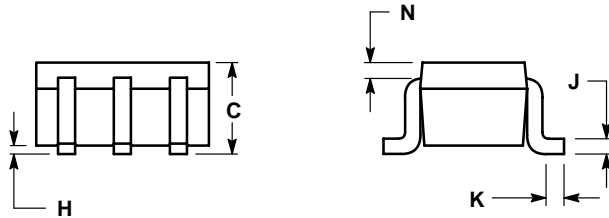
## PACKAGE DIMENSIONS

**SOT-353**  
**DF SUFFIX**  
 5-LEAD PACKAGE  
 CASE 419A-02  
 ISSUE F



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. 419A-01 OBSOLETE. NEW STANDARD 419A-02.

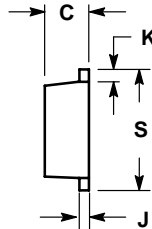
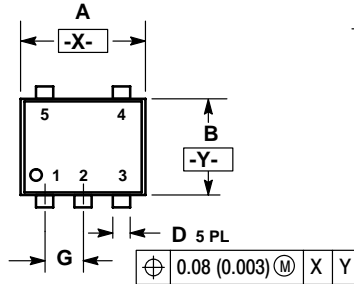
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20



# NL17SZ07

## PACKAGE DIMENSIONS

**SOT-553**  
**XV5 SUFFIX**  
**5-LEAD PACKAGE**  
**CASE 463B-01**  
**ISSUE O**



### NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL

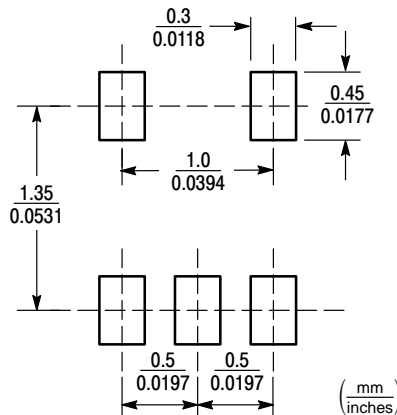
DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.50	1.70	0.059	0.067
B	1.10	1.30	0.043	0.051
C	0.50	0.60	0.020	0.024
D	0.17	0.27	0.007	0.011
G	0.50 BSC		0.020 BSC	
J	0.08	0.18	0.003	0.007
K	0.10	0.30	0.004	0.012
S	1.50	1.70	0.059	0.067


### STYLE 1:

- PIN 1. BASE 1
- EMITTER 1/2
- BASE 2
- COLLECTOR 2
- COLLECTOR 1

### STYLE 2:

- PIN 1. CATHODE
- ANODE
- CATHODE
- CATHODE
- CATHODE



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