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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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## HD74HC4518, HD74HC4520

# Dual BCD Up Counters Dual Binary Up Counters

REJ03D0653-0200 (Previous ADE-205-542) Rev.2.00 Mar 30, 2006

## **Description**

The HD74HC4518 dual BCD counter and the HD74HC4520 dual binary counter consist of two identical, independent, internally synchronous 4-stage counters. The counter stages are type D flip-flops, with interchangeable Clock and Enable lines for incrementing on either the positive-going or negative-going transition as required when cascading multiple stages. Each counter can be cleared by applying a high level on the Reset line. In addition, the HD74HC4518 will count out of all undefined states within two clock periods. These complementary MOS up counters find primary use in multi-stage synchronous or ripple counting applications requiring low power dissipation and/or high noise immunity.

#### **Features**

• High Speed Operation

• High Output Current: Fanout of 10 LSTTL Loads

• Wide Operating Voltage:  $V_{CC} = 2$  to 6 V

• Low Input Current: 1 µA max

• Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
HD74HC4518P	DILD 16 nin	PRDP0016AE-B	D		
HD74HC4520P	DILP-16 pin	(DP-16FV)			
HD74HC4518FPEL	COD 16 pin (ICITA)	PRSP0016DH-B	FP	EL (2,000 pcs/reel)	
HD74HC4520FPEL	SOP-16 pin (JEITA)	(FP-16DAV)			
HD74HC4520RPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,000 pcs/reel)	

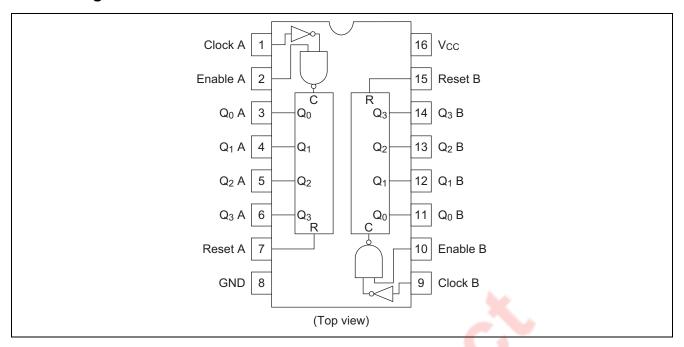
Note: Please consult the sales office for the above package availability.

#### **Function Table**

Clock	Enable	Reset	Operation
	Н	L	Increment counter
L	_	L	Increment counter
_	X	L	No change
X		L	No change
	L	L	No change
Н	_	L	No change
X	X	Н	$Q_0$ to $Q_3 = L$

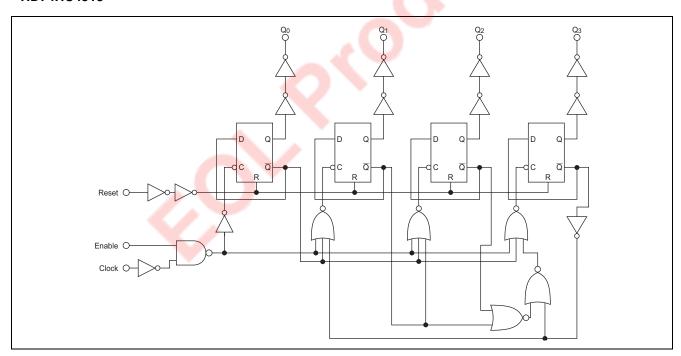
Notes: 1. X: Don't care

## **Pin Arrangement**

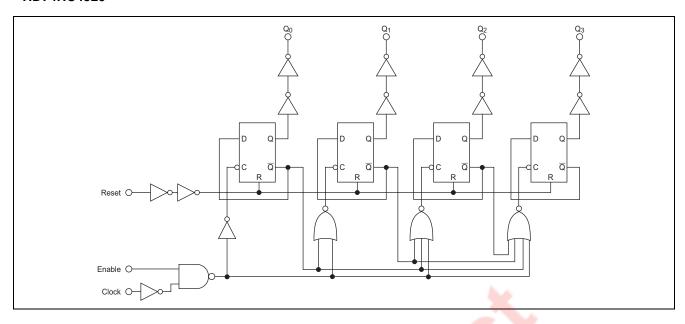


## **Block Diagram**

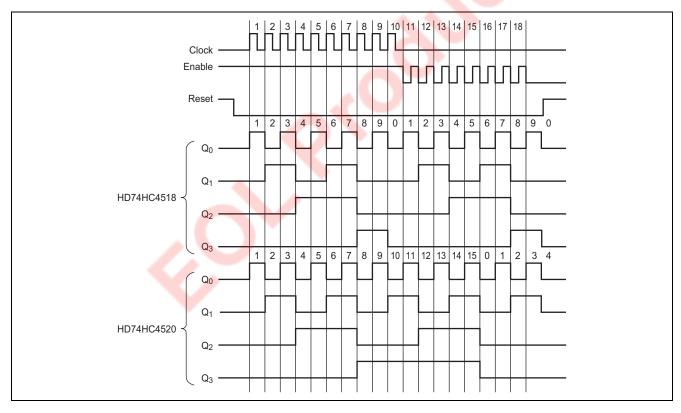
#### HD74HC4518



#### HD74HC4520



## **Timing Diagram**



## **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
Input / Output voltage	$V_{IN}, V_{OUT}$	-0.5 to V <sub>CC</sub> +0.5	V
Input / Output diode current	I <sub>IK</sub> , I <sub>OK</sub>	±20	mA
Output current	I <sub>OUT</sub>	±25	mA
V <sub>CC</sub> , GND current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA
Power dissipation	P <sub>T</sub>	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## **Recommended Operating Conditions**

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V <sub>CC</sub>	2 to 6	V	
Input / Output voltage	V <sub>IN</sub> , V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t <sub>r</sub> , t <sub>f</sub>	0 to 1000	ns	V <sub>CC</sub> = 2.0 V
		0 to 500		$V_{CC} = 4.5 \text{ V}$
		0 to 400		V <sub>CC</sub> = 6.0 V

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

## **Electrical Characteristics**

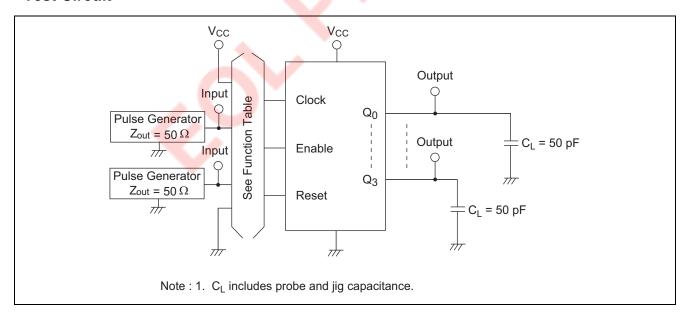
			Ta = 25°C		$Ta = -40 \text{ to} +85^{\circ}\text{C}$					
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	V <sub>IH</sub>	2.0	1.5	-/	) <del>}</del> `	1.5	_	V		
		4.5	3.15	1		3.15	_			
		6.0	4.2		d	4.2	_			
	$V_{IL}$	2.0	À	_	0.5	_	0.5	V		
		4.5	J		1.35	_	1.35			
		6.0	ĺ		1.8	_	1.8			
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5		4.4	_			
		6.0	5.9	6.0	_	5.9	_			
		4.5	4.18		_	4.13	_			$I_{OH} = -4 \text{ mA}$
		6.0	5.68	_	_	5.63	_			$I_{OH} = -5.2 \text{ mA}$
	$V_{OL}$	2.0		0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \mu A$
		4.5		0.0	0.1	_	0.1			
		6.0		0.0	0.1	_	0.1			
		4.5			0.26	_	0.33			$I_{OH} = 4 \text{ mA}$
		6.0			0.26	_	0.33			$I_{OH} = 5.2 \text{ mA}$
Input current	lin	6.0			±0.1	_	±1.0	μΑ	$Vin = V_{CC} \text{ or } GN$	ID
Quiescent supply current	I <sub>CC</sub>	6.0	_	_	4.0	_	40	μА	$Vin = V_{CC} \text{ or } GN$	ID, lout = 0 μA

## **Switching Characteristics**

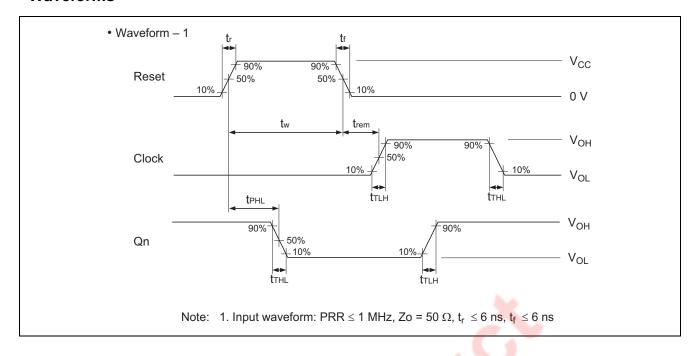
( $C_L = 50$  pF, Input  $t_r = t_f = 6$  ns)

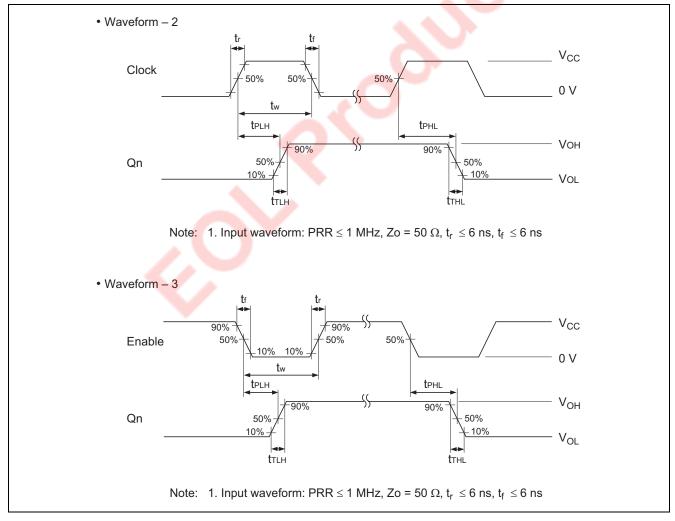
			Т	a = 25°	С	Ta = -40 to +85°C				
Item	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions	
Maximum clock	f <sub>max</sub>	2.0	_	_	5	_	4	MHz		
frequency		4.5	_	_	27	_	21			
		6.0	_	_	32	_	25			
Propagation delay time	t <sub>PLH</sub>	2.0	_	_	190	_	240	ns	Clock to Q	
	t <sub>PHL</sub>	4.5	_	22	38	_	48			
		6.0	_	_	32	_	41			
	t <sub>PLH</sub>	2.0	_	_	200	_	250	ns	Reset to Q	
	t <sub>PHL</sub>	4.5	_	22	40	_	50			
		6.0	_	_	34	_	43			
	t <sub>PLH</sub>	2.0	_	_	190	_	240	ns	Enable to Q	
	t <sub>PHL</sub>	4.5	_	17	38	_	48			
		6.0	_	_	32	_	41			
Pulse width	t <sub>w</sub>	2.0	80	_	_	100	_	ns		
		4.5	16	7	_	20	_	X		
		6.0	14	_	_	17	_	b		
Removal time	t <sub>rem</sub>	2.0	0	_	_	0		ns	Reset clock	
		4.5	0	-3	_	0	7			
		6.0	0	_	_	0				
Output rise/fall time	t <sub>TLH</sub>	2.0	_	_	75	-	95	ns		
	t <sub>THL</sub>	4.5	_	_	15	-	19			
		6.0	_	_	13		16			
Input capacitance	Cin	_	_	5	10		10	pF		

## **Test Circuit**

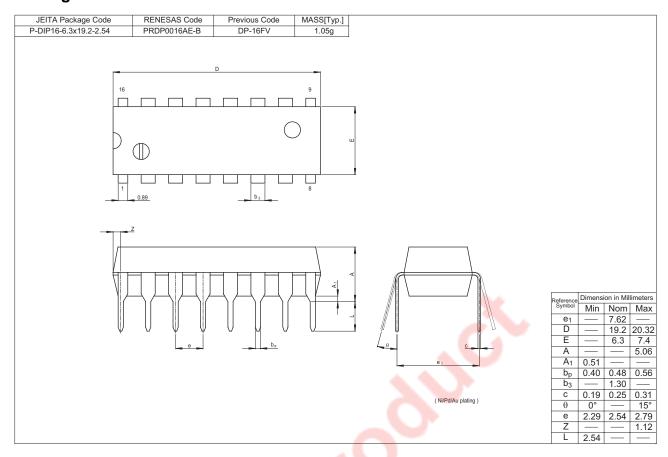


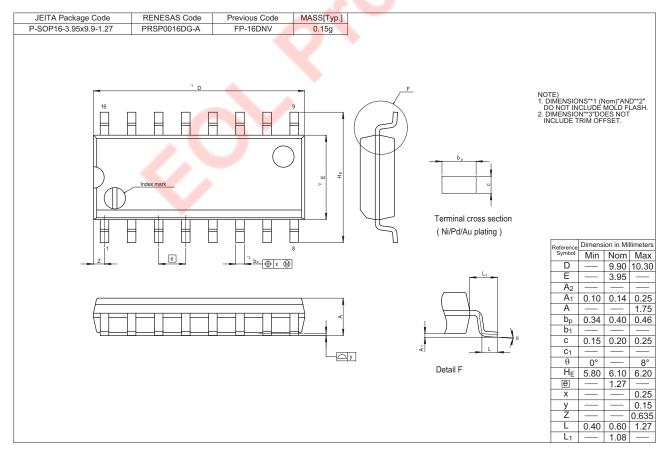
## **Waveforms**

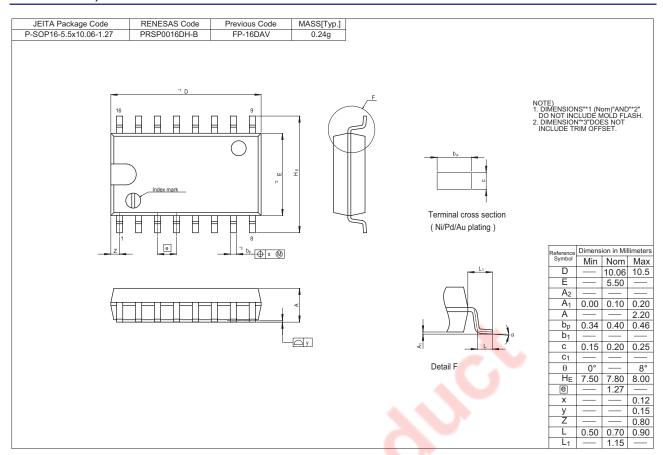




## **Package Dimensions**







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