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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# **HD74LS368A**

Hex Bus Drivers (inverted data outputs with three-state outputs)

REJ03D0481-0200 Rev.2.00 Feb.18.2005

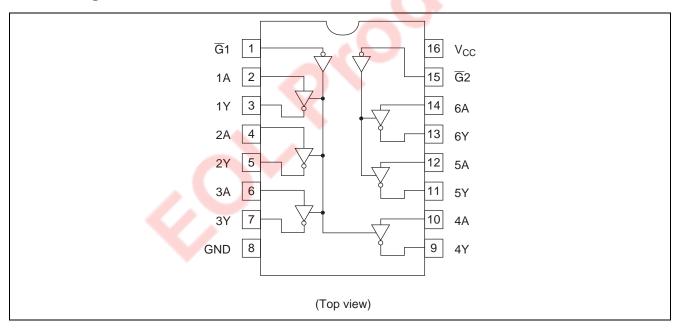
#### **Features**

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS368AP	DILP-16 pin	PRDP0016AE-B (DP-16FV)	Р	_
HD74LS368AFPEL	SOP-16 pin (JEITA)	PRSP0016DH-B (FP-16DAV)	FP	EL (2,000 pcs/reel)
HD74LS368ARPEL	SOP-16 pin (JEDEC)	PRSP0016DG-A (FP-16DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

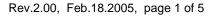
#### **Pin Arrangement**



#### **Function Table**

G	Α	Y
Н	X	Z
L	L	Н
L	Н	L

Note: H; high level, L; low level, X; irrelevant, Z; off (high-impedance) state of a 3-state output



### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit
Supply voltage	V <sub>CC</sub>	7	V
Input voltage	V <sub>IN</sub>	7	V
Output voltage (off-state)	V <sub>O (off)</sub>	5.5	V
Power dissipation	P <sub>T</sub>	400	mW
Operating temperature	Topr	-20 to +75	°C
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

#### **Recommended Operating Conditions**

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	$V_{CC}$	4.75	5.00	5.25	V
Output current	Іон	_	_	-2.6	mA
Output current	I <sub>OL</sub>	_	_	24	mA
Operating temperature	Topr	-20	25	75	°C

#### **Electrical Characteristics**

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$ 

Item		Symbol	min.	typ.*	max.	Unit	Condition	
Input voltage		V <sub>IH</sub>	2.0	_	_	V		
Input voltage		$V_{IL}$		_	0.8	V		
		V <sub>OH</sub>	2.4	_		V	$V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V},$ $I_{OH} = -2.6 \text{ mA}$	
Output voltage		V <sub>OL</sub>		- 1	0.4	V	$I_{OL} = 12 \text{ mA}$ $V_{CC} = 4.75 \text{ V},$	
		V OL			0.5		$I_{OL} = 24 \text{ mA}$ $V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V}$	
Output current		l <sub>ozh</sub>		}	20	^	$V_{O} = 2.4 \text{ V}$ $V_{CC} = 5.25 \text{ V},$	
Output current		I <sub>OZL</sub>	_	<b>—</b>	-20	μΑ	$V_0 = 0.4 \text{ V}$ $V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V}$	
		I <sub>IH</sub>	_	-	20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 2.7 \text{ V}$	
	A inputs			· —	-20	μΑ	$V_{CC} = 5.25 \text{ V}, V_I = 0.5 \text{ V},$ $\overline{G}$ input at 2 V	
Input current		I <sub>IL</sub>		_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V},$ $\overline{G}$ inputs at 0.4 V	
	G inputs		_	_	-0.4	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$	
		li li	_	_	0.1	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$	
Short-circuit output current		los	-40	_	-225	mA	V <sub>CC</sub> = 5.25 V	
Supply current**		Icc	_	12	21	mA	V <sub>CC</sub> = 5.25 V	
Input clamp voltage		V <sub>IK</sub>	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$	

Notes:  $V_{CC} = 5 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ 



<sup>\*\*</sup> With all outputs open,  $I_{CC}$  is measured with all inputs grounded and all  $\overline{G}$  inputs at 4.5 V.

### **Switching Characteristics**

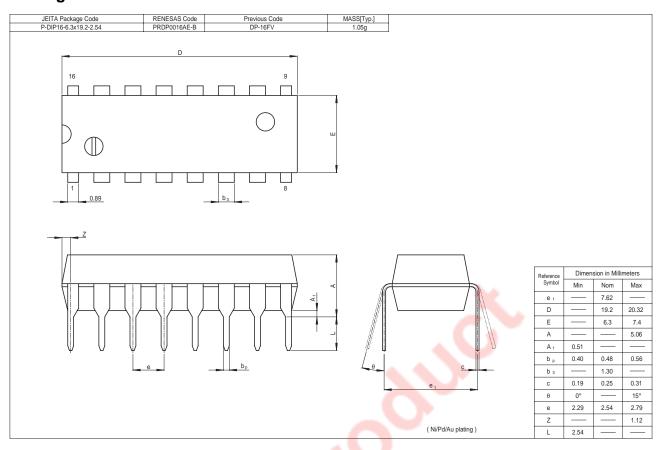
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$ 

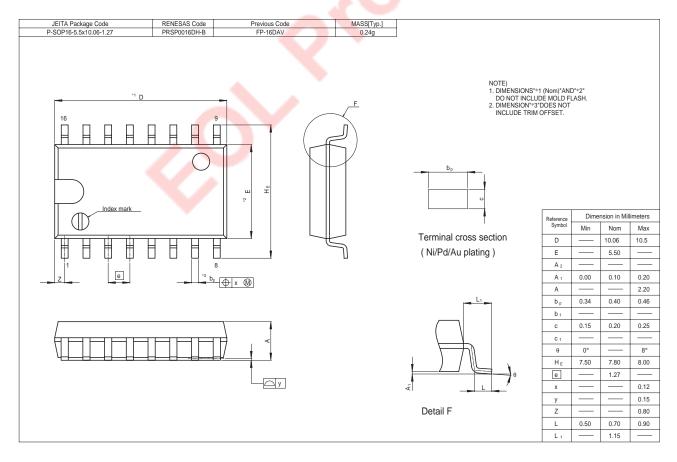
Item	Symbol	min.	typ.	max.	Unit	Condition	
Drana vation daloutina	t <sub>PLH</sub>	_	7	15		C 45 p 667.0	
Propagation delay time	t <sub>PHL</sub>	_	12	18			
Outrout analyte time	t <sub>zH</sub>	_	18	35	20	$C_L = 45 \text{ pF}, R_L = 667 \Omega$	
Output enable time	$t_{ZL}$	_	28	45	ns		
Output disable time	t <sub>HZ</sub>	_	_	32		$C_L = 5 \text{ pF}, R_L = 667 \Omega$	
Output disable time	$t_{LZ}$	_	_	35		$C_L = 5 \text{ pr}, R_L = 607.22$	

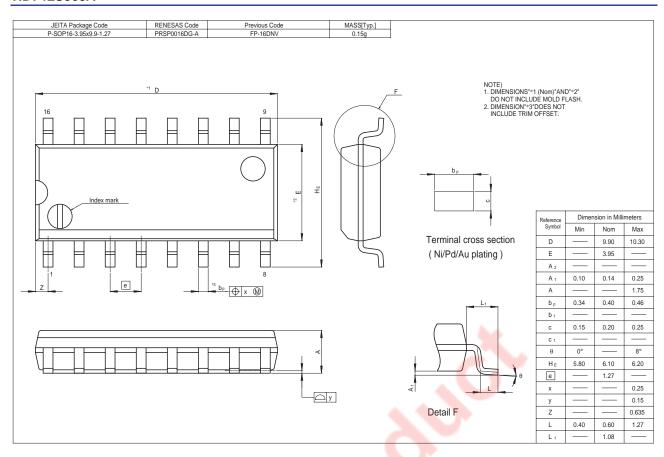
Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".



#### **Package Dimensions**







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