

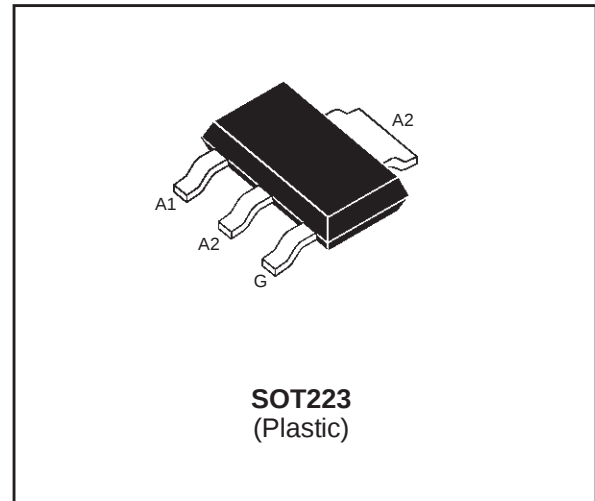
SENSITIVE GATE TRIACS

FEATURES

- $I_{T(RMS)} = 1A$
- $V_{DRM} = 400V$ to $800V$
- $I_{GT} \leq 3mA$ to $\leq 25mA$

DESCRIPTION

The Z01xxxN series of triacs uses a high performance TOP GLASS PNP technology. These parts are intended for general purpose high volume applications using surface mount technology.



ABSOLUTE RATINGS (limiting values)

Symbol	Parameter		Value	Unit
$I_{T(RMS)}$	RMS on-state current (360° conduction angle)	$T_{tab} = 90^{\circ}C$	1	A
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = $25^{\circ}C$)	$t_p = 8.3 ms$	8.5	A
		$t_p = 10 ms$	8	
I^2t	I^2t Value for fusing	$t_p = 10 ms$	0.35	A^2s
di/dt	Critical rate of rise of on-state current $I_G = 50 mA$ $di_G/dt = 0.1 A/\mu s$.	Repetitive $F = 50 Hz$	10	$A/\mu s$
		Non Repetitive	50	
T_{stg} T_j	Storage and operating junction temperature range		- 40, + 150 - 40, + 125	$^{\circ}C$
TI	Maximum lead temperature for soldering during 10s		260	$^{\circ}C$

Symbol	Parameter	Voltage				Unit
		D	M	S	N	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage $T_j = 125^{\circ}C$	400	600	700	800	V

Z01xxxN**THERMAL RESISTANCES**

Symbol	Parameter	Value	Unit
Rth(j-a)	Junction to ambient	60	°C/W
Rth(j-t)	Junction to leads for D.C	30	°C/W
Rth(j-t)	Junction to leads for A.C 360°conduction angle (F=50Hz)	25	°C/W

GATE CHARACTERISTICS (maximum values)

$P_G (AV) = 0.1 \text{ W}$ $P_{GM} = 2 \text{ W}$ ($t_p = 20 \mu\text{s}$) $I_{GM} = 1 \text{ A}$ ($t_p = 20 \mu\text{s}$)

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions		Quadrant		Sensitivity				Unit
					03	07	09	10	
I _{GT}	V _D =12V (DC) R _L =140Ω	T _j = 25°C	I-II-III	MAX	3	5	10	25	mA
			IV	MAX	5	7	10	25	
V _{GT}	V _D =12V (DC) R _L =140Ω	T _j = 25°C	I-II-III-IV	MAX	1.5				V
V _{GD}	V _D =V _{DRM} R _L =3.3kΩ	T _j = 125°C	I-II-III-IV	MIN	0.2				V
tgt	V _D =V _{DRM} I _G = 40mA I _T = 1.4A dI _G /dt = 0.5A/μs	T _j = 25°C	I-II-III-IV	TYP	2				μs
I _H *	I _T = 50 mA Gate open	T _j = 25°C		MAX	7	10	10	25	mA
I _L	I _G = 1.2 I _{GT}	T _j = 25°C	I-III-IV	TYP	7	10	10	25	
			II	TYP	14	20	20	50	
V _{TM} *	I _{TM} = 1.4A tp=380μs	T _j = 25°C		MAX	1.8				V
I _{DRM} I _{RRM}	V _D = V _{DRM} V _R = V _{RRM}	T _j = 25°C		MAX	10				μA
		T _j = 110°C		MAX	200				
dV/dt *	VD=67%V _{DRM} Gate open	T _j = 110°C		MIN	10	20	50	100	V/μs
(dV/dt)c *	(dI/dt)c = 0.44 A/ms	T _j = 110°C		MIN			2	5	V/μs
				TYP	1	1			

* For either polarity of electrode A₂ voltage with reference to electrode A₁

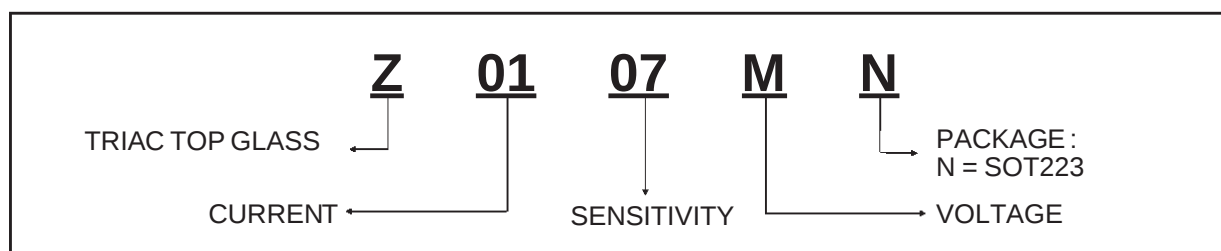
ORDERING INFORMATION

Fig.1 : Maximum power dissipation versus RMS on-state current.

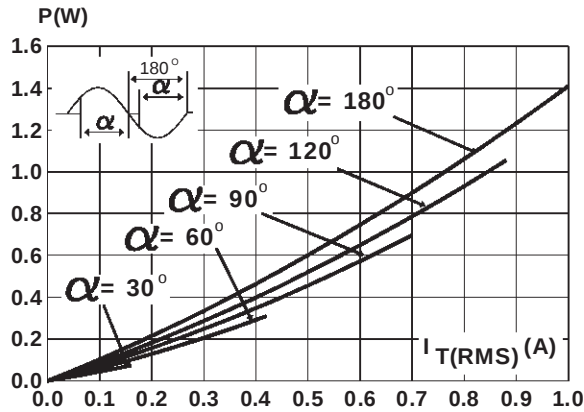


Fig.2 : Correlation between maximum power dissipation and maximum allowable temperature (T_{amb} and T_{tab}).

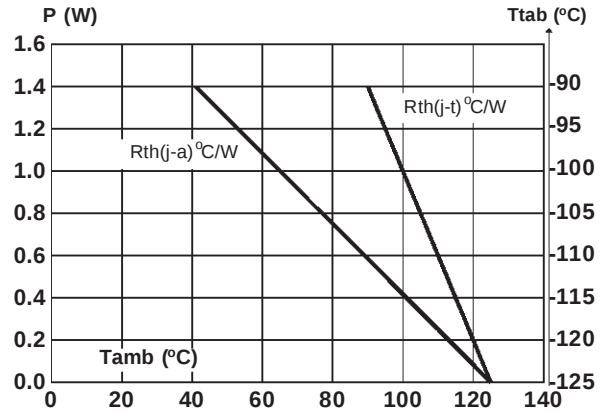


Fig.3 : RMS on-state current versus tab temperature.

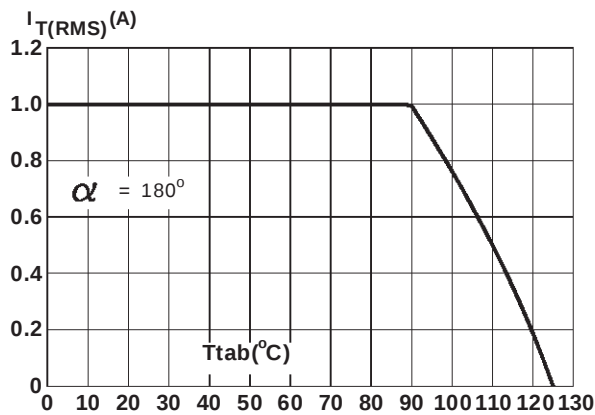


Fig.4 : Relative variation of thermal impedance junction to ambient versus pulse duration.

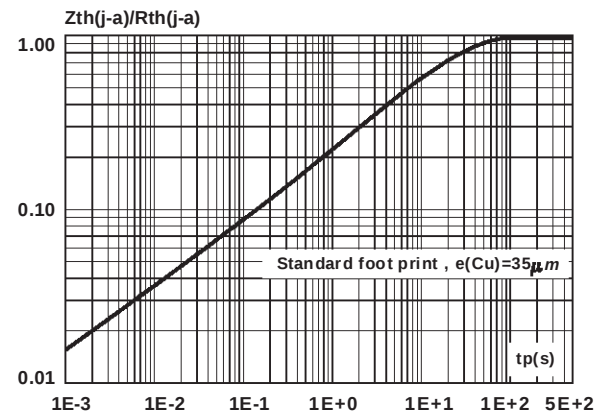


Fig.5 : Relative variation of gate trigger current and holding current versus junction temperature.

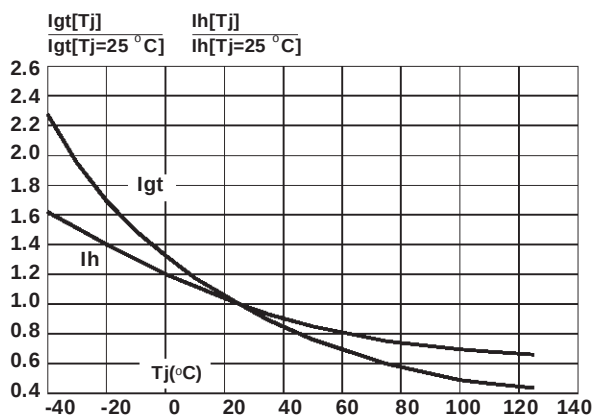


Fig.6 : Non repetitive surge peak on-state current versus number of cycles.

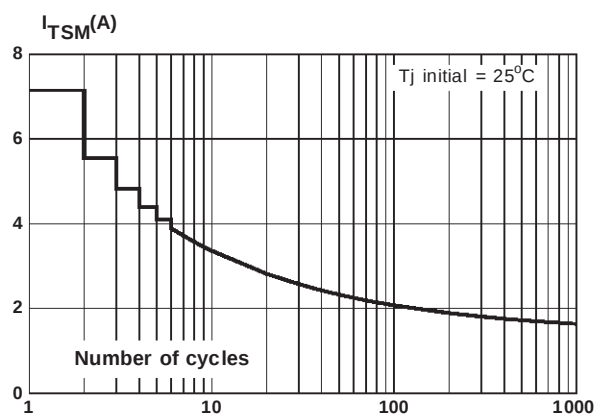


Fig.7 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t_p \leq 10\text{ms}$, and corresponding value of I^2t .

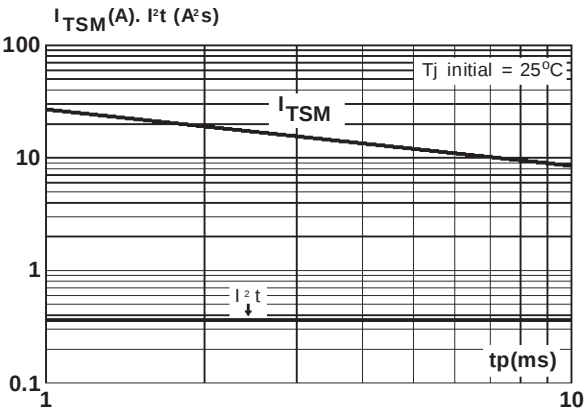
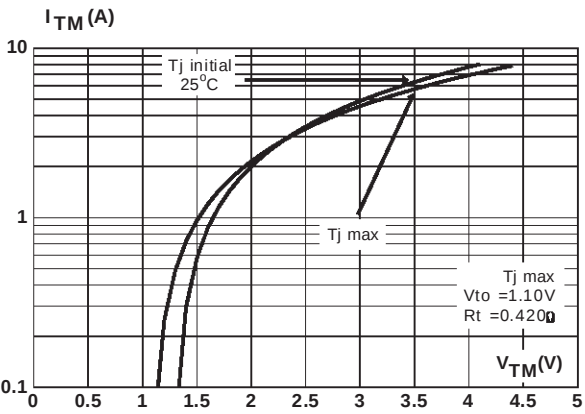
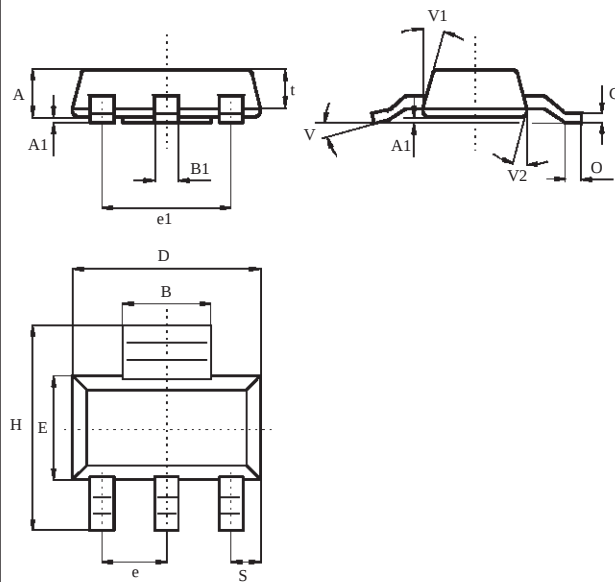


Fig.8 : On-state characteristics(maximum values).



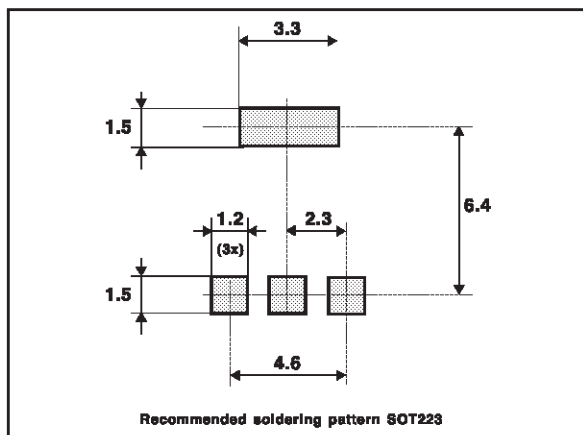
PACKAGE MECHANICAL DATA
SOT223 (Plastic)



REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.50		1.70	0.059		0.067
A1	0.02		0.10	0.001		0.004
B	2.95		3.15	0.090		0.124
B1	0.65		0.85	0.026		0.033
C	0.25		0.35	0.010		0.014
D	6.30		6.70	0.248		0.264
e		2.3			0.091	
e1		4.6			0.181	
E	3.30		3.70	0.130		0.146
H	6.70		7.30	0.264		0.287
O	0.63	0.65	0.67	0.025	0.026	0.026
S	0.85		1.05	0.033		0.041
t	1.10		1.30	0.043		0.051
V	10° max					
V1	10° min 16°max					
V2	10° min 16°max					

Weight : 0.11 g

FOOT PRINT



Z01xxxN

MARKING

Type	Marking
Z0103DN	Z3D
Z0103MN	Z3M
Z0103SN	Z3S
Z0103NN	Z3N
Z0107DN	Z7D
Z0107MN	Z7M
Z0107SN	Z7S
Z0107NN	Z7N
Z0109DN	Z9D
Z0109MN	Z9M
Z0109SN	Z9S
Z0109NN	Z9N
Z0110DN	Z0D
Z0110MN	Z0M
Z0110SN	Z0S
Z0110NN	Z0N

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