Benchmarq Products

bq24400

Features

- Safe management of fast charge for NiCd and NiMH battery packs.
- High-frequency switching controller for efficient and simple charger design
- Pre-charge qualification for detecting shorted, damaged, or overheated cells
- Fast-charge termination by peak voltahge (PVD), maximum temperature, and maximum charge time
- Selectable top-off mode for achieving maximum capacity in NiMH batteries
- Programmable trickle-charge mode for reviving deeply discharged batteries and for postcharge maintenance
- Built-in battery removal and insertion detection
- Sleep mode for low power consumption

Pin Connections



SLUS498 -SEPTEMBER 2001

Programmable NiCd/NiMH Fast-Charge Management Device

General Description

The bq24400 is a programmable, monolithic IC for fast-charge management of nickel cadmium (NiCd) and nickel metal-hydride (NiMH) batteries.

The bq24400 provides a number of charge termination criteria:

- Peak voltage, PVD (for NiCd and NiMH)
- Maximum temperature
- Maximum charge time

For safety, the bq24400 inhibits fast charge until the battery voltage and temperature are within user-defined limits. If the battery voltage is below the low-voltage threshold, the bq24400 uses trickle-charge to condition the battery. For NiMH batteries, the bq24400 provides an optional top-off charge to maximize the battery capacity.

The integrated high-frequency comparator allows the bq24400 to be the basis for a complete, high-efficiency power-conversion circuit.

Pin Names

SNS	Current-sense input	TS	Temperature-sense input
$\overline{\text{V}_{\text{SS}}}$	System ground Charge-status	RC	Timer-program input
	output	V _{CC}	Supply-voltage input
BAT	Battery-voltage input	MOD	Modulation-control output

Pin Descriptions

SNS Current-sense input

Enables the bq24400 to sense the battery current via the voltage developed on this pin by an external sense-resistor connected in series with the battery pack.

Vss System Ground

LED Charge-status output

Open-drain output that indicates the charging status by turning on, turning off, or flashing an external LED.

BAT Battery-voltage input

Battery-voltage sense input. A simple resistive divider, across the battery terminals, generates this input.

TS Temperature-sense input

Input for an external battery-temperature monitoring circuit. An external resistive divider network with a negative temperature-coefficient thermistor sets the lower and upper temperature thresholds.

RC Timer-program input

RC input used to program the maximum charge-time, hold-off period, and trickle rate during the charge cycle, and to disable or enable top-off charge.

V_{CC} Supply-voltage input

MOD Modulation-control output

Push-pull output that controls the charging current to the battery. MOD switches high to enable charging current to flow and low to inhibit charging- current flow.

Functional Description

The bq24400 is a versatile, NiCd, NiMH battery- charge control device. See Figure 1 for a functional block diagram and Figure 2 for the state diagram.



Figure 1. Functional Block Diagram



Figure 2. State Diagram

Initiation and Charge Qualification

The bq24400 initiates a charge cycle when it detects

- Application of power to $V_{CC} \label{eq:VCC}$
- Battery replacement
- Exit from sleep mode

Immediately following initiation, the IC enters a charge-qualification mode. The bq24400 charge qualification is based on battery voltage and temperature. If voltage on pin BAT is less than the internal threshold, V_{LBAT} , the bq24400 enters the charge-pending state. This condition indicates the possibility of a defective or shorted battery pack. In an attempt to revive a fully depleted pack, the bq24400 enables the MOD pin to trickle-charge at a rate of once every 1.0s. As explained in the section "Top-Off and Pulse-Trickle Charge," the trickle pulse-width is user-selectable and is set by the value of the resistance connected to pin RC.

During this period, the LED pin blinks at a 1Hz rate, indicating the pending status of the charger.

Similarly, the bq24400 suspends fast charge if the battery temperature is outside the $V_{\rm LTF}$ to $V_{\rm HTF}$ range. (See Table 4.) For safety reasons, however, it disables the pulse trickle, in the case of a battery over-temperature condition (i.e., $V_{\rm TS}$ < $V_{\rm HTF}$). Fast charge begins when the battery temperature and voltage are valid.

NiCd and NiMH Batteries

Following qualification, the bq24400 fast-charges NiCd or NiMH batteries using a current-limited algorithm. During the fast-charge period, it monitors charge time, temperature, and voltage for adherence to the termination criteria. This monitoring is further explained in later sections. Following fast charge, the battery is topped off, if top-off is selected. The charging cycle ends with a trickle maintenance-charge that continues as long as the voltage on pin BAT remains below V_{MCV} .

Charge Termination Maximum Charge Time

The bq24400 sets the maximum charge-time through pin RC. With the proper selection of external resistor and capacitor, various time-out values may be achieved. Figure 3 shows a typical connection.

The following equation shows the relationship between the R_{MTO} and C_{MTO} values and the maximum charge time (MTO) for the bq24400:

$$MTO = R_{MTO} * C_{MTO} * 35,988$$

MTO is measured in minutes, R_{MTO} in ohms, and C_{MTO} in farads. (**Note:** R_{MTO} and C_{MTO} values also determine other features of the device. See Tables 2 and 3 for details.)

Maximum Temperature

A negative-coefficient thermistor, referenced to V_{SS} and placed in thermal contact with the battery, may be used as a temperature-sensing device. Figure 4 shows a typical temperature-sensing circuit.

During fast charge, the bq24400 compares the battery temperature to an internal high-temperature cutoff threshold, V_{TCO} . As shown in Table 4, high-temperature termination occurs when voltage at pin TS is less than this threshold.

Peak Voltage

The bq24400 uses a peak-voltage detection (PVD) scheme to terminate fast charge for NiCd and NiMH batteries. The bq24400 continuously samples the voltage on the BAT pin, representing the battery voltage, and triggers the peak detection feature if this value falls below the maximum sampled value by as much as 3.8 mV (PVD). As shown in figure 5, a resistor voltage-divider between the battery pack's positive terminal and Vss scales the battery voltage measured at pin BAT.

The resistor values R_{B1} And R_{B2} are calculated by the following equation:

Battery Chemistry	Charge Algorithm
NiCd or NiMH	1. Charge qualification 2. Trickle charge, if required 3. Fast charge (constant current) 4. Charge termination $(\Delta T/\Delta t, time)$ 5. Top-off (optional) 6. Trickle charge

Table 1. Charge Algorithm



Figure 3. Typical Connection for the RC Input



Figure 4. Temperature Monitoring Configuration



Figure 5. Battery Voltage Divider

Parameter	Value
Maximum cell voltage (V _{MCV})	2V
Minimum pre-charge qualification voltage (V _{LBAT})	950mV
High-temperature cutoff voltage (V_{TCO})	$0.225 * V_{CC}$
High-temperature fault voltage (V _{HTF})	$0.25 * V_{CC}$
Low-temperature fault voltage $\left(V_{LTF}\right)$	$0.5 * V_{CC}$
bq24400 fast-charge maximum time out (MTO)	$R_{MTO} * C_{MTO} * 35,988$
Fast-charge charging current (I_{MAX})	$0.05/\mathrm{R}_\mathrm{SNS}$
Hold-off period	MTO/32
Top-off charging current (optional)	$I_{MAX}/16$
Top-off period (optional)	МТО
Trickle-charge frequency	1Hz

Table 3. Temperature-Monitoring Conditions

Temperature	Condition	Action
$V_{\rm TS} > V_{\rm LTF}$	Cold battery—checked at all times	Suspends fast charge or top-off and timer Allows trickle charge—LED flashes at 1Hz rate during pre-charge qualification and fast charge
$V_{\rm HTF} < V_{\rm TS} < V_{\rm LTF}$	Optimal operating range	Allows charging
$V_{\rm TS} < V_{\rm HTF}$	Hot battery—checked during charge qualifi- cation and top-off and trickle-charge	Suspends fast-charge initiation, does not allow trickle charge—LED flashes at 1Hz rate during pre-charge qualification
$V_{\rm TS} < V_{\rm TCO}$	Battery exceeding maximum allowable tem- perature—checked at all times	Terminates fast charge or top-off

$$\frac{R_{_{B1}}}{R_{_{B2}}} = N-1$$

where N is the number of cells in series.

The end-to-end input impedance of this resistive divider network should be at least $200k\Omega$ and no more than $1M\Omega.$

Initial Hold-Off Period

The values of the external resistor and capacitor connected to pin RC set the initial hold-off period. During this period, the bq24400 avoids early termination by disabling the $\Delta T/\Delta t$ feature. This period is fixed at the programmed value of the maximum charge time divided by 32.

hold-off period =
$$\frac{\text{maximum time - out}}{32}$$

Table 4. Charge Status Display

Charge Action State	LEDStatus
Battery absent	High impedance
Pre-charge qualification	1Hz flash
Trickle charge (before fast charge)	1Hz flash
Fast charging	Low
Top-off or trickle	High impedance
Charge complete	High impedance
Sleep mode	High impedance
Charge suspended $(V_{TS} > V_{LTF})$	1Hz flash

Top-Off and Pulse-Trickle Charge

Top-off may be desirable on batteries that have a tendency to terminate charge before reaching full capacity. To enable this option, the capacitance value of $C_{\rm MTO}$ connected to pin RC (Figure 3) should be greater than $0.13\mu F$, and the value of the resistor connected to this pin should be less than $15k\Omega$. To disable top-off, the capacitance value should be less than $0.07\mu F$. The tolerance of the capacitor needs to be taken into account in component selection.

Once enabled, the top-off is performed over a period equal to the maximum charge time at a rate of $\frac{1}{16}$ that of fast charge.

Following top-off, the bq24400 trickle-charges the battery by enabling the MOD to charge at a rate of once every 1.0 second. The trickle pulse-width is user-selectable and is set by the value of the resistor R_{MTO} , which is on pin RC. Figure 6 shows the relationship between the trickle pulse-width and the value of R_{MTO} . The typical tolerance of the pulsewidth below 150k Ω is ±10%.

During top-off and trickle-charge, the bq24400 monitors battery voltage and temperature. These functions are suspended if the battery voltage rises above the maximum cell voltage (V_{MCV}) or if the temperature exceeds the high-temperature fault threshold (V_{HTF}).

Charge Current Control

The bq24400 controls the charge current through the MOD output pin. The current-control circuit supports a switching-current regulator with frequencies up to 500kHz. The bq24400 monitors charge current at the SNS input by the voltage drop across a sense-resistor, $R_{\rm SNS}$, in series with the battery pack. See Figure 8 for a typical current-sensing circuit. $R_{\rm SNS}$ is sized to provide the desired fast-charge current ($I_{\rm MAX}$):

$$I_{MAX} = \frac{0.05}{R_{SNS}}$$

If the voltage at the SNS pin is greater than $V_{\rm SNSLO}$ or less than $V_{\rm SNSHI}$, the bq24400 switches the MOD output high to pass charge current to the battery. When the SNS voltage is less than $V_{\rm SNSLO}$ or greater than $V_{\rm SNSHI}$, the bq24400 switches the MOD output low to shut off charging current to the battery. Figure 7 shows a typical multi-chemistry charge circuit.



Figure 6. Relationship Between Trickle Pulse-Width and Value of 8

bq24400



Figure 7. Three-Cell NiCd/NiMH 1A Charger

Temperature Monitoring

The bq24400 measures the temperature by the voltage at the TS pin. This voltage is typically generated by a negative-temperature-coefficient thermistor. The bq24400 compares this voltage against its internal threshold voltages to determine if charging is safe. These thresholds are the following:

- High-temperature cutoff voltage: $V_{TCO} = 0.225 * V_{CC}$ This voltage corresponds to the maximum temperature (TCO) at which fast charging is allowed. The bq24400 terminates fast charge if the voltage on pin TS falls below V_{TCO} .
- High-temperature fault voltage: $V_{HTF}=0.25\ * V_{CC}$ This voltage corresponds to the temperature (HTF) at which fast charging is allowed to begin.
- Low-temperature fault voltage: $V_{\rm LTF}=0.5*V_{\rm CC}$ This voltage corresponds to the minimum temperature (LTF) at which fast charging or top-off is allowed. If the voltage on pin TS rises above $V_{\rm LTF}$, the bq24400 suspends fast charge or top-off but does not terminate charge. When the voltage falls back below $V_{\rm LTF}$, fast charge or top-off resumes from the point where suspended. Trickle-charge is allowed during this condition.

Table 3 summarizes these various conditions.

Charge Status Display

The charge status is indicated by open-drain output LED. Table 4 summarizes the display output of the bg24400.

Sleep Mode

The bq24400 features a sleep mode for low power consumption. This mode is enabled when the voltage at pin BAT is above the low-power-mode threshold, $V_{\rm SLP}$. During sleep mode, the bq24400 shuts down all internal circuits, drives the LED output to high-impedance state, and drives pin MOD to low. Restoring BAT below the $V_{\rm MCV}$ threshold initiates the IC and starts a fast-charge cycle.



Figure 8. Current-Sensing Circuit

Symbol	Parameter	Minimum	Maximum	Unit	Notes
V _{CC}	$V_{\rm CC}$ relative to $V_{\rm SS}$	-0.3	+7.0	V	
VT	DC voltage applied on any pin, excluding V_{CC} relative to V_{SS}	-0.3	+7.0	v	
TOPR	Operating ambient temperature	-20	+70	°C	
T _{STG}	Storage temperature	-40	+125	°C	
TSOLDER	Soldering temperature	-	+260	°C	10s max.

Absolute Maximum Ratings

Note: Permanent device damage may occur if Absolute Maximum Ratings are exceeded. Functional operation should be limited to the Recommended DC Operating Conditions detailed in this data sheet. Exposure to conditions beyond the operational limits for extended periods of time may affect device reliability.

Symbol	Parameter	Rating	Tolerance	Unit	Notes
V _{TCO}	Temperature cutoff	$0.225 * V_{CC}$	$\pm 5\%$	V	Voltage at pin TS
V _{HTF}	High-temperature fault	$0.25 * V_{CC}$	$\pm 5\%$	V	Voltage at pin TS
V _{LTF}	Low-temperature fault	$0.5 * V_{CC}$	$\pm 5\%$	V	Voltage at pin TS
V _{MCV}	Maximum cell voltage	2.00	$\pm 2.5\%$	v	V _{BAT} > V _{MCV} inhibits fast charge
VLBAT	Minimum cell voltage	950	$\pm 5\%$	mV	Voltage at pin BAT
PVD	BAT input change for PVD detection	3.8	±20%	mV	
V _{SNSHI}	High threshold at SNS, resulting in MOD-low	50	±10	mV	Voltage at pin SNS

-50

V_{CC} - 1

V_{MCV} - 0.1

 ± 10

 ± 0.5

 ± 0.02

mV

V

v

Voltage at pin SNS

Applied to pin BAT

At pin BAT

DC Thresholds (TA = TOPR; VCC = 5V ±20% unless otherwise specified)

Low threshold at SNS, resulting in

Sleep-mode input threshold

Recharge threshold

MOD-high

VSNSLO

 $\mathrm{V}_{\mathrm{SLP}}$

 $V_{\rm RCH}$

Symbol	Condition	Minimum	Туріса	I Maximu	m Ui	nit Notes
V _{CC}	Supply voltage	4.0	5.0	6.0	v	
Icc	Supply current	-	0.5	1	mA	Exclusive of external loads
I _{CCS}	Sleep current	-	-	5	μA	$V_{BAT} = V_{SLP}$
V _{TS}	Thermistor input	0.5	-	$V_{\rm CC}$	v	$V_{\rm TS} < 0.5 V$ prohibited
VOH	Output high	V _{CC} - 0.2	-	-	v	MOD, I _{OH} = 20mA
VOL	Output low	-	-	0.2	v	MOD, LED, I _{OL} = 20mA
I _{OZ}	High-impedance leakage current	-	-	5	μΑ	LED
I _{snk}	Sink current	-	-	20	mA	MOD, LED
R _{MTO}	Charge timer resistor	2	-	250	kΩ	
C _{MTO}	Charge timer capacitor	0.001	-	1.0	μF	

Recommended DC Operating Conditions = TOPR)

Note: All voltages relative to V_{SS} except as noted.

Impedance

Symbol	Parameter	Minimum	Typical	Maximum	Unit
R _{BAT}	Battery input impedance	10	-	-	MΩ
R _{TS}	TS input impedance	10	-	-	MΩ
R _{SNS}	SNS input impedance	10	-	-	MΩ

Timing (TA = TOPR; VCC = 5V \pm 20% unless otherwise specified)

Symbol	Parameter	Minimum	Typical	Maximum	Unit
d _{MTO}	MTO time-base variation	-5	-	+5	%
f _{TRKL}	Pulse-trickle frequency	0.9	1.0	1.1	Hz

Ordering Information



bq24400 NiCd/NiMH Fast-Charge IC with $\Delta T/\Delta t$ Detection

8-Pin DIP(P)



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001

8-Pin SOIC Narrow (D)



NOTES:

A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0.006 (0,15).

D. Falls within JEDEC MS-012

8-Pin TSSOP ~ PW Package Suffix



NOTES:

A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
BQ24400D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
BQ24400DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
BQ24400DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
BQ24400DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
BQ24400PW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
BQ24400PWG4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
BQ24400PWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR
BQ24400PWRG4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details. TBD: The Pb-Free/Green conversion plan has not been defined.

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Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*A	Il dimensions are nominal												
	Device		Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
	BQ24400DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
	BQ24400PWR	TSSOP	PW	8	2000	330.0	12.4	7.0	3.6	1.6	8.0	12.0	Q1



PACKAGE MATERIALS INFORMATION

11-Mar-2008



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ24400DR	SOIC	D	8	2500	346.0	346.0	29.0
BQ24400PWR	TSSOP	PW	8	2000	346.0	346.0	29.0

D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.

E. Reference JEDEC MS-012 variation AA.



MECHANICAL DATA

MTSS001C - JANUARY 1995 - REVISED FEBRUARY 1999

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



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