

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX125F, TC74LCX125FK

Low-Voltage Quad Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX125 is a high-performance CMOS quad bus buffers. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

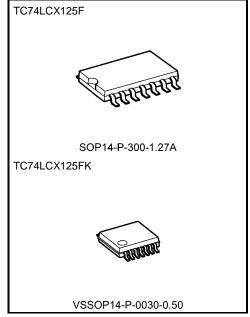
The device is designed for low-voltage $(3.3 \text{ V}) \text{ V}_{CC}$ applications, but it could be used to interface to 5-V supply environment for inputs.

This device requires the 3-state control input (\overline{OE}) to be set high to place the output into the high impedance state.

All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: VCC = 1.65 to 3.6 V
- High-speed operation: $t_{pd} = 6.0 \text{ ns (max)} (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: $> \pm 500 \text{ mA}$
- Available in JEITA SOP, VSSOP (US)
- · Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 125 type



Weight

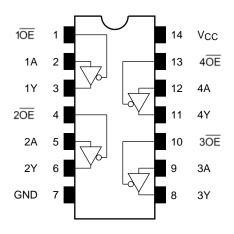
SOP14-P-300-1.27A : 0.18 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

Note: The Electrical Characteristics of $V_{\rm CC}$ = 1.8 \pm 0.15 V is only applicable for products which manufactured from January 2009 onward.

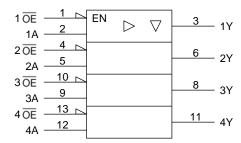
Start of commercial production 1994-10



Pin Assignment (top view)



IEC Logic Symbol



Truth Table

Inp	uts	Outputs
ŌĒ	Α	Y
Н	Х	Z
L	L	L
L	Н	Н

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	Vouт	-0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	lıK	-50	mA
Output diode current	lok	±50 (Note 4)	mA
DC output current	lout	±50	mA
Power dissipation	PD	180	mW
DC Vcc/ground current	ICC/IGND	±100	mA
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: Output in OFF state

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: VOUT < GND, VOUT > VCC



Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit	
Dower own by weltone	Voc	1.65 to 3.6		
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)	V	
Input voltage	VIN	0 to 5.5	V	
Output voltage	\/ou	0 to 5.5 (Note 3)	V	
Output voltage	Vout	0 to Vcc (Note 4)	V	
Output current	IOH/IOL	±24 (Note 5)	mA	
Output current	IOH/IOL	±12 (Note 6)	IIIA	
Operating temperature	Topr	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

- Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

 Unused inputs must be tied to either VCC or GND.
- Note 2: Data retention only
 Note 3: Output in OFF state
 Note 4: High or low state
 Note 5: VCC = 3.0 to 3.6 V
- Note 5: VCC = 3.0 to 3.6 VNote 6: VCC = 2.7 to 3.0 V
- Note 7: VIN = 0.8 to 2.0 V, VCC = 3.0 V



Electrical Characteristics

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteris	stics	Symbol	Test Condition										Max	Unit
					Vcc (V)	V								
		.,			1.65 to 2.3	Vcc×0.9								
	H-level	VIH	_		2.3 to 2.7	1.7								
Input voltage					2.7 to 3.6	2.0		٧						
					1.65 to 2.3	_	Vcc × 0.1							
	L-level	VIL	_		2.3 to 2.7	_	0.7							
					2.7 to 3.6	_	0.8							
				$IOH = -100 \mu A$	1.65 to 3.6	Vcc-0.2	_							
				$I_{OH} = -4 \text{ mA}$	1.65	1.05	_							
	I I I I I I I I I I I I I I I I I I I	\/ - · ·	Maria Marian Mari	I _{OH} = -8 mA	2.3	1.7		V						
	H-level	el Voh	$V_{IN} = V_{IH}$ or V_{IL}	I _{OH} = -12 mA	2.7	2.2	_							
				I _{OH} = -18 mA	3.0	2.4	_							
				I _{OH} = -24 mA	3.0	2.2	_							
Output voltage		VoL	VIN = VIH or VIL	I _{OL} = 100 μA	1.65 to 3.6	_	0.2							
				I _{OL} = 4 mA	1.65	_	0.45							
	1 11			IOL = 8 mA	2.3		0.7							
	L-level			I _{OL} = 12 mA	2.7	_	0.4							
				I _{OL} = 16 mA	3.0		0.4							
				I _{OL} = 24 mA	3.0		0.55							
Input leakage currer	nt	I _{IN}	V _{IN} = 0 to 5.5 V		1.65 to 3.6		±5.0	μΑ						
3-state output OFF	state current	loz	VIN = VIH or VIL VOUT = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА						
Power-off leakage c	urrent	loff	V _{IN} /V _{OUT} = 5.5 V		0	_	10.0	μΑ						
Outroposi			VIN = VCC or GND		1.65 to 3.6	_	10.0							
Quiescent supply cu	Quiescent supply current		V _{IN} /V _{OUT} = 3.6 to 5.5 V		1.65 to 3.6	_	±10.0	μА						
Increase in ICC per	input	Δlcc	VIH = VCC - 0.6 V (per 1	input)	2.7 to 3.6		500							



AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics	Cumbal	Symbol Test Condition		Min Max	Mov	Lloit
Characteristics	Symbol	rest Condition	V _{CC} (V)	IVIII	IVIAX	Unit
			1.8 ± 0.15	_	20.0	
Dran a gation, dalay times	tpLH	Figure 4 Figure 0	2.5 ± 0.2		7.5	
Propagation delay time	tpHL	Figure 1, Figure 2	2.7	_	6.5	ns
			3.3 ± 0.3	1.5	6.0	
			1.8 ± 0.15	_	30.0	- ns
Output anable time	t _{pZL} tpzH	Figure 1, Figure 3	2.5 ± 0.2	_	15.0	
Output enable time			2.7	_	8.0	
			3.3 ± 0.3	1.5	7.0	
	tpLZ tpHZ	Figure 1, Figure 3	1.8 ± 0.15	_	28.0	
Output Backle See			2.5 ± 0.2		14.0	
Output disable time			2.7		7.0	ns
			3.3 ± 0.3	1.5	6.0	
Output to suitant aliani	tosLH	(NI-4-)	2.7	_	_	
Output to output skew	tosHL	(Note)	3.3 ± 0.3	_	1.0	ns

Note: Parameter guaranteed by design.

(tosLH = |tpLHm - tpLHn|, tosHL = |tpHLm - tpHLn|)

Dynamic Switching Characteristics (Ta = 25°C, input: tr = tf = 2.5 ns, CL = 50 pF, RL = 500 Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic VoL	VOLP	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic VoL	Volv	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	_	3.3	7	pF
Output capacitance	Соит	_	3.3	8	pF
Power dissipation capacitance	CPD	f _{IN} = 10 MHz (Note) 3.3	25	pF

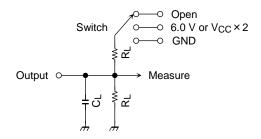
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) = CPD·VCC·fIN + ICC/4 (per gate)



AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
	6.0 V	@ $V_{CC} = 3.3 \pm 0.3 \text{ V}$ @ $V_{CC} = 2.7 \text{ V}$	
tpLZ, tpZL	V _{CC} ×2	@ $V_{CC} = 2.5 \pm 0.2 \text{ V}$ @ $V_{CC} = 1.8 \pm 0.15 \text{ V}$	
t _{pHZ} , t _{pZH}	GND		

Figure 1



AC Waveform

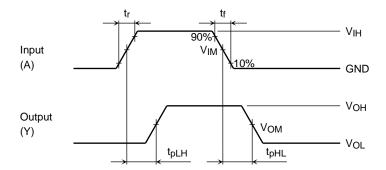


Figure 2 t_{pLH}, t_{pHL}

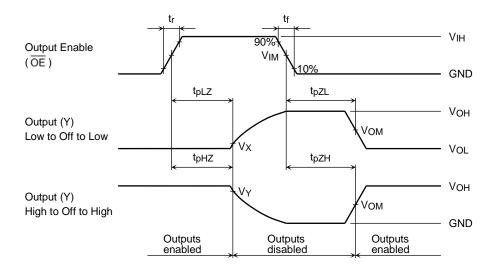


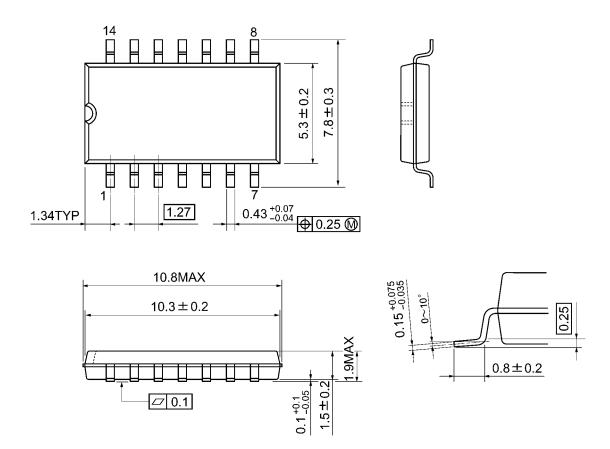
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

			Vcc	
	Symbol	$3.3 \pm 0.3 \text{ V}$ 2.7 V	$2.5\pm0.2~\textrm{V}$	1.8 ± 0.15 V
Input	VIH	2.7 V	Vcc	Vcc
	V _{IM}	1.5 V	V _{CC} /2	V _{CC} /2
	t _r , t _f	2.5 ns	2.0 ns	2.0 ns
Output	Vом	1.5 V	VoH/2	VoH/2
	Vx	V _{OL} +0.3 V	V _{OL} +0.15 V	VoL +0.15 V
	VY	VoH -0.3 V	VoH -0.15 V	Vон -0.15 V
Load	CL	50 pF	30 pF	30 pF
	RL	500 Ω	500 Ω	1 kΩ



Package Dimensions

SOP14-P-300-1.27A Unit: mm

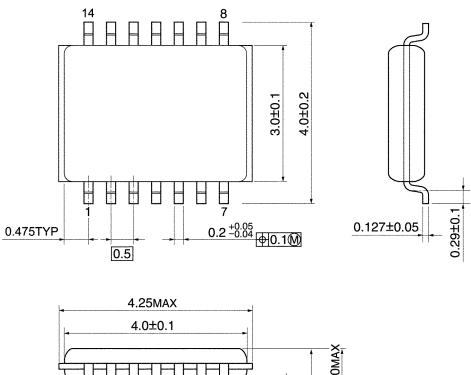


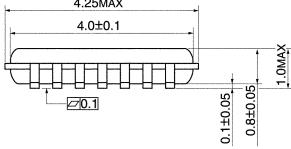
Weight: 0.18 g (typ.)



Package Dimensions

VSSOP14-P-0030-0.50 Unit: mm





Weight: 0.02 g (typ.)



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