TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG04FE

Inverter

Features

High output current : ±8 mA (min) at V_{CC} = 3.0 V

• Super high speed operation: tpd = 2.3 ns (typ.)

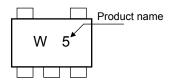
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

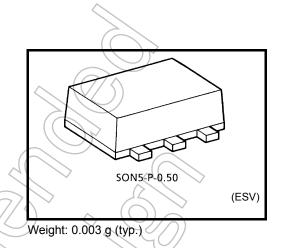
Operating voltage range : V_{CC} = 0.9 to 3.6 V

• 5.5-V tolerant input

• 3.6-V power down protection output

Marking

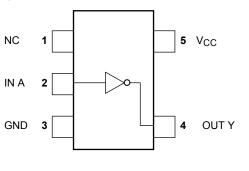




Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	-0.5 to 4.6	//V	
DC input voltage	VIN	-0.5 to 7.0	V	
DC output voltage	Value	-0.5 to 4.6 (Note 1)	<u> </u>	
DC output voltage	Voni	-0.5 to VCC + 0.5 (Note 2)	v	
Input diode current	/I _{IK}	-20	mA	
Output diode current	loк	-20 (Note 3)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	150	mW	
Storage temperature	T _{stg}	-65 to 150	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

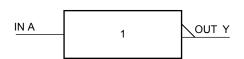
Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

Start of commercial production 2004-12

IEC Logic Symbol

Truth Table



Α	Y
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit				
Supply voltage	V_{CC}	0.9 to 3.6	V				
Input voltage	V _{IN}	0 to 5.5	>				
Output voltage	Vout	0 to 3.6 (Note 4)	V				
	V _{OUT}	0 to V _{CC} (Note 5)	·				
Output Current	I _{OH} /I _{OL}	±8.0 (Note 6)	S ((
		±4.0 (Note 7)	7				
		±3.0 (Note 8)	mA				
		±1.7 (Note 9)					
		±0.3 (Note 10)	7^				
		±0.02 (Note 11)))				
Operating temperature	T _{opr}	-40 to 85	ပဲ့				
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V				

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$

Note 10: $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$

Note 11: $V_{CC} = 0.9 V$

Note 12: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics

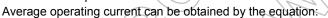
Characteristics	Currente ed	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics Symbol Test Condit		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
			0.9	V _{CC}	_	7	V _{CC}	_		
				1.1 to 1.3	V _{CC} × 0.7	_		V _{CC} ×0.7	_	
High-level input voltage	V _{IH}		_	1.4 to 1.6	V _{CC} × 0.65	-(7/5	V _{CC} × 0.65	_	V
Voltage				1.65 to 1.95	V _{CC} × 0.65			V _{CC} × 0.65	_	
				2.3 to 2.7	1.7	(-)	> _	1.7	_	
				3.0 to 3.6	2.0		_	2.0	_	
				0.9	4	\searrow	GND	4)	GND	
				1.1 to 1.3	7/3	> _ ^	V _{CC} × 0.3	5-	V _{CC} × 0.3	
Low-level input voltage	V _{IL}		_	1.4 to 1.6		_	V _{CC} × 0.35	(4)	V _{CC} × 0.35	V
Voltage				1.65 to 1.95	<u> </u>	-(V _{CC} × 0.35	<u> </u>	V _{CC} × 0.35	
				2.3 to 2.7	_		0.7		0.7	
				3.0 to 3.6		W	8.0 (8.0	
			I _{OH} =-0.02 mA	0.9	0.75	//	_	0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V _{CC} × 0.75) $+$	_	V _{CC} × 0.75	_	
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	V
Vollage			1 _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	2.48	_	
•			$I_{OL} = 0.02 \text{ mA}$	0.9	_	_	0.1	_	0.1	
		\supset	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_	_	V _{CC} × 0.25	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
Low-level output voltage	Vol	V _{IN} = V _{IH}	I _{OL} = 1.7 mA	1.4 to 1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
			$I_{OL} = 3.0 \text{ mA}$	1.65 to 1.95	_	_	0.45	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
		> ((l _{OL} = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	_
Input leakage current	I _{IN}	V _{IN} = 0 to 5.5 V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current	l _{OFF}	V _{IN} = 0 to 5.5 V V _{OUT} = 0 to 3.6 V		0	_	_	1.0		10.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		3.6	_	_	1.0	_	10.0	μА

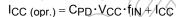
AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40 to 85°C		Unit	
Grandoteristics Symbol 16		rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	18.6	_	_	_	ns
			1.1 to 1.3	_	8.7	18.4	1.0	34.2	
			1.4 to 1.6	1	4.9	8.5	1.0	10.0	
			1.65 to 1.95		3.8	6.2	1,0	6.7	
			2.3 to 2.7		2.6	3.9	1.0	4.4	
			3.0 to 3.6	- <	2.1	//3.1	1.0	3.7	
		C_L = 15 pF, R_L = 1 M Ω	0.9	_	21.0		_	_	
	tpLH tpHL		1.1 to 1.3		9.8	21.5	1.0	37.1	
			1.4 to 1.6	((5.4	9.3	1.0	11.2	
			1.65 to 1.95	Ŧ	4.2	6.9	1.0	7,1	
			2.3 to 2.7	/ ((2.8	4.4	1.0	5.0	
			3.0 to 3.6	//-\)	2.3	3.4 (1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9)	31.2		~(/ /)	/ —	
			1.1 to 1.3	<u> </u>	13.8	29.6	1.0	56.0	
			1.4 to 1.6		7.4	13.1)	1.0	15.9	
			1.65 to 1.95		5.6	9.2	1.0	9.6	
			2.3 to 2.7		3.7	5.7	1.0	6.1	
			3.0 to 3.6		2.9	4.4	1.0	4.8	
Input capacitance	C _{IN}		3.6	1	3	_	_	_	pF
Power dissipation capacitance	C_{PD}	(Note 13)	0.9 to 3.6	_//	/ 6	_	_	_	pF

Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

4



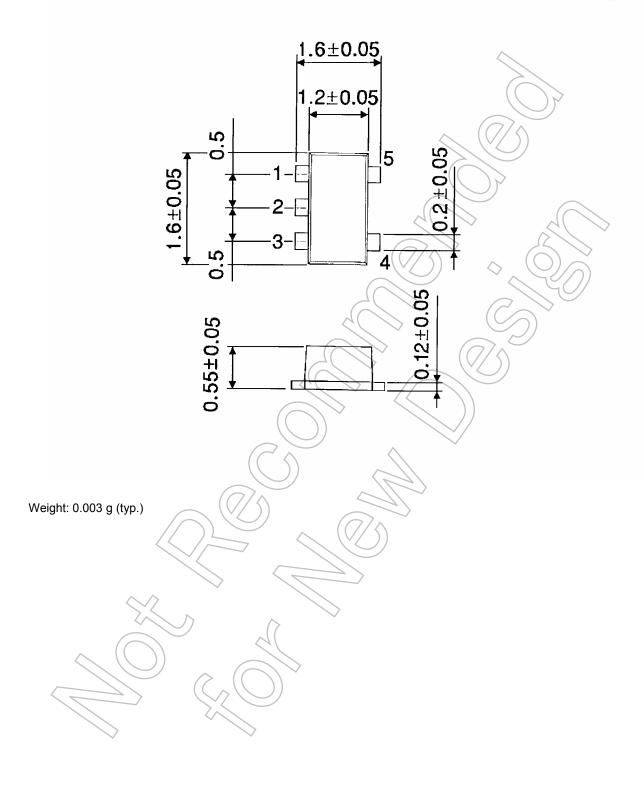






Package Dimensions

SON5-P-0.50 Unit: mm



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