TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG04FU

Inverter

#### **Features**

High output current : ±8 mA (min) at V<sub>CC</sub> = 3.0 V

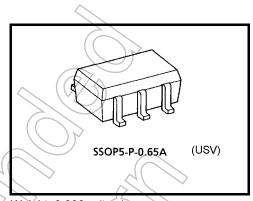
• Super high speed operation: tpd = 2.3 ns (typ.)

at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

Operating voltage range : V<sub>CC</sub> = 0.9 to 3.6 V

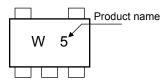
5.5-V tolerant input

• 3.6-V power down protection output

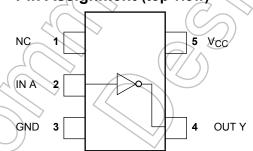


Weight: 0.006 g (typ.)

#### Marking



#### Pin Assignment (top view)



# Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 4.6	V
DC input voltage	$\supset_{V_{IN}}$	-0.5 to 7.0	٧
DC output voltage	V <sub>OUT</sub>	-0.5 to 4.6 (Note 1) -0.5 to V <sub>CC</sub> + 0.5 (Note 2)	٧
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1:  $V_{CC} = 0 V$ 

Note 2: High or Low state. Do not exceed I<sub>OUT</sub> of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2005-02

# **IEC Logic Symbol**

# IN A 1 OUT Y

#### **Truth Table**

А	Y
L	Н
Н	L

# **Operating Ranges**

Characteristics	Symbol	Rating
Supply voltage	V <sub>CC</sub>	0.9 to 3.6
Input voltage	V <sub>IN</sub>	0 to 5.5
Output voltage	Vout	0 to 3.6 (Note 4)
	V <sub>OUT</sub>	0 to V <sub>CC</sub> (Note 5)
	I <sub>OH</sub> /I <sub>OL</sub>	±8.0 (Note 6)
		±4.0 (Note 7)
Output Current		±3.0 (Note 8) mA
Output Current		±1.7 (Note 9)
		±0.3 (Note 10)
		±0.02 (Note 11)
Operating temperature	T <sub>opr</sub>	-40 to 85 °C
Input rise and fall time	dt/dv	0 to 10 (Note 12) ns/V

Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 7:  $V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \text{ to } 1.6 \text{ V}$ 

Note 10:  $V_{CC} = 1.1 \text{ to } 1.3 \text{ V}$ 

Note 11:  $V_{CC} = 0.9 V$ 

Note 12:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V

TC7SG04FU



## **Electrical Characteristics**

#### **DC Characteristics**

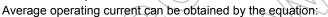
Characteristics	Coursels al	nbol Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
			0.9	V <sub>CC</sub>	_	7	V <sub>CC</sub>	-		
				1.1 to 1.3	V <sub>CC</sub> × 0.7	_		V <sub>CC</sub> ×0.7	_	
High-level input voltage	V <sub>IH</sub>		_	1.4 to 1.6	V <sub>CC</sub> × 0.65	70	775	V <sub>CC</sub> × 0.65	_	٧
				1.65 to 1.95	V <sub>CC</sub> × 0.65		9	V <sub>CC</sub> × 0.65	_	
				2.3 to 2.7	1.7		> _	1.7	_	
				3.0 to 3.6	2.0	$\overline{}$	_	2.0	_	
				0.9	CIF	$\searrow$	GND	4)	GND	
				1.1 to 1.3	75		V <sub>CC</sub> × 0.3	5	V <sub>CC</sub> × 0.3	
Low-level input voltage	V <sub>IL</sub>		_	1.4 to 1.6		_	V <sub>CC</sub> × 0.35		V <sub>CC</sub> × 0.35	V
			1.65 to 1.95	_	-(	V <sub>CC</sub> × 0.35		V <sub>CC</sub> × 0.35		
			2.3 to 2.7	_		0.7		0.7		
				3.0 to 3.6	_	(Y	0.8		0.8	
			I <sub>OH</sub> =-0.02 mA	0.9	0.75	/	_	0.75	_	· · · · · · · · · · · · · · · · · · ·
			$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	V <sub>CC</sub> × 0.75	)}	_	V <sub>CC</sub> × 0.75	_	
High-level output voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>I</sub> C   I <sub>O</sub> I	I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75		_	V <sub>CC</sub> × 0.75	_	
			l <sub>OH</sub> = −3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45		_	V <sub>CC</sub> -0.45		
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0		
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48	_	_	2.48	-	
			$I_{OL} = 0.02 \text{ mA}$	9.9	_	_	0.1	_	0.1	
		>	$I_{OL} = 0.3 \text{ mA}$	1.1 to 1.3	_		V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level output voltage	Vol	V <sub>IN</sub> = V <sub>IH</sub>	$I_{OL} = 1.7 \text{ mA}$	1.4 to 1.6	_		V <sub>CC</sub> × 0.25		V <sub>CC</sub> × 0.25	٧
		$\prec$	OL = 3.0 mA	1.65 to 1.95	_	_	0.45	_	0.45	
			$I_{OL} = 4.0 \text{ mA}$	2.3 to 2.7	_	_	0.4	_	0.4	
	$\Diamond$		$I_{OL} = 8.0 \text{ mA}$	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	IIN	$V_{IN} = 0$ to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μА
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0 to 5.5V V <sub>OUT</sub> = 0 to 3.6V		0	_	_	1.0	_	10.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	_	10.0	μΑ

3 2014-03-01

## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	tPLH tPHL	$C_L$ = 10 pF, $R_L$ = 1 $M\Omega$	0.9	_	18.6	_	_	_	
			1.1 to 1.3	_	8.7	18.4	1.0	34.2	
			1.4 to1.6	_	4.9	8.5	1.0	10.0	ns
			1.65 to 1.95	_	3.8	6.2	1,0	6.7	
			2.3 to 2.7	_	2.6	3.9	1.0	4.4	
			3.0 to 3.6	- <	2.1	//3.1	1.0	3.7	
		$C_L$ = 15 pF, $R_L$ = 1 $M\Omega$	0.9		21.0		_	_	
			1.1 to 1.3	_	9.8	21.5	1.0	37.1	
			1.4 to 1.6	7	5.4	9.3	1.0	11.2	
			1.65 to 1.95	F	4.2	6.9	1.0	77	
			2.3 to 2.7	<u></u>	2.8	4.4	21.0	5.0	
			3.0 to 3.6	//\)	2.3	3.4 (	1.0	3.9	
		$C_L$ = 30 pF, $R_L$ = 1 $M\Omega$	0.9	)	31.2		Z({/)	/ —	
			1.1 to 1.3		13.8	29.6	1.0	56.0	
			1.4 to 1.6	_	7.4	13.1)	1.0	15.9	
			1.65 to 1.95	_	5.6	9.2	1.0	9.6	
			2.3 to 2.7		3.7	5.7	1.0	6.1	
			3.0 to 3.6		2.9	4.4	1.0	4.8	
Input capacitance	C <sub>IN</sub>		3.6	1	3	_			pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9 to 3.6		<b>/</b> 6		_	_	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

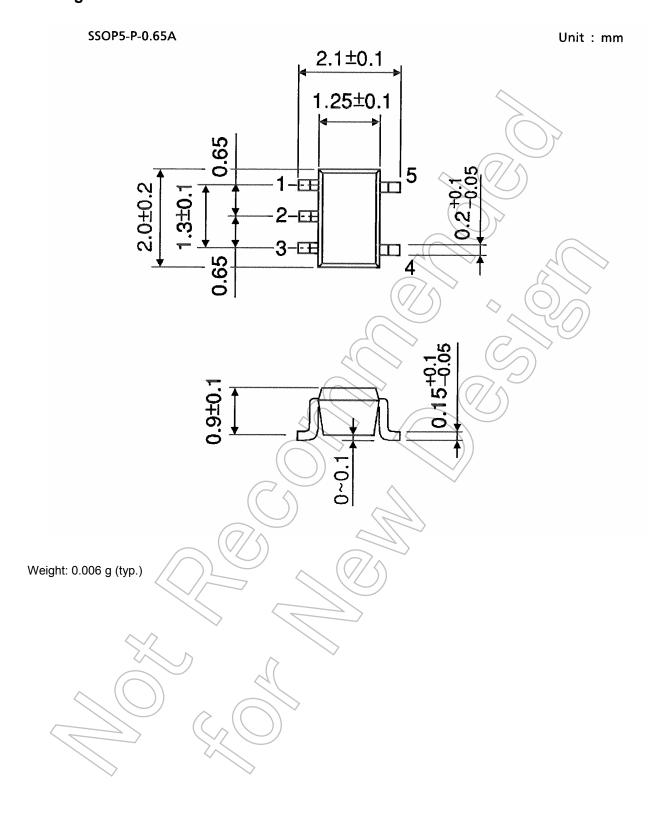


ICC (opr.) = CPD·VCC·fIN + ICC



# **Package Dimensions**

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