TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG17FE

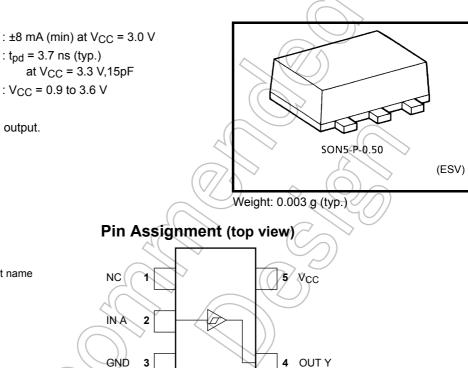
Schmitt Buffer



High output current : ±8 mA (min) at V_{CC} = 3.0 V

Product name

- Super high speed operation : tpd = 3.7 ns (typ.)
- Operating voltage range
- 5.5-V tolerant input.
- 3.6-V power down protection output.



Marking

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	⊃ V _{CC}	-0.5 to 4.6	V
DC input voltage	V _{IN} -0.5 to 7.0		V
DC output voltage	Vout	-0.5 to 4.6 (Note 1)	V
		-0.5 to V _{CC} + 0.5 (Note 2)	v
Input diode current	lik⁄>	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	ICG	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0V$

Note 2: High or Low state. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND

Start of commercial production 2005-07

TOSHIBA

IEC Logic Symbol



А	Y
L	L
Н	н

Truth Table

Operating Pandos

berating Ranges		
Characteristics	Symbol	Rating
Supply voltage	V _{CC}	0.9 to 3.6 V
Input voltage	V _{IN}	0 to 5.5 V
Output voltage	Varia	0 to 3.6 (Note 4) V
Oulput voltage	Vout	0 to Vcc (Note 5)
		±8.0 (Note 6)
		±4.0 (Note 7)
Output Current	leu/leu	±3.0 (Note 8)
Oulput Current	I _{OH} /I _{OL}	±1.7 (Note 9)
		±0.3 (Note 10)
		±0.02 (Note 11)
Operating temperature	T _{opr}	-40 to 85 °C
Note 4: $V_{CC} = 0 V$	C	
Note 5: High or Low state		
Note 6: $V_{CC} = 3.0$ to 3.6 V	$C \sim$	\sim
Note 7: $V_{CC} = 2.3$ to 2.7 V	(\bigcirc)	
Note 8: $V_{CC} = 1.65$ to 1.95 V		
Note 9: $V_{CC} = 1.4$ to 1.6 V	$\langle \bigcirc \rangle$	
Note 10: $V_{CC} = 1.1$ to 1.3 V		$\sim (\checkmark)$
Note 11: V _{CC} = 0.9 V		
	\land	\checkmark
	\triangleleft	
$\langle (\bigcirc) \rangle$		
	\sim	
	$\langle \rangle$	

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test		Symbol Test Condition				Ta = 25°C			Ta = -40 to 85°C		Unit	
		V _{CC} (V)		Min	Тур.	Max	Min	Max	Unit			
					0.9	—	_ <	0.73	—	0.80		
Positive				1.1	_	_	0.86	_	0.93			
				1.4	_	_	1.07)/	1.12			
	threshold voltage	VP		_	1.65	_	+0	1.23	_	1.25		
					2.3		-FX	1.66	_	1.68		
Threshold					3.0	-(A.	2.14	_	2.15	V	
voltage					0.9	0.18	\square	-	0.07	_	V	
					1.1	0.26		_	0.18	-		
	Negative				1.4	0.36	~		0.31	$\overline{}$		
	threshold voltage	V _N		_	1.65	0.45		-6	0.41	> _		
					2.3	0.69			0.64) —		
					3.0	0.96	_	$\langle \rangle$	0.91	_		
				1	0.9	0.20	-((0.38	0.15	0.53		
					1.1	0.25		0.41	0.21	0.53		
		N			1.4	0.35	\mathcal{A}^{\langle}	0.48	0.34	0.57	V	
Hysteresis voltag	le	V _H		-	1.65	0.42		0.56	0.40	0.60	V	
					2.3	0.60		0.74	0.60	0.76		
				$(\bigcirc)^{\checkmark}$	3.0	0.79	7_	0.93	0.79	0.94		
			\bigcap	I _{OH} ==0.02 mA	0.9	0.75	_		0.75			
				I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75		_	$\begin{array}{c} V_{CC} \\ \times \ 0.75 \end{array}$	_		
	High level	VQH		I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75		_	V _{CC} × 0.75	_		
				I _{OH}	1.65 to 1.95	V _{CC} -0.45			V _{CC} -0.45			
		\sim			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	—	2.0	—	
Output voltage		\rightarrow		I _{OH} = -8.0 mA	3.0 to 3.6	2.48		—	2.48	—	V	
	~ 2			I _{OL} = 0.02 mA	0.9			0.1	—	0.1		
(D	20	I _{OL} = 0.3 mA	1.1 to 1.3	_		V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
	Low level	V _{OL}	VIN=VIL	1 _{OL} = 1.7 mA	1.4 to 1.6	_		V _{CC} × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
				J _{OL} = 3.0 mA	1.65 to 1.95	—	_	0.45	—	0.45		
		22		I _{OL} = 4.0 mA	2.3 to 2.7			0.4	—	0.4		
\searrow			\rightarrow	I _{OL} = 8.0 mA	3.0 to 3.6	—	_	0.4	—	0.4		
Input leakage cu	rrent	I _{IN}	$V_{IN} = 0$ to	5.5V	0 to 3.6			±0.1	—	±1.0	μΑ	
Power off leakag	e current	IOFF	$V_{IN} = 0$ to $V_{OUT} = 0$		0			1.0		10.0	μA	
Quiescent supply	/ current	ICC	V _{IN} = V _{CC}	or GND	3.6	_		1.0	_	10.0	μΑ	

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		1.1	
Characteristics Sy		Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		27.3		_	—	
			1.1 to 1.3	_	13.0	22.6	1.0	35.9	
			1.4 to 1.6	_	7.5	10.5	1.0	11.3	ns
			1.65 to 1.95		6.0	7.8	1.0	8.2	
			2.3 to 2.7	_	4.3	5.4	1.0	5.8	
			3.0 to 3.6	-0	3.5	4.4	1.0	4.6	
	t _{pLH} t _{pHL}	C _L = 15 pF, R _L = 1 MΩ	0.9	_	29.5	\mathcal{T}	—	—	
			1.1 to 1.3	_ (14.3	25.1	1.0	41.8	
			1.4 to 1.6		8.0	11.5	1.0	12.6	
			1.65 to 1.95	Æ	6,3	8.4	1.0	8.7	
			2.3 to 2.7		4.6	5.7	21.0	6.1	
			3.0 to 3.6	$(/ \rightarrow)$	3.7	4.6	10	5.0	
		C _L = 30 pF, R _L = 1 MΩ	0.9	\mathcal{T}	40.5	\mathbf{A}	-4/	7 –	
			1.1 to 1.3	$\geq -$	19.6	35.7	1.0	58.1	
			1.4 to 1.6		10.7	15.8	1.0	17.6	
			1.65 to 1.95	—	7.8	10.7	1.0	11.7	
			2.3 to 2.7	_	5.4	6.9	1.0	8.1	
			3.0 to 3.6	$\langle - \rangle$	4.3	5.2	1.0	6.1	
Input capacitance	C _{IN}		3.6	\geq	3	—	—	—	pF
Power dissipation capacitance	C _{PD}	(Note 12)	0.9 to 3.6		//7	_	—	—	pF

Note 12: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

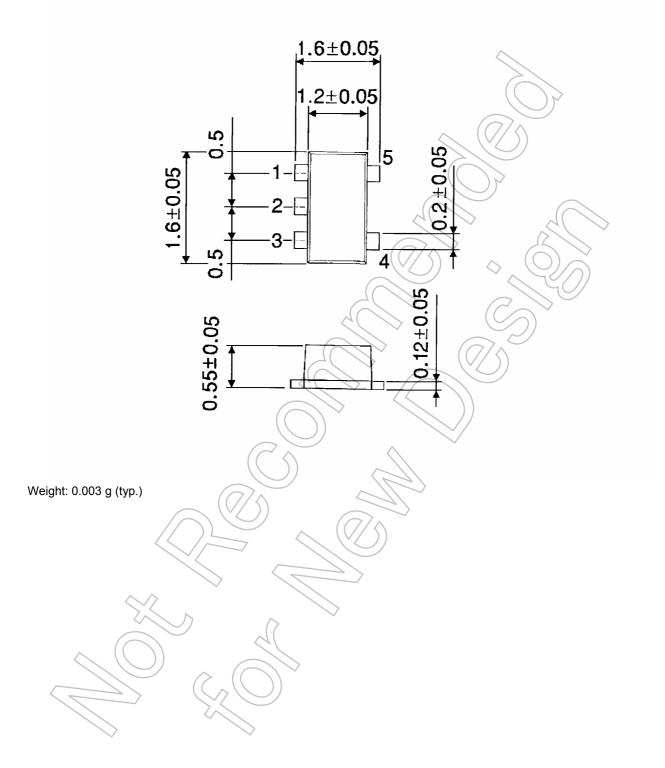
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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