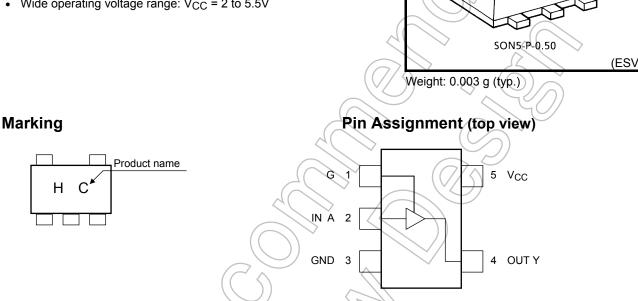
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH126FE

Bus Buffer with 3-STATE Output

Features

- High speed: t_{pd} = 3.8ns (typ.) at V_{CC} = 5 V, C_L = 15 pF
- Low power dissipation: $I_{CC} = 2\mu A \pmod{at}$ Ta = 25°C •
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (min) •
- 5.5 V tolerant input.
- Wide operating voltage range: V_{CC} = 2 to 5.5V



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	-0.5 to 7	V
DC input voltage	VIN	–0.5 to 7	V
DC output voltage	VOUT	-0.5 to V _{CC} + 0.5	V
Input diode current	LIK	-20	mA
Output diode current	Іок	±20 (Note1) mA
DC output current	Гоит	±25	mA
DC VCC/ground current	Ice	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65 to150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: VOUT < GND, VOUT > VCC

Start of commercial production 2004-05

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Supply voltage

Input voltage

Output voltage

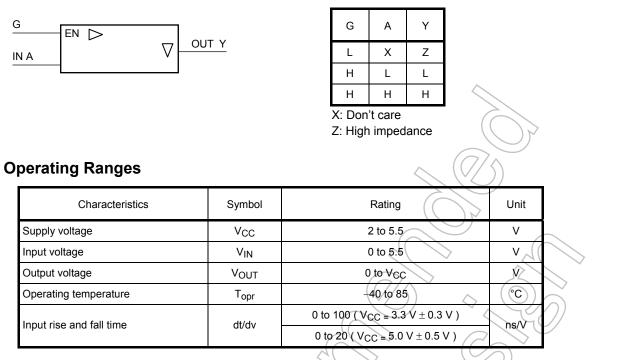
Operating temperature

Input rise and fall time

IEC Logic Symbol



Truth Table



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Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C		$Ta = -40$ to $85^{\circ}C$		Unit		
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
High-level input	_		2.0	1.5	— <	_	1.5	—	V		
voltage			3.0 to 5.5	$V_{CC} \times 0.7$	_	X	V _{CC} × 0.7				
			2.0			0.5) <u> </u>	0.5			
voltage	Low-level input VIL		_	3.0 to 5.5	\checkmark	(Vcc × 0.3	_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	V	
High-level output V(voltage			I _{OH} = -50 μA	2.0	1.9	2.0		1.9	_	v	
		VIN = VIH		3.0	2.9	3.0	>	2.9	_		
	V _{OH}			4.5	4.4	4.5	_	4.4			
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	\geq	—	2.48	\checkmark	-	
			I _{OH} = -8 mA	4.5	3.94		_	3.80	> _		
		DL VIN = VIH or VIL	I _{OL} = 50 μA	2.0	\bigcirc	0 🔷	0.1		0.1	V	
Low-level output V _{OL} voltage				3.0		0	0.1	4	0.1		
	V _{OL}			4.5		0	0.1	\geq _	0.1		
			I _{OL} = 4 mA	3.0	—		0.36	—	0.44	-	
			I _{OL} = 8 mA	4.5	—	(7]	0.36	—	0.44		
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or } GND$		5.5			±0.25	—	±2.5	μΑ	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5) - (±0.1		±1.0	μA	
Quiescent supply current	Icc	VIN = V _{CC} or GND		5.5	X	/_	2.0	—	20.0	μΑ	

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AC Characteristics (unless otherwise specified, input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Т	Test Condition		Ta = 25°C			$Ta = -40$ to $85^{\circ}C$		Unit
			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH		3.3 ± 0.3	15		5.6	8.0	1.0	9.5	ns
				50		8.1	11.5	1.0	13.0	
			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
				50	_	5.3	7.5	1.0	8.5	
3-state output enable time	^t pZL		3.3 ± 0.3	15	_	5.4	8.0))1.0	9.5	– ns
				50	<	7.9	11.5	1.0	13.0	
			5.0 ± 0.5	15	1	3.6	5.1	1.0	6.0	
				50	-((5.1	7.1	1.0	8.0	
3-state output disable time	t _{pLZ}		$\textbf{3.3}\pm\textbf{0.3}$	50		9.5	13.2	1.0	15.0	- ns
	t _{pHZ}		5.0 ± 0.5	50		6.1	8.8	1.0	10.0	
Input capacitance	C _{IN}		_	(X	4	10	St.	10	pF
Output capacitance	C _{OUT}		_	(7)		6	-	$\overline{)}$	<u> </u>	pF
Power dissipation capacitance	C _{PD}			(Note 2)	2	14	K	L)	_	pF

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

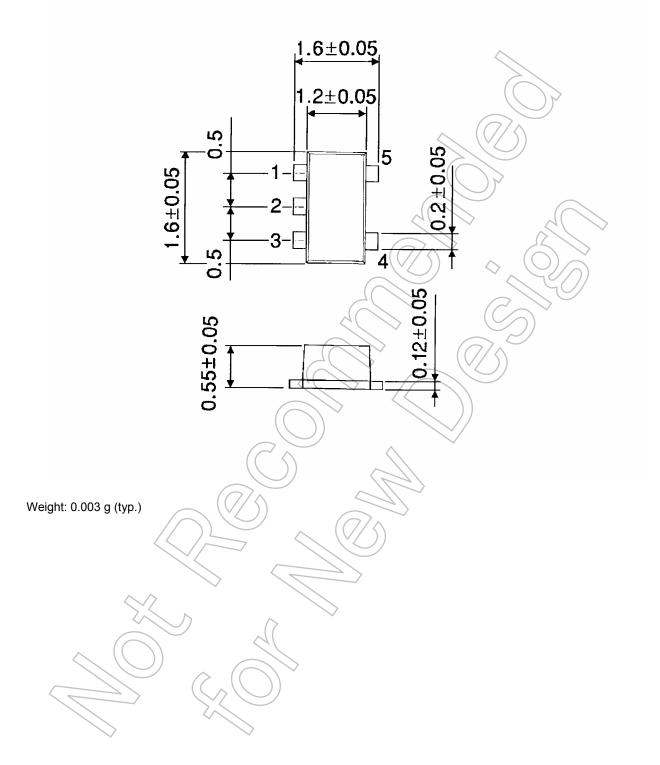
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

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Package Dimensions

SON5-P-0.50

Unit : mm



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