TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SZ00AFE

#### 2 Input NAND Gate

#### Features

Marking

- High output drive: ±24 mA (min) at V<sub>CC</sub> = 3 V •
- Super high speed operation:  $t_{PD} = 2.4$  ns (typ.)
- Operation voltage range: V<sub>CC (opr)</sub> = 1.8~5.5 V ٠
- Supply voltage data retention:  $V_{CC}$  = 1.5~5.5 V
- 5.5-V tolerant inputs.
- · Matches the performance of TC74LCX series when operated at 3.3-V V<sub>CC</sub>

at V<sub>CC</sub> = 5 V, 50 pF SON5-P-0.50 (ESV) Weight: 0.003 g (typ.) Pin Assignment (top view) Product name IN Ď 1 R IN A 2 4 OUT Y GND 3

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	_0.5~6	V
DC input voltage	VIN	-0.5~6	V
DC output voltage	VOUT	-0.5~V <sub>CC</sub> + 0.5	V
Input diode current	<u> I</u> IK	-20	mA
Output diode current	lok	±20	mA
DC output current	τυοι	±50	mA
DC V <sub>CC</sub> /ground current	tce	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C
Lead temperature (10 s)	ΤL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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## Logic Diagram

**Truth Table** 

N A & N B	OUT Y	A     B     Y       L     L     H       L     H     H       H     L     H       H     H     L
Characteristics	Symbol	Rating
Supply voltage	Symbol V <sub>CC</sub>	Rating         Unit           1.8~5.5         V           1.575.5         (Note 1)
Input voltage	VIN	
Output voltage	V <sub>OUT</sub>	O-VCC V
Operating temperature	T <sub>opr</sub>	40~85 °C
Input rise and fall time	dţ/dv	$0 \sim 20 (V_{GC} = 1.8 \text{ V}, 2.5 \text{ V} \pm 0.2 \text{ V})$ $0 \sim 10 (V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}) \text{ ns/V}$ $0 \sim 5 (V_{CC} = 5.5 \text{ V} \pm 0.5 \text{ V})$
Note 1: Data retention only		

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#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics Symbol Test Condition			Ta = 25°C Ta				Ta = −40~85°C			
			st condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage			1.8	$0.75 \times V_{CC}$		$\mathcal{A}$	0.75 × V <sub>CC</sub>	_	V	
		—	2.3-5.5	$0.7 \times V_{CC}$		$\left( \left( \right) \right)$	0.7 Vcc	—		
Low-level input		_	1.8	_<	_((	0.25 × V <sub>CC</sub>	_	0.25 × V <sub>CC</sub>	V	
voltage	۲Ľ			2.3-5.5	— (		0.3 × V <sub>CC</sub>	_	$0.3 \times V_{CC}$	v
				1.8	1.7	1.8	) _	1.7	_	
High-level V <sub>OH</sub>			100 4	2.3	2.2	23	—	2.2		
	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = −100 μA	3.0	2.9	3.0		2.9	$\geq$	v	
			4.5 ((	74.4	4.5	(	44	$\geq$ –		
		I <sub>OH</sub> = -8 mA	2,3	1.9	2.15	K	~1.9	) -		
			I <sub>OH</sub> = -16 mA	3.0	2.4	2.8		2.4		-
			$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68 (	$\langle \mathcal{A} \rangle$	2.3	_	
			I <sub>OH</sub> = -32 mA	4.5	3.8	4.2		3.8	_	
				18			0.1		0.1	
Low-level output voltage		I <sub>OL</sub> = 100 μA	2.3		6	0.1		0.1		
			3.0 <		0	0.1		0.1		
	V <sub>IN</sub> = V <sub>IH</sub>		4.5	X	0	0.1		0.1	v	
	VIN = VIH	Iot = 8 mA	2.3	_	√ 0.1	0.3		0.3		
		I <sub>OL</sub> = 16 mA	3.0	$\setminus -$	0.15	0.4	_	0.4		
		$I_{OL} = 24 \text{ mA}$	3.0	$\rightarrow$	0.22	0.55	_	0.55		
		$\sim$ ((/	1 <sub>OL</sub> = 32 mA	4.5	> -	0.22	0.55	_	0.55	
Input leakage current	Ім	$V_{IN} = 5.5 V$	or GND	0-5.5	_	_	±1	_	±10	μA
Quiescent supply current	Icc	VIN = VCC	or GND	5.5	_	_	2		20	μA

#### AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay <sup>t</sup> PLH time tPHL			1.8	2.0	4.5	9.5	2.0	10.0	
	$C_{I} = 15  pF$ ,	$2.5\pm0.2$	0.8	3.0	6.5	0.8	7.0		
	t <sub>PLH</sub>	$R_{L} = 1 M\Omega$	$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.4	4.5	0.5	4.7	ns
	<b>t</b> PHL		$5.0\pm0.5$	0.5	2.0	3.9	0.5	4.1	
	$\begin{array}{l} C_{L} = 50 \; pF, \\ R_{L} = 500 \; \Omega \end{array}$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	2.9	5.0	1.5	5.2		
		$5.0\pm0.5$	0.8	2.4	4.3	(0,8	4.5		
Input capacitance	CIN	—	0-5.5	_	4	X.	Ľ	_	pF
Power dissipation C <sub>PD</sub>	(Note 2)	3.3		19 (	-			pF	
	(Note 2)	5.5	_	27	$\mathbb{R}$				

Note2: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation

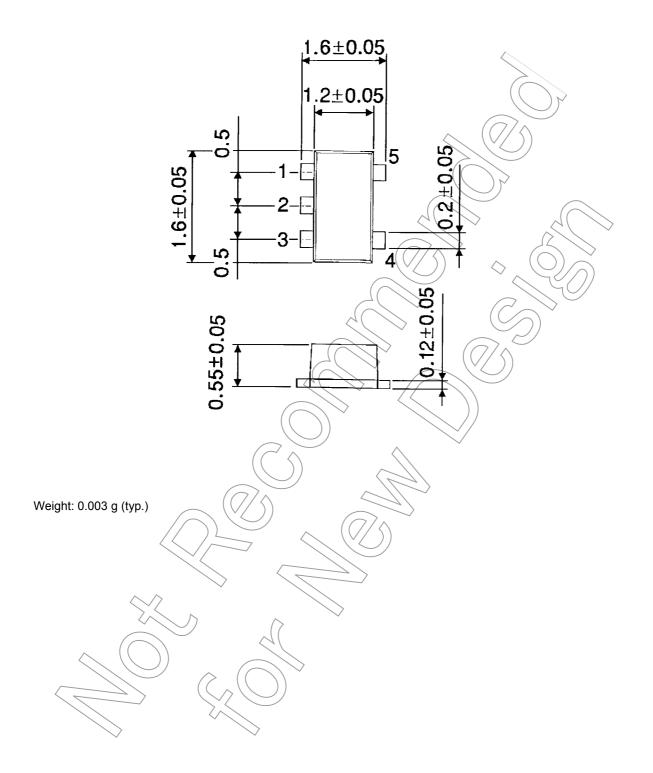
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

# **TOSHIBA**

#### Package Dimensions

SON5-P-0.50

Unit : mm



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