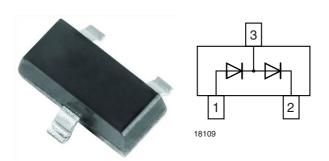


Vishay Semiconductors

Small Signal Switching Diode, Dual in Series



FEATURES

- · Fast switching speed
- · High conductance
- Surface mount package ideally suited for automatic insertion



- · Connected in series
- AEC-Q101 qualified
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

MECHANICAL DATA

Case: SOT-23

Weight: approx. 8.8 mg
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/3K per 7" reel (8 mm tape), 15K/box

PARTS TABLE						
PART	ORDERING CODE	TYPE MARKING	INTERNAL CONSTRUCTION	REMARKS		
BAV99-V	BAV99-V-GS18 or BAV99-V-GS08	JE	Dual diodes serial	Tape and reel		

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Non repetitive peak reverse voltage		V _{RM}	100	V		
Repetitive peak reverse voltage = Working peak reverse voltage = DC Blocking voltage		$V_{RRM} = V_{RWM} = V_{R}$	70			
Peak forward surge current	t _p = 1 s	I _{FSM}	1	А		
Peak forward surge current	t _p = 1 μs		4.5			
Average forward current	Half wave rectification with resistive load and f ≥ 50 MHz, on ceramic substrate 10 mm x 8 mm x 0.7 mm	I _{F(AV)}	150	mA		
Forward current	On ceramic substrate 10 mm x 8 mm x 0.7 mm	I _F	250			
Power dissipation	On ceramic substrate 10 mm x 8 mm x 0.7 mm	P _{tot}	300	mW		

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	On ceramic substrate 10 mm x 8 mm x 0.7 mm	R _{thJA}	430	K/W	
Junction and storage temperature range		$T_j = T_{stg}$	- 55 to + 150	°C	



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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I _F = 1 mA	V _F			715	- mV
Famuurd valtage	I _F = 10 mA				855	
Forward voltage	I _F = 50 mA				1	V
	I _F = 150 mA				1.25	
	V _R = 70 V	I _R			2.5	μА
Reverse current	V _R = 70 V, Tj = 150 °C				50	
	V _R = 25 V, Tj = 150 °C				30	
Diode capacitance	V _R = 0, f = 1 MHz	C _D			1.5	pF
Reverse recovery time	I_F = 10 mA to I_R = 1 mA, V_R = 6 V, R_L = 100 Ω	t _{rr}			6	ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

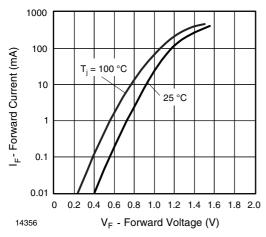


Fig. 1 - Forward Current vs. Forward Voltage

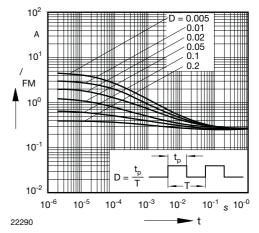
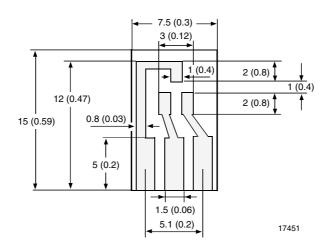


Fig. 2 - Peak forward current $f_{FM} = f(t_p)$

LAYOUT FOR R_{thJA} TEST

Thickness:

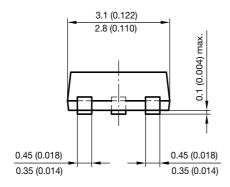
Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)

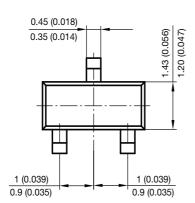




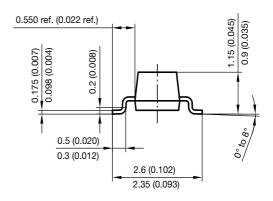
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PACKAGE DIMENSIONS in millimeters (inches): SOT-23

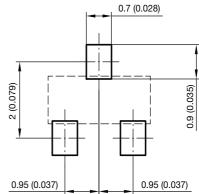




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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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