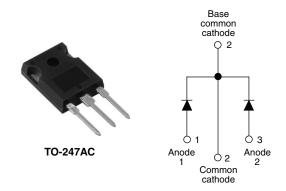


Vishay High Power Products

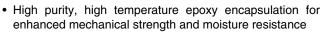
Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 15 A			
V_{R}	150 V			

FEATURES

- 175 °C T_J operation
- Center tap TO-247 package
- · Low forward voltage drop
- · High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 30CPQ150PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	30	A			
V _{RRM}		150	V			
I _{FSM}	t _p = 5 μs sine	1000	A			
V _F	15 Apk, T _J = 125 °C (per leg)	0.78	V			
T _J		- 55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	30CPQ150PbF	UNITS			
Maximum DC reverse voltage	V _R	150	V			
Maximum working peak reverse voltage	V _{RWM}	150	V			

ABSOLUTE MAXIMUM RATINGS							
PARAMETER SYMBOL TEST CONDITION		ITIONS	VALUES	UNITS			
Maximum average per device		I _{F(AV)} 50 % duty cycle at T _C = 135 °C, rectangular waveform		30			
forward current See fig. 5 per leg	I _{F(AV)}			15			
Maximum peak one cycle non-repetitive surge current per leg	l=a	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1000	A		
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	340			
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.50 \text{A}, L = 90 \text{mH}$		11.25	mJ		
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α		

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

30CPQ150PbF

Vishay High Power Products Schottky Rectifier, 2 x 15 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS			
		15 A	T _{.1} = 25 °C	1.00	V		
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	30 A	1J=25 C	1.19			
See fig. 1	VFM (1)	15 A	T 105 °C	0.78			
		30 A	- T _J = 125 °C	0.93			
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	0.1	mA		
See fig. 2		T _J = 125 °C	VR = nateu VR	15			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		340	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nΗ		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	BOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range	je	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance junction to case per leg	,	D	DC operation See fig. 4	2.20		
Maximum thermal resistance junction to case per package	,	R_{thJC}	DC operation	1.10	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24		
Approximate weight				6	g	
Approximate weight				0.21	OZ.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque —	maximum			12 (10)	(lbf \cdot in)	
Marking device			Case style TO-247AC (JEDEC)	30CP	Q150	



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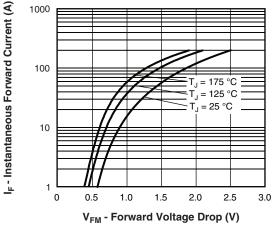


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

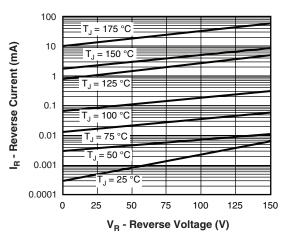


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

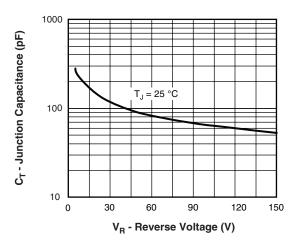


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

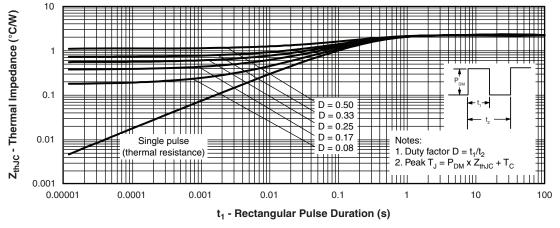


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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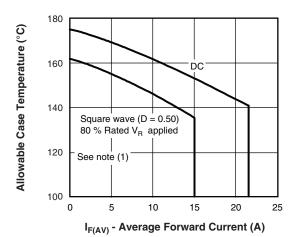


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

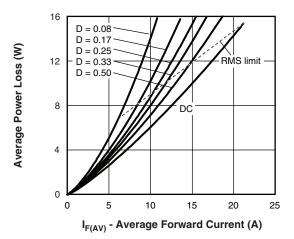


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

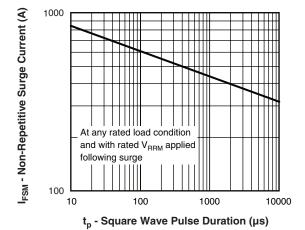


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

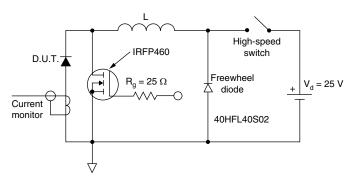


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ \text{Pd} = \text{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \text{at} \; (I_{F(AV)}/D) \; (\text{see fig. 6}); \\ \text{Pd}_{REV} = \text{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \text{at} \; V_{R1} = 80 \; \% \; \text{rated} \; V_R \\ \end{array}$

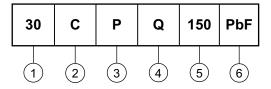
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (30 = 30 A)
- 2 Circuit configuration:

C = Common cathode

- 3 Package:
 - P = TO-247
- 4 Schottky "Q" series
- 5 Voltage code (150 = 150 V)
- 6 None = Standard production
 - PbF = Lead (Pb)-free

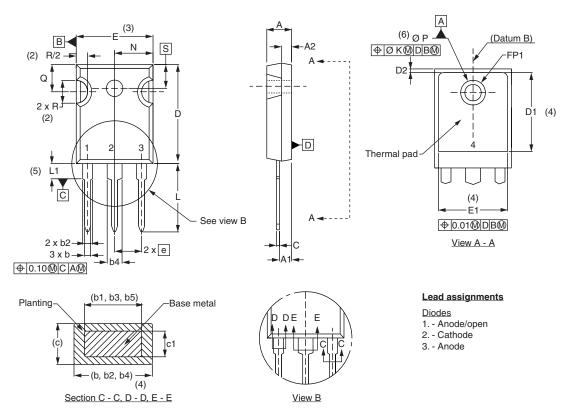
Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95223			
Part marking information http://www.vishay.com/doc?95226				



Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	MIN. MAX.		NOTES
STIVIBUL	MIN.	MAX.			NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.54		0.0	10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0	.3	
ΦР	3.56	3.66	0.14	0.144	
ФР1	1	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217	BSC	

Notes

- $^{(1)}$ Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c





Vishay

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