

- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation

Dual High-Voltage TMBS<sup>®</sup> (Trench MOS Barrier Schottky) Rectifier

Ultra Low V<sub>F</sub> = 0.57 V at I<sub>F</sub> = 5.0 A

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
   Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

### **MECHANICAL DATA**

**Case:** SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** as marked

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V30DM150C	UNIT	
Device marking code			V30DM150C		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	150	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	30	А	
	per diode		15	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	140	A	
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +175	*0	
Storage temperature range		T <sub>STG</sub>	-55 to +175	- °C	

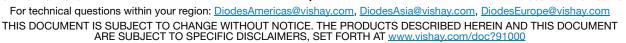
#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{0JA}$ 

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## DESIGN SUPPORT TOOLS AVAILABLE

Anode 1 O

Anode 2 C



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 15 A			
V <sub>RRM</sub>	150 V			
I <sub>FSM</sub>	140 A			
$V_F$ at $I_F$ = 15 A ( $T_A$ = 125 °C)	0.73 V			
T <sub>J</sub> max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			



Cathode

eSMP<sup>®</sup> Series

www.vishay.com

# V30DM150C

RoHS

COMPLIANT

Vishay General Semiconductor

V30DM150C



## Vishay General Semiconductor

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5 A$	T <sub>A</sub> = 25 °C	V <sub>F</sub> (1)	0.72	-	V	
	I <sub>F</sub> = 7.5 A			0.84	-		
	I <sub>F</sub> = 15 A			1.15	1.30		
	$I_F = 5 A$	T <sub>A</sub> = 125 °C		0.57	-		
	I <sub>F</sub> = 7.5 A			0.62	-		
	I <sub>F</sub> = 15 A			0.73	0.8		
Reverse current at rated V <sub>R</sub> per diode	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> (2)	0.01	-	mA	
		T <sub>A</sub> = 125 °C		2	-		
	V <sub>R</sub> = 150 V	T <sub>A</sub> = 25 °C		-	0.2		
		T <sub>A</sub> = 125 °C		4	14		
Typical junction capacitance	4.0 V, 1 MHz		CJ	820	-	pF	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V30DM150C	UNIT	
Typical thermal resistance per device	R <sub>0JC</sub> <sup>(1)</sup>	1.8	°C/W	
	R <sub>0JA</sub> <sup>(1)(3)</sup>	58		

#### Notes

<sup>(1)</sup> Mounted on infinite heatsink

<sup>(2)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$  - junction-to-ambient

<sup>(3)</sup> Free air, without heatsink

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V30DM150C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
V30DM150CHM3/I (1)	0.55	l	2000/reel	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

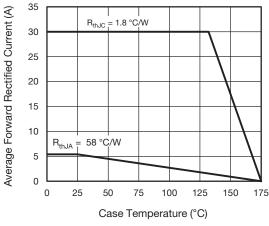
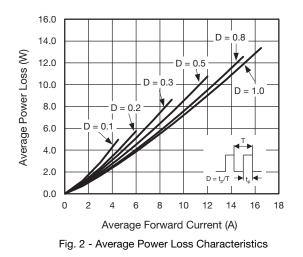
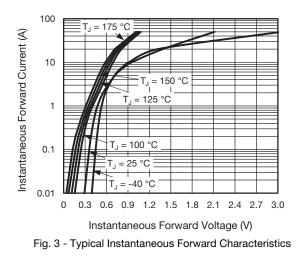


Fig. 1 - Maximum Forward Current Derating Curve





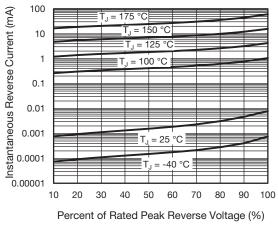
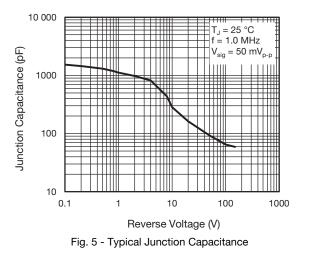
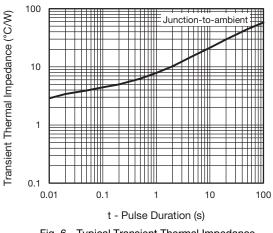


Fig. 4 - Typical Reverse Leakage Characteristics







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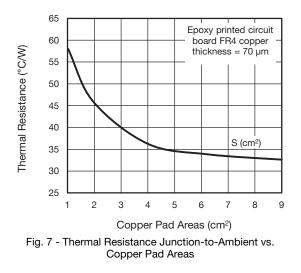
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## V30DM150C

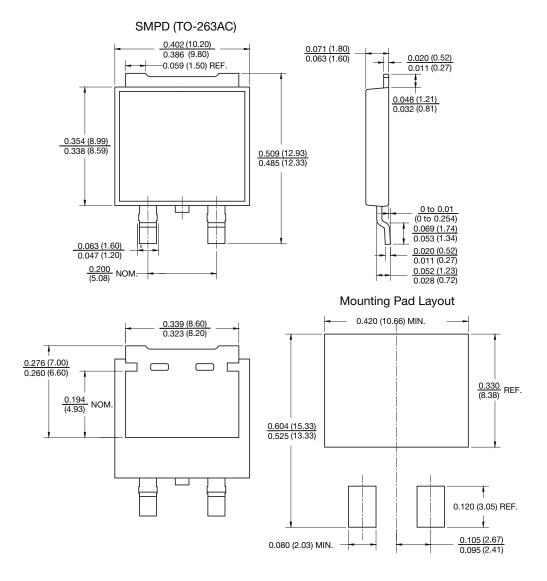




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