



# Low-Voltage, Sub-Ohm, SPDT Analog Switch

## **DESCRIPTION**

The DG2711 is a sub-ohm single-pole/double-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed ( $t_{ON}$ : 25 ns,  $t_{OFF}$ : 14 ns), low on-resistance ( $R_{DS(on)}$ : 0.44  $\Omega$ ) and small physical size (SC70), the DG2711 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG2711 is built on Vishay Siliconix's low voltage submicron CMOS process. An epitaxial layer prevents latchup. Break-before-make is guaranteed for DG2711.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

As a committed partner to the community and the environment, Vishay Siliconix manufactures this product with the lead (Pb)-free device terminations. For analog switching products manufactured with 100 % matte tin device terminations, the lead (Pb)-free "-E3" suffix is being used as a designator.

## **FEATURES**

- Low voltage operation (1.6 V to 3.6 V)
- Low on-resistance  $R_{DS(on)}$ : 0.44  $\Omega$  typ.
- Fast switching t<sub>ON</sub>: 25 ns, t<sub>OFF</sub>: 14 ns
- Low leakage
- TTL/CMOS compatible
- 6-pin SC-70 package
- Compliant to RoHS directive 2002/95/EC

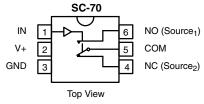
## **BENEFITS**

- · Reduced power consumption
- Simple logic interface
- · High accuracy
- · Reduce board space

## **APPLICATIONS**

- · Cellular phones
- · Communication systems
- · Portable test equipment
- · Battery operated systems
- · Sample and hold circuits

## **FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION**



**Device Marking:** E9xx

TRUTH TABLE						
Logic NC NO						
0	ON	OFF				
1	OFF	ON				

ORDERING INFORMATION							
Temp. Range	Temp. Range Package Part Number						
- 40 to 85 °C	SC70-6	DG2711DL-T1-E3					

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ABSOLUTE MAXIMUM RATINGS						
Parameter	Limit	Unit				
Reference V+ to GND	- 0.3 to + 4	V				
IN, COM, NC, NO <sup>a</sup>	- 0.3 to (V+ + 0.3)	V				
Continuous Current (NO, NC and COM Pins	± 200	mA				
Peak Current (Pulsed at 1 ms, 10 % duty cycle)		± 300	IIIA			
Storage Temperature (D Suffix)		- 65 to 150	°C			
Power Dissipation (Packages) <sup>b</sup>	6-Pin SO70 <sup>c</sup>	250	mW			

### Notes

- a. Signals on NC, NO, or COM or IN exceeding V+ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
- b. All leads welded or soldered to PC board.
- c. Derate 3.1 mW/°C above 70 °C.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

SPECIFICATIONS (V+	= 1.8 V)						
		Test Conditions		Limits			
		Otherwise Unless Specified			°C to 85		
Parameter	Symbol	$V+ = 1.8 \text{ V}, \pm 10 \text{ %}, V_{IN} = 0.4 \text{ V or } 1.0 \text{ V}^e$	Temp. <sup>a</sup>	Min. <sup>b</sup>	Typ. <sup>c</sup>	Max.b	Unit
Analog Switch			1	1	1	1	
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC}, V_{COM}$		Full	0		V+	V
On-Resistance	R <sub>ON</sub>	$V+ = 1.8 \text{ V}, V_{COM} = 0.9 \text{ V}$ $I_{NO}, I_{NC} = 100 \text{ mA}$	Room Full		0.8	2.0 2.5	Ω
Switch Off Lookaga Current	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 2.2 V,		- 1 - 10		1 10	
Switch Off Leakage Current <sup>1</sup>	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 0.2 \text{ V/2 V}$ , $V_{COM} = 2 \text{ V/0.2 V}$	Room Full <sup>d</sup>	- 1 - 10		1 10	nA
Channel-On Leakage Current <sup>f</sup>	I <sub>COM(on)</sub>	$V+ = 2.2 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 0.2 \text{ V/2 V}$	Room Full <sup>d</sup>	- 1 - 10		1 10	
Digital Control							
Input High Voltage	V <sub>INH</sub>		Full	1.0			V
Input Low Voltage	V <sub>INL</sub>		Full			0.4	v
Input Capacitance <sup>d</sup>	C <sub>in</sub>		Full		5		pF
Input Current <sup>f</sup>	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	- 1		1	μΑ
Dynamic Characteristics			•		•		
Turn-On Time <sup>d</sup>	t <sub>ON</sub>	V 04V 15VP 2000 C 25 pF	Room Full <sup>d</sup>		36	60 62	
Turn-Off Time <sup>d</sup>	t <sub>OFF</sub>	$\rm V_{NO}$ or $\rm V_{NC}$ = 1.5 V, $\rm R_L$ = 300 $\Omega,  \rm C_L$ = 35 pF Figures1 and 2	Room Full		22	42 44	ns
Break-Before-Make Time <sup>d</sup>	t <sub>d</sub>		Room	3			
Charge Injection <sup>d</sup>	Q <sub>INJ</sub>	$C_L = 1$ nF, $V_{GEN} = 0$ V, $R_{GEN} = 0$ $\Omega$ , figure 3	Room		20		рC
Off-Isolation <sup>d</sup>	OIRR	$R_1 = 50 \Omega$ , $C_1 = 5 pF$ , $f = 1 MHz$	Room		- 56		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	1 ι – 30 34, Ο	Room		- 56		ub
NO, NC Off Capacitance <sup>d</sup>	C <sub>NO(off)</sub> C <sub>NC(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		73		pF
Channel-On Capacitanced	C <sub>ON</sub>				167		1





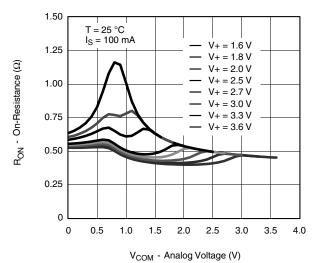
		Test Conditions Otherwise Unless Specified		<b>Limits</b> - 40 °C to 85 °C		5 °C	
Parameter	Symbol	$V+ = 3 V$ , $\pm 10 \%$ , $V_{IN} = 0.5 V$ or 1.4 $V^e$	Temp.a	Min.b	Typ.c	Max.b	Uni
Analog Switch							
Analog Signal Range <sup>d</sup>	$V_{NO}, V_{NC}, V_{COM}$		Full	0		V+	٧
On-Resistance	R <sub>ON</sub>	V+ = 2.7 V, V <sub>COM</sub> = 1.5 V I <sub>NO</sub> , I <sub>NC</sub> = 100 mA	Room Full		0.44	0.6 0.7	
R <sub>ON</sub> Flatness	R <sub>ON</sub> Flatness	V+ = 2.7 V, V <sub>COM</sub> = 0.6 V, 1.5 V I <sub>NO</sub> , I <sub>NC</sub> = 100 mA	Room		0.14	0.2	Ω
R <sub>ON</sub> Match	ΔR <sub>ON</sub>	V+ = 2.7 V, V <sub>COM</sub> = 1.5 V I <sub>NO</sub> , I <sub>NC</sub> = 100 mA	Room			0.07	
Switch Off Leakage Current	I <sub>NO(off)</sub> I <sub>NC(off)</sub>	V+ = 3.3 V,	Room Full	- 1 - 10		1 10	
Owner on Leakage Guilen	I <sub>COM(off)</sub>	$V_{NO}$ , $V_{NC} = 0.3 \text{ V/3 V}$ , $V_{COM} = 3 \text{ V/0.3 V}$	Room Full	- 1 - 10		1 10	nA
Channel-On Leakage Current	I <sub>COM(on)</sub>	$V+ = 3.3 \text{ V}, V_{NO}, V_{NC} = V_{COM} = 0.3 \text{ V/3 V}$	Room Full	- 1 - 10		1 10	
Digital Control							
Input High Voltage	$V_{INH}$		Full	1.4			V
Input Low Voltage	V <sub>INL</sub>		Full			0.5	
Input Capacitance <sup>d</sup>	C <sub>in</sub>		Full		5		р
Input Current <sup>f</sup>	I <sub>INL</sub> or I <sub>INH</sub>	$V_{IN} = 0$ or V+	Full	- 1		1	μ
Dynamic Characteristics							
Turn-On Time	t <sub>ON</sub>	$V_{NO}$ or $V_{NC}$ = 1.5 V, $R_L$ = 300 $\Omega$ , $C_L$ = 35 pF	Room Full		25	46 48	
Turn-Off Time	t <sub>OFF</sub>	figures 1 and 2	Room Full		14	38 40	ns
Break-Before-Make Time	t <sub>d</sub>		Room	1			
Charge Injection <sup>d</sup>	$Q_{INJ}$	$C_L = 1 \text{ nF}, V_{GEN} = 0 \text{ V}, R_{GEN} = 0 \Omega$ , figure 3	Room		28		р
Off-Isolation <sup>d</sup>	OIRR	$R_1 = 50 \Omega$ , $C_1 = 5 pF$ , $f = 1 MHz$	Room		- 56		dl
Crosstalk <sup>d</sup>	X <sub>TALK</sub>	11 <u>2</u> = 30 1 <u>2</u> , 3 <u>2</u> = 3 p1, 1 = 1 m1.2	Room		- 56		ď
NO, NC Off Capacitance <sup>d</sup>	C <sub>NO(off)</sub> C <sub>NC(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		70		р
Channel-On Capacitance <sup>d</sup>	C <sub>ON</sub>		Room		163		
Power Supply							
Power Supply Range	V+			1.6		3.6	١
Power Supply Current	l+	$V+ = 3.6 \text{ V}, V_{1N} = 0 \text{ or } V+$			0.01	1.0	μ

## Notes:

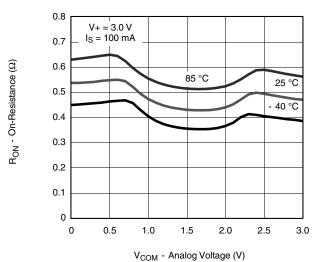
- a. Room = 25  $^{\circ}$ C, full = as determined by the operating suffix.
- b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- c. Typical values are for design aid only, not guaranteed nor subject to production testing.
- d. Guarantee by design, nor subjected to production test.
- e. V<sub>IN</sub> = input voltage to perform proper function.
- f. Guaranteed by 3 V leakage testing, not production tested.

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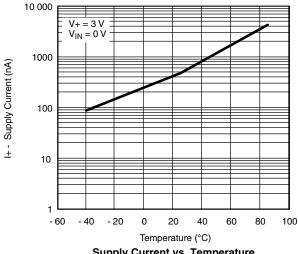
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



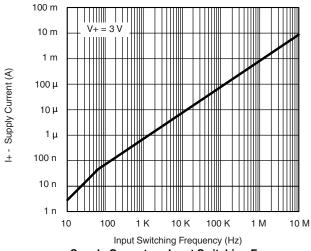
R<sub>ON</sub> vs. V<sub>COM</sub> and Single Supply Voltage



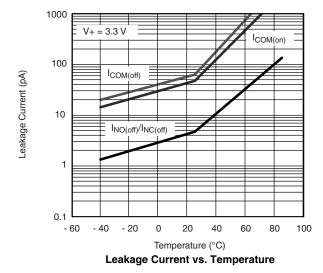
R<sub>ON</sub> vs. Analog Voltage and Temperature

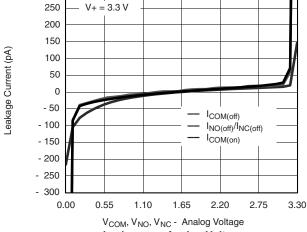


**Supply Current vs. Temperature** 



**Supply Current vs. Input Switching Frequency** 

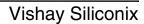




300

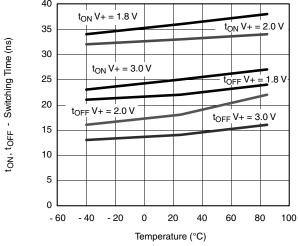
Leakage vs. Analog Voltage



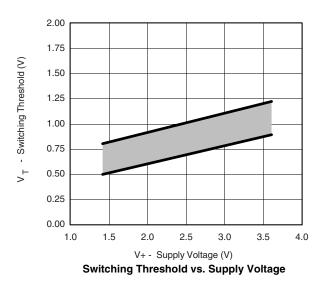




## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

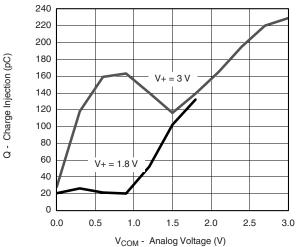


Switching Time vs. Temperature



10 0 LOSS - 10 Loss, OIRR, X<sub>TALK</sub> (dB) - 20 - 30 - 40 - 50  $\begin{array}{l} V+=3~V \\ R_L=50~\Omega \end{array}$ - 60 - 70 - 80 - 90 100 K 100 M 1 M 10 M 1 G Frequency (Hz)

Insertion Loss, Off-Isolation, Crosstalk vs. Frequency



Charge Injection vs. Analog Voltage

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## **TEST CIRCUITS**

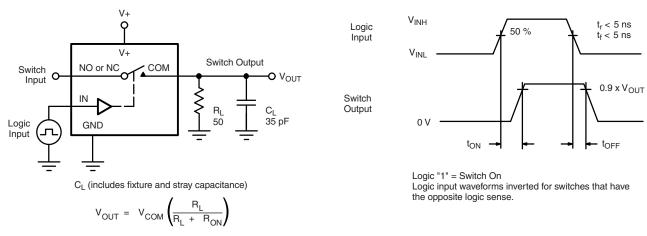


Figure 1. Switching Time

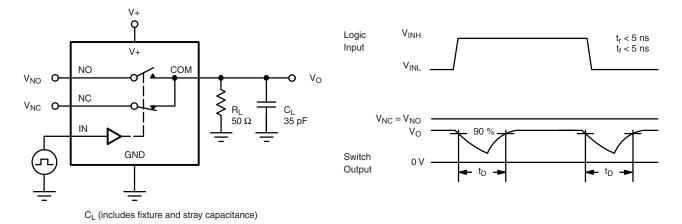


Figure 2. Break-Before-Make Interval

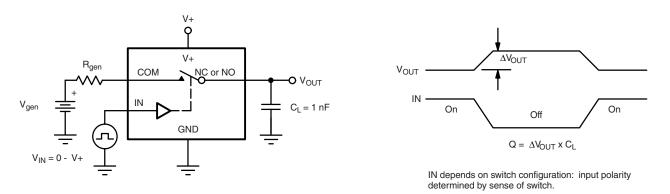


Figure 3. Charge Injection



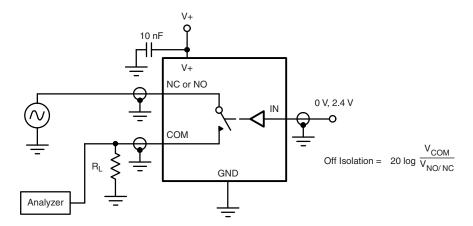


Figure 4. Off-Isolation

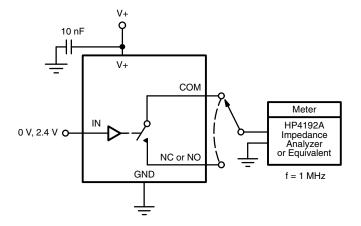


Figure 5. Channel Off/On Capacitance

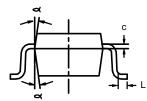
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## SC-70: 6-LEADS





	MIL	LIMET	ERS	I	S		
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.90	-	1.10	0.035	-	0.043	
<b>A</b> <sub>1</sub>	_	-	0.10	_	_	0.004	
A <sub>2</sub>	0.80	-	1.00	0.031	_	0.039	
b	0.15	0.15 - 0.30 0.006		0.006	_	0.012	
С	0.10	-	0.25	0.004	-	0.010	
D	1.80	2.00	2.20	0.071	0.079	0.087	
Е	1.80	2.10	2.40	0.071	0.083	0.094	
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053	
е		0.65BSC			0.026BSC	;	
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055	
L	0.10	0.20	0.30	0.004	0.008	0.012	
۵	7°Nom 7°Nom						
ECN: S-03946—Rev. B, 09-Jul-01							

DWG: 5550



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