

## Low-Voltage Single SPDT Analog Switch

### DESCRIPTION

The DG9411 is a single-pole/double-throw monolithic CMOS analog switch designed for high performance switching of analog signals. Combining low power, high speed ( $t_{ON}$ : 9 ns,  $t_{OFF}$ : 5 ns), low on-resistance ( $r_{DS(on)}$ : 7  $\Omega$ ) and small physical size (SC70), the DG9411 is ideal for portable and battery powered applications requiring high performance and efficient use of board space.

The DG9411 is built on Vishay Siliconix's low voltage J12 process. An epitaxial layer prevents latchup. Break-before-make is guaranteed for DG9411.

Each switch conducts equally well in both directions when on, and blocks up to the power supply level when off.

### FEATURES

- Low voltage operation (2.25 V to 5.5 V)
- Low on-resistance -  $r_{DS(on)}$ : 7  $\Omega$
- Fast switching -  $t_{ON}$ : 9 ns,  $t_{OFF}$ : 5 ns
- Low charge injection -  $Q_{INJ}$ : 5 pC
- Low power consumption
- TTL/CMOS compatible
- 6-Pin SC70 package



Available  
**RoHS\***  
COMPLIANT

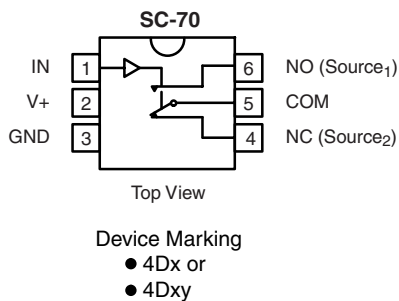
### BENEFITS

- Reduced power consumption
- Simple logic interface
- High accuracy
- Reduce board space

### APPLICATIONS

- Cellular phones
- Communication systems
- Portable test equipment
- Battery operated systems
- Sample and hold circuits

### FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION



### TRUTH TABLE

Logic	NC	NO
0	ON	OFF
1	OFF	ON

Logic "0"  $\leq$  0.8 V

Logic "1"  $\geq$  2.4 V

### ORDERING INFORMATION

Temp Range	Package	Part Number
- 40 to 85 $^{\circ}$ C	SC70-6	DG9411DL-T1 DG9411DL-T1-E3

\* Pb containing terminations are not RoHS compliant, exemptions may apply.

**ABSOLUTE MAXIMUM RATINGS**

Parameter	Limit	Unit
Reference V <sub>+</sub> to GND	- 0.3 to + 6	V
IN, COM, NC, NO <sup>a</sup>	- 0.3 to (V <sub>+</sub> + 0.3)	
Continuous Current (Any Terminal)	± 50	mA
Peak Current (Pulsed at 1 ms, 10 % duty cycle)	± 200	
Storage Temperature	- 65 to 150	°C
Power Dissipation (Packages) <sup>b</sup>	6-Pin SC70 <sup>c</sup>	mW
	250	

Notes:

a. Signals on NC, NO, or COM or IN exceeding V<sub>+</sub> will be clamped by internal diodes. Limit forward diode current to maximum current ratings.

b. All leads welded or soldered to PC Board.

c. Derate 3.1 mW/°C above 70 °C.

**SPECIFICATIONS V<sub>+</sub> = 2.5 V**

Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 2.5 V, ± 10 % VIN = 0.4 or 2.0 V <sup>e</sup>	Temp <sup>a</sup>	Limits - 40 to 85 °C			Unit
				Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	
Analog Switch							
Analog Signal Range <sup>d</sup>	V <sub>NO</sub> , V <sub>NC</sub> V <sub>COM</sub>		Full	0		V+	V
Drain-Source On-Resistance	r <sub>DS(on)</sub>	V+ = 2.25 V, V <sub>D</sub> = 1.0 V, I <sub>S</sub> = 10 mA	Room Full <sup>d</sup>		26 29	35 40	Ω
r <sub>DS(on)</sub> Flatness <sup>d</sup>	r <sub>DS(on)</sub> Flatness	V+ = 2.5 V	Room		10		
Switch Off Leakage Current <sup>f</sup>	I <sub>S(off)</sub>	V+ = 2.75 V, V <sub>S</sub> = 0.5 V/1.5 V, V <sub>D</sub> = 1.5 V/0.5 V	Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
	I <sub>D(off)</sub>		Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
Channel-On Leakage Current <sup>f</sup>	I <sub>D(on)</sub>	V+ = 2.75 V, V <sub>S</sub> = V <sub>D</sub> = 0.5 V/1.5 V	Room Full <sup>d</sup>	- 250 - 3.0		250 3.0	pA nA
Digital Control							
Input High Voltage	V <sub>INH</sub>		Full	2			V
Input Low Voltage	V <sub>INL</sub>		Full			0.4	
Input Capacitance <sup>d</sup>	C <sub>in</sub>		Full		3		pF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time	t <sub>ON</sub>	V <sub>D</sub> or V <sub>S</sub> = 1.5 V, R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF Figures 1 and 2	Room Full <sup>d</sup>		16	40 45	ns
Turn-Off Time	t <sub>OFF</sub>		Room Full		7	23 28	
Break-Before-Make Time	t <sub>d</sub>		Room <sup>d</sup>	1	12		
Charge Injection <sup>d</sup>	Q <sub>INJ</sub>	C <sub>L</sub> = 1 nF, V <sub>GEN</sub> = 0 V, V <sub>S</sub> = 0 V, R <sub>GEN</sub> = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation <sup>d</sup>	OIRR	R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz	Room		- 73		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>		Room		- 70		
Source-Off Capacitance <sup>d</sup>	C <sub>S(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		7		pF
Channel-On Capacitance <sup>d</sup>	C <sub>D(on)</sub>		Room		20		
Drain-to-Source Capacitance <sup>d</sup>	C <sub>DS(off)</sub>		Room		20		
Power Supply							
Power Supply Range	V+			2.25		2.75	V
Power Supply Current <sup>d</sup>	I+	V <sub>IN</sub> = 0 or V+			0.01	1.0	μA
Power Consumption	P <sub>C</sub>					0.3	μW



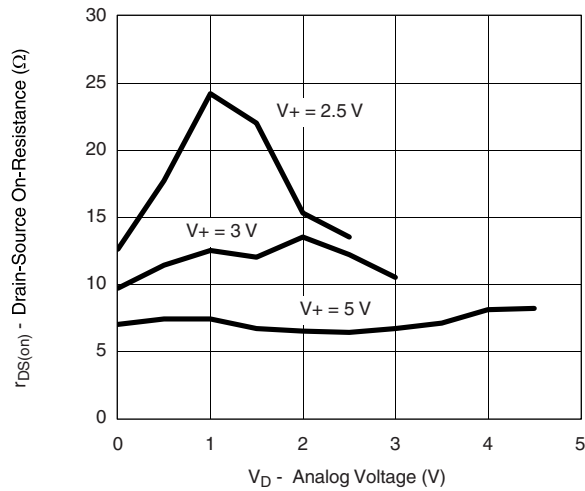
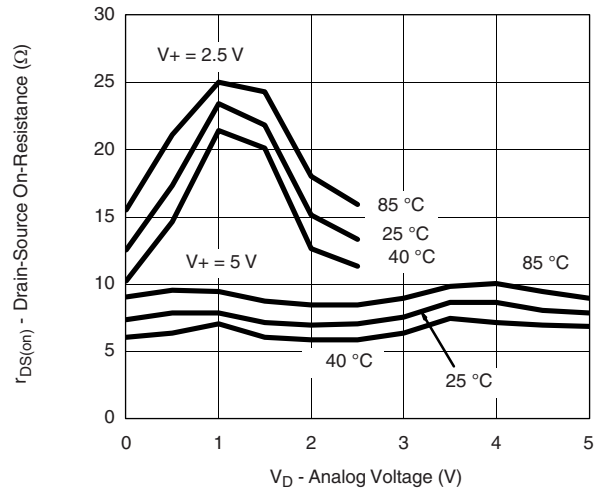
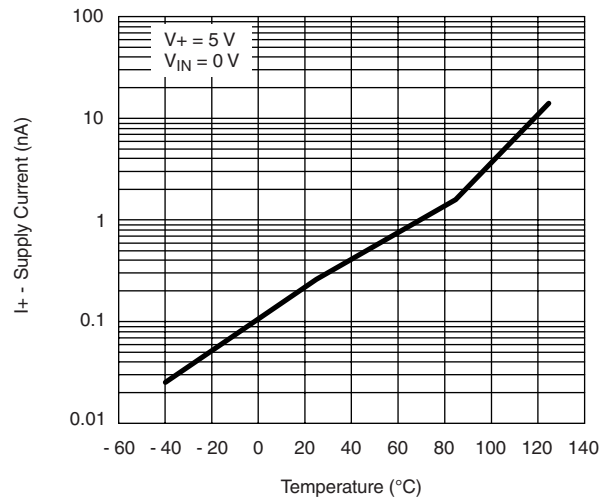
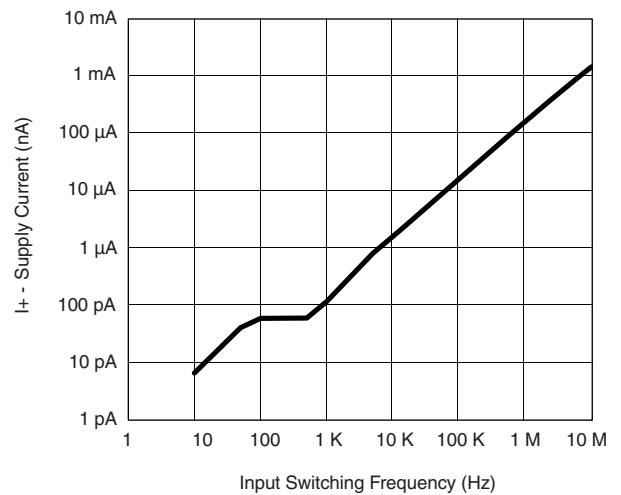
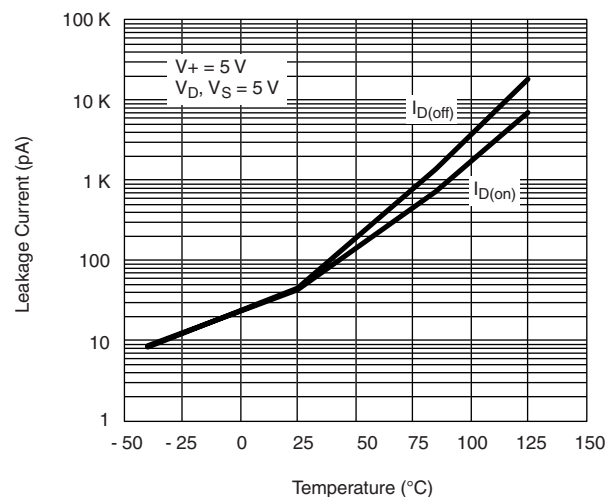
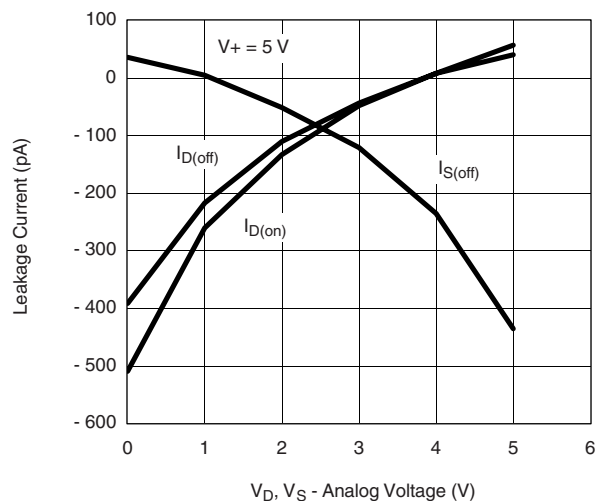
SPECIFICATIONS V+ = 3 V							
Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 3 V, ± 10 % VIN = 0.4 or 2.0 V <sup>e</sup>	Temp <sup>a</sup>	Limits - 40 to 85 °C			Unit
				Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	
Analog Switch							
Analog Signal Range <sup>d</sup>	V <sub>NO</sub> , V <sub>NC</sub> V <sub>COM</sub>		Full	0		V+	V
Drain-Source On-Resistance <sup>d</sup>	r <sub>DS(on)</sub>	V+ = 2.7 V, V <sub>D</sub> = 1.5 V, I <sub>S</sub> = 10 mA	Room Full		15 19	25 30	Ω
r <sub>DS(on)</sub> Flatness <sup>d</sup>	r <sub>DS(on)</sub> Flatness	V <sub>S</sub> = 0 to V+, I <sub>S</sub> = 10 mA	Room		7.5		
Switch Off Leakage Current <sup>f</sup>	I <sub>S(off)</sub>	V+ = 3.3 V, V <sub>S</sub> = 1 V/3 V, V <sub>D</sub> = 3 V/1 V	Room Full	- 500 - 4.0		500 4.0	pA nA
	I <sub>D(off)</sub>		Room Full	- 500 - 4.0		500 4.0	pA nA
Channel-On Leakage Current <sup>f</sup>	I <sub>D(on)</sub>	V+ = 3.3 V, V <sub>S</sub> = V <sub>D</sub> = 1 V/3 V	Room Full	- 500 - 4.0		500 4.0	pA nA
Digital Control							
Input High Voltage	V <sub>INH</sub>		Full	2			V
Input Low Voltage	V <sub>INL</sub>		Full			0.8	
Input Capacitance <sup>d</sup>	C <sub>in</sub>		Full		3		pF
Input Current	I <sub>INL</sub> or I <sub>INH</sub>	V <sub>IN</sub> = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time <sup>d</sup>	t <sub>ON</sub>	V <sub>D</sub> or V <sub>S</sub> = 2.0 V, R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF Figures1 and 2	Room Full		12	15 20	ns
Turn-Off Time <sup>d</sup>	t <sub>OFF</sub>		Room Full		6	8 10	
Break-Before-Make Time <sup>d</sup>	t <sub>d</sub>		Room	1	7		
Charge Injection <sup>d</sup>	Q <sub>INJ</sub>	C <sub>L</sub> = 1 nF, V <sub>GEN</sub> = 0 V, V <sub>S</sub> = 0 V, R <sub>GEN</sub> = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation <sup>d</sup>	OIRR	R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz	Room		- 73		dB
Crosstalk <sup>d</sup>	X <sub>TALK</sub>		Room		- 70		
Source-Off Capacitance <sup>d</sup>	C <sub>S(off)</sub>	V <sub>IN</sub> = 0 or V+, f = 1 MHz	Room		7		pF
Channel-On Capacitance <sup>d</sup>	C <sub>D(on)</sub>		Room		20		
Drain-to-Source Capacitance <sup>d</sup>	C <sub>DS(off)</sub>		Room		20		
Power Supply							
Power Supply Range	V+			2.7		3.3	V
Power Supply Current	I+	V <sub>IN</sub> = 0 or V+			0.01	1.0	μA
Power Consumption	P <sub>C</sub>					0.4	μW

SPECIFICATIONS V+ = 5 V							
Parameter	Symbol	Test Conditions Unless Otherwise Specified V+ = 5 V, ± 10 % VIN = 0.8 or 2.4 V <sup>e</sup>	Temp <sup>a</sup>	Limits - 40 to 85 °C			Unit
				Min <sup>b</sup>	Typ <sup>c</sup>	Max <sup>b</sup>	
Analog Switch							
Analog Signal Range <sup>d</sup>	VNO, VNC VCOM		Full	0		V+	V
Drain-Source On-Resistance	rDS(on)	V+ = 4.5 V, VD = 3 V, IS = 10 mA	Room Full		7 10	12 16	Ω
rDS(on) Flatness <sup>d</sup>	rDS(on) Flatness	V+ = 2.5 V	Room		2		
Switch Off Leakage Current	IS(off)	V+ = 5.5 V, VS = 1 V/4.5 V, VD = 4.5 V/1 V	Room Full	- 1.0 - 4.0		1.0 4.0	nA
	ID(off)		Room Full	- 1.0 - 4.0		1.0 4.0	
Channel-On Leakage Current	ID(on)	V+ = 5.5 V, VS = VD = 1 V/4.5 V	Room Full	- 1.0 - 3.0		1.0 4.5	
Digital Control							
Input High Voltage	VINH		Full	2.4			V
Input Low Voltage	VINL		Full			0.8	
Input Capacitance	Cin		Full		3		pF
Input Current	IINL or IINH	VIN = 0 or V+	Full	- 1		1	μA
Dynamic Characteristics							
Turn-On Time <sup>d</sup>	tON	VD or VS = 3 V, RL = 300 Ω, CL = 35 pF Figure 1 and 2	Room Full		9	11 15	ns
Turn-Off Time <sup>d</sup>	tOFF		Room Full		5	7 9	
Break-Before-Make Time <sup>d</sup>	td		Room	1	4		
Charge Injection <sup>d</sup>	QINJ	CL = 1 nF, VS = 0 V, VGEN = 0 V, RGEN = 0 Ω, Figure 3	Room		5	10	pC
Off-Isolation <sup>d</sup>	OIRR	RL = 50 Ω, CL = 5 pF, f = 1 MHz	Room		- 73		dB
Crosstalk <sup>d</sup>	XTALK		Room		- 70		
Source-Off Capacitance <sup>d</sup>	CS(off)	VIN = 0 or V+, f = 1 MHz	Room		7		pF
Channel-On Capacitance <sup>d</sup>	CD(on)		Room		20		
Drain-to-Source Capacitance <sup>d</sup>	CDS(off)		Room		20		
Power Supply							
Power Supply Range	V+			4.5		5.5	V
Power Supply Current	I+	VIN = 0 or V+			0.01	1.0	μA
Power Consumption	PC					0.6	μW

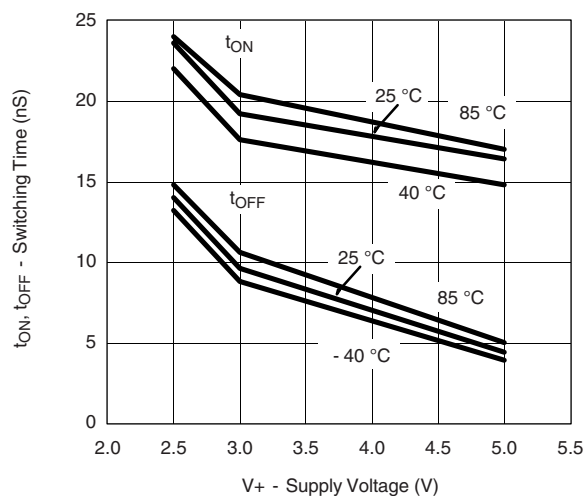
## Notes:

- Room = 25 °C, Full = as determined by the operating suffix.
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this datasheet.
- Typical values are for design aid only, not guaranteed nor subject to production testing.
- Guarantee by design, nor subjected to production test.
- $V_{IN}$  = input voltage to perform proper function.
- Guaranteed by 5 V leakage testing, not production tested.

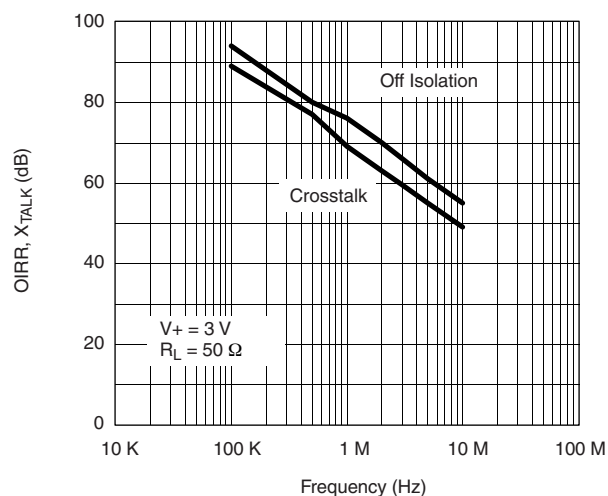
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

 **$r_{DS(on)}$  vs. Analog and Power Voltage**

 **$r_{DS(on)}$  vs. Analog Voltage and Temperature**

**Supply Current vs. Temperature**

**Supply Current vs. Input Switching Frequency**

**Leakage Current vs. Temperature**

**Leakage vs. Analog Voltage**

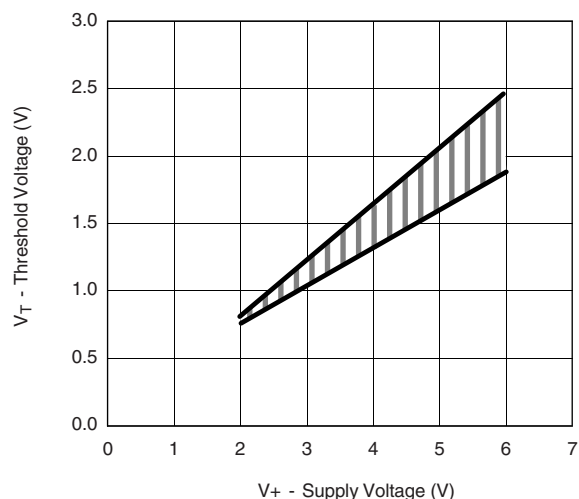
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



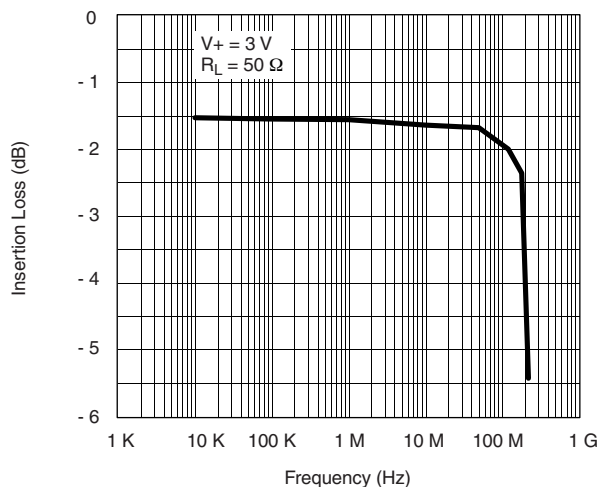
Switching Time vs. Temperature and Supply Voltage



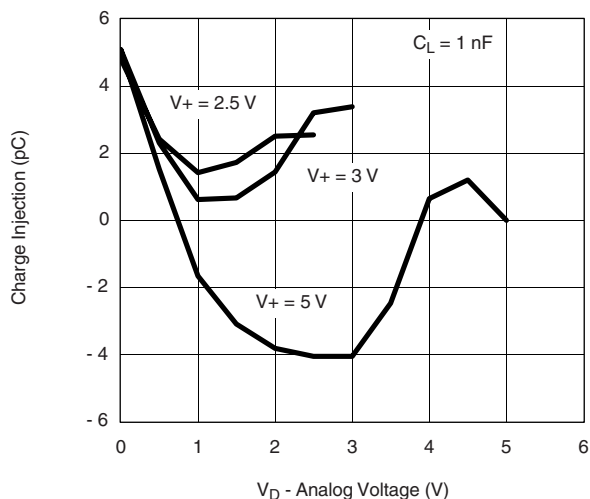
Crosstalk and Off Isolation vs. Frequency



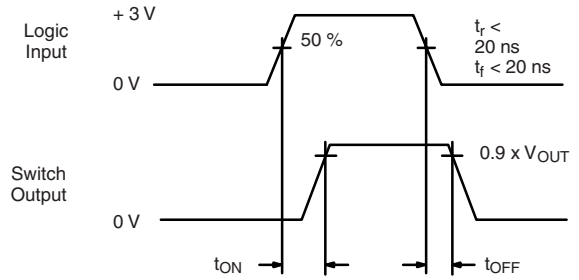
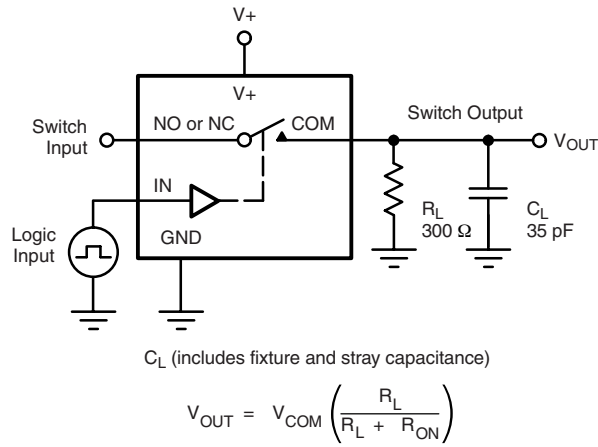
Input Switching Threshold vs. Supply Voltage



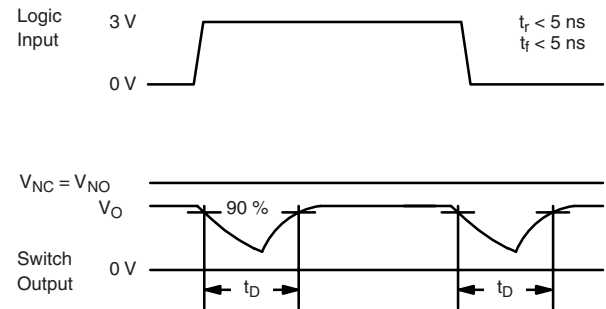
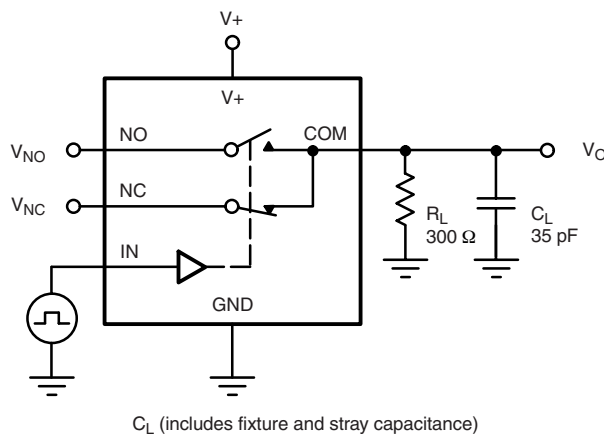
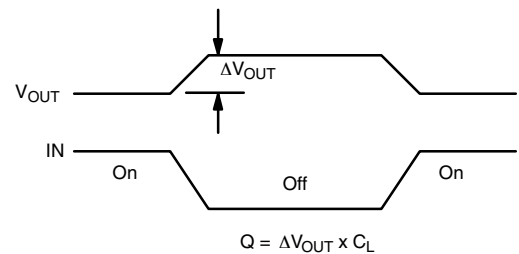
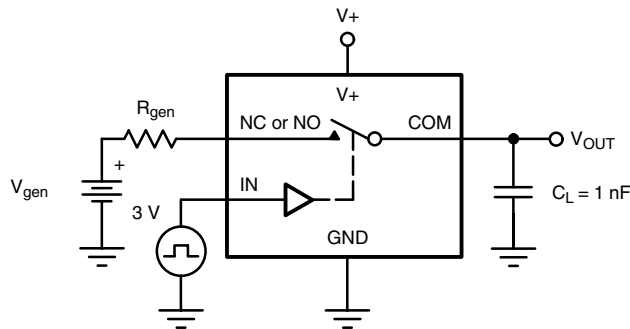
Insertion Loss vs. Frequency



Charge Injection vs. Analog Voltage

**TEST CIRCUITS**


Logic "1" = Switch On  
Logic input waveforms inverted for switches that have the opposite logic sense.

**Figure 1. Switching Time**

**Figure 2. Break-Before-Make Interval**


IN depends on switch configuration: input polarity determined by sense of switch.

**Figure 3. Charge Injection**

## TEST CIRCUITS

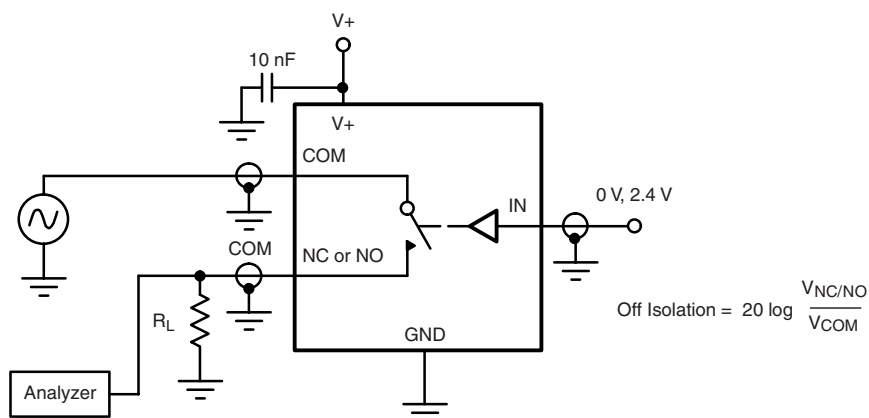


Figure 4. Off-Isolation

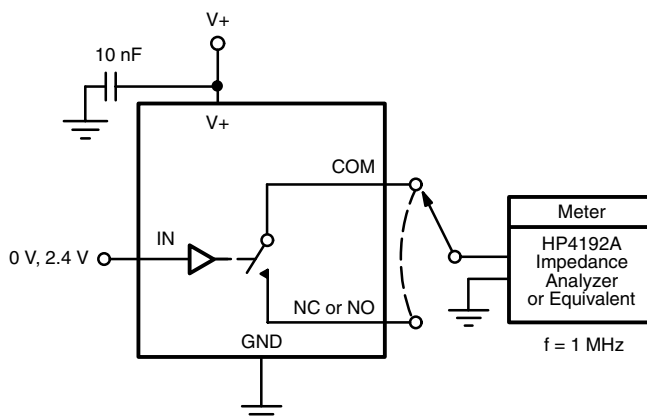
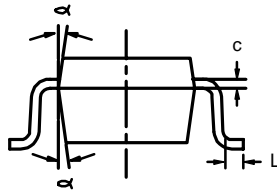
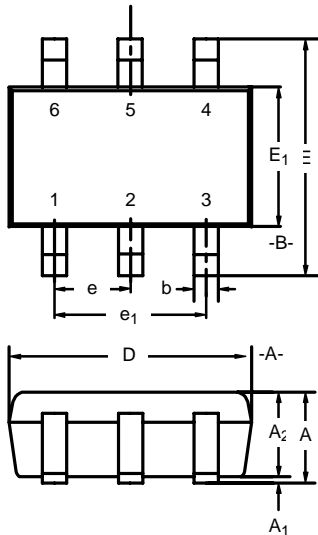


Figure 5. Channel Off/On Capacitance

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <http://www.vishay.com/ppg?71347>.



### SC-70: 6-LEADS



	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
A	0.90	—	1.10	0.035	—	0.043
A <sub>1</sub>	—	—	0.10	—	—	0.004
A <sub>2</sub>	0.80	—	1.00	0.031	—	0.039
b	0.15	—	0.30	0.006	—	0.012
c	0.10	—	0.25	0.004	—	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
E	1.80	2.10	2.40	0.071	0.083	0.094
E <sub>1</sub>	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65BSC			0.026BSC		
e <sub>1</sub>	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
α	7°Nom			7°Nom		
ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5550						



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