



# P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)			
- 30	0.042 at V <sub>GS</sub> = - 10 V	- 6	7 nC			
- 30	0.072 at V <sub>GS</sub> = - 4.5 V	- 6	7110			

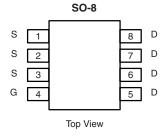
### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



### **APPLICATIONS**

- Load Switch
- · Notebook Adaptor Switch





P-Channel MOSFET

Ordering Information: Si4485DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20		
	T <sub>C</sub> = 25 °C		- 6 <sup>e</sup>		
Continuous Drain Current (T, = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	- 6 <sup>e</sup>		
Continuous Dialii Curient (1, = 150°C)	T <sub>A</sub> = 25 °C		- 5.9 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		- 4.7 <sup>b, c</sup>	A	
Pulsed Drain Current	I <sub>DM</sub>	- 25			
Continous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	- 4.2		
Continous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 2 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		5		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	3.2	w	
	T <sub>A</sub> = 25 °C		2.4 <sup>b, c</sup>	v	
	T <sub>A</sub> = 70 °C		1.5 <sup>b, c</sup>		
Operating Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	42	53	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	19	25			

### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. t = 10 s.
- d. Maximum under Steady State conditions is 85  $^{\circ}\text{C/W}.$
- e. Package Limited.

## **Si4485DY**

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<b>SPECIFICATIONS</b> $T_J = 25  ^{\circ}\text{C}$ , Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	Symbol	rest conditions	IVIIII.	тур.	IVIAA.	Oilit	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 19		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	- I <sub>D</sub> = - 250 μA		4.4			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = -250 \mu A$	- 1.2		- 2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 25			Α	
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5.9 A		0.035	0.042		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 4.5 A		0.060	0.072	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5.9 A		10		S	
Dynamic <sup>b</sup>	l	,		l	l		
Input Capacitance	C <sub>iss</sub>			590			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		115		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	1		93			
Table Carte Observes		V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5.9 A		13.6	21	nC	
Total Gate Charge	Q <sub>g</sub>			7	11		
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5.9 \text{ A}$		2.3			
Gate-Drain Charge	$Q_{gd}$			3.2			
Gate Resistance	$R_g$	f = 1 MHz	1	5	10	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			30	45		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 3.2 $\Omega$		25	38		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -4.7 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		16	24		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			8	16	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 3.2 $\Omega$		10	20	- - -	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -4.7 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		18	27		
Fall Time	t <sub>f</sub>			8	16		
<b>Drain-Source Body Diode Characteristi</b>	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.2	Α	
Pulse Diode Forward Current	I <sub>SM</sub>				- 25		
Body Diode Voltage	$V_{SD}$	$I_S = -4.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			17	26	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = - 4.7 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		9	18	nC	
Reverse Recovery Fall Time t <sub>a</sub>		i <sub>F</sub> = -τ.7 Λ, αι/αι = 100 Λ/μο, 1J = 25 °C		10		ne	
Reverse Recovery Rise Time	t <sub>b</sub>		· · · · · · · · · · · · · · · · · · ·	7		ns	

### Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

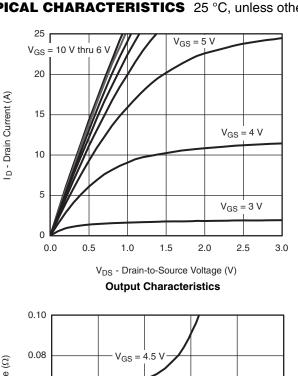
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

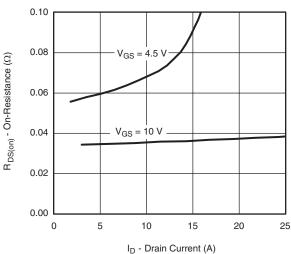
b. Guaranteed by design, not subject to production testing.

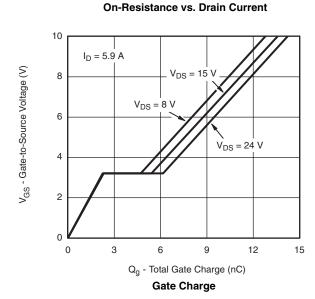


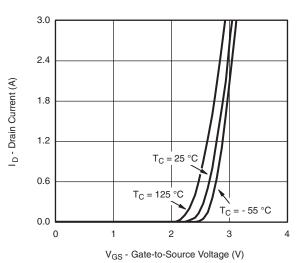


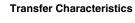
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

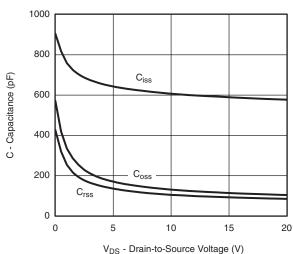




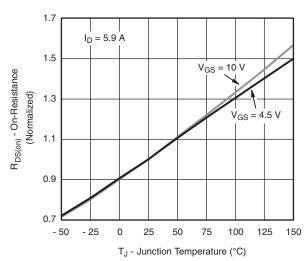








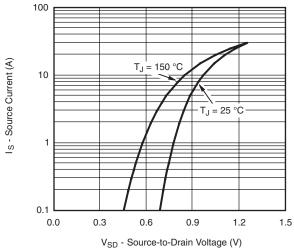
Capacitance



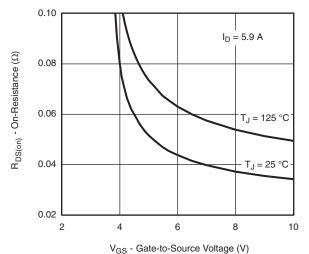
On-Resistance vs. Junction Temperature

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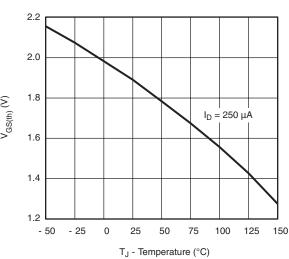
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



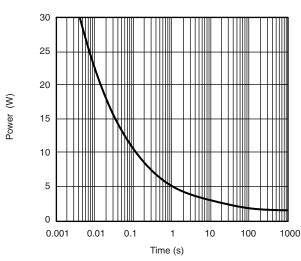
Source-Drain Diode Forward Voltage



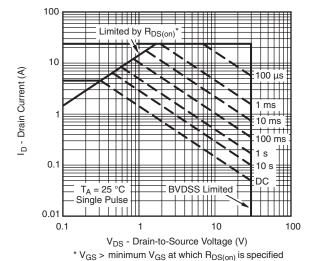
On-Resistance vs. Gate-to-Source Voltage







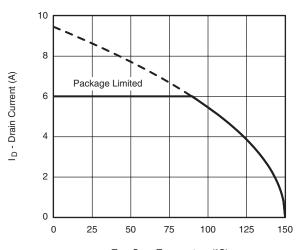
Single Pulse Power (Junction-to-Ambient)



Safe Operating Area, Junction-to-Ambient



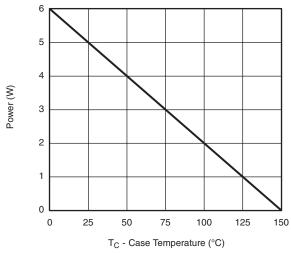
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

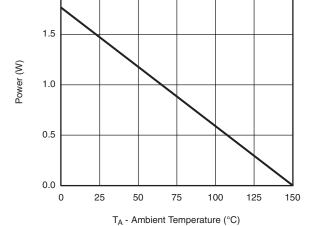


 $T_{\mbox{\scriptsize C}}$  - Case Temperature (°C)

### **Current Derating\***

2.0





Power, Junction-to-Foot

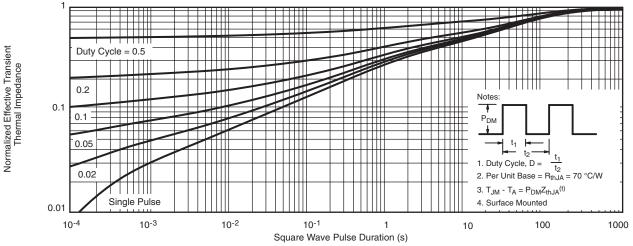
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

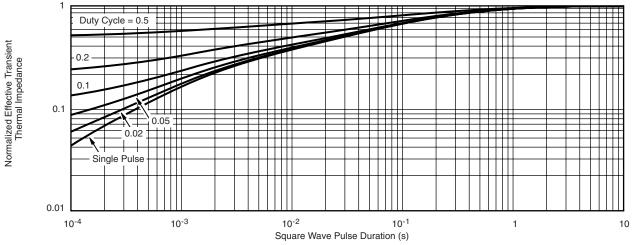
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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppq?64989">www.vishay.com/ppq?64989</a>.



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



### **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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