



# N Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>(BR)DSS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)			
100	0.0082 at V <sub>GS</sub> = 10 V	90 <sup>d</sup>	97			

TO-263

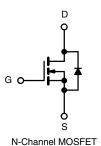
### **FEATURES**

- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature
- 100 %  $R_g$  and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



### **APPLICATIONS**

- Power Supply
  - Secondary Synchronous Rectification
- Industrial
- Primary Switch



Ordering Information: SUM90N10-8m2P-E3 (Lead (Pb)-free)

Top View

<b>ABSOLUTE MAXIMUM RATINGS</b>	$T_C = 25  ^{\circ}C$ , unless o	therwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	100	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I-	90 <sup>d</sup>	
Continuous Diam Guitent (1j = 173 G)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	90 <sup>d</sup>	
Pulsed Drain Current	I <sub>DM</sub>	240		
Avalanche Current		I <sub>AS</sub>	60	
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	180	mJ
Mariana Barra Biraira di and	T <sub>C</sub> = 25 °C	В	300 <sup>b</sup>	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C <sup>c</sup>	$ P_D$ $-$	3.75	VV
Operating Junction and Storage Temperature Ra	inge	T <sub>J</sub> , T <sub>stq</sub>	- 55 to 175	°C

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Limit	Unit			
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	40	°C/W			
Junction-to-Case (Drain)	R <sub>thJC</sub>	0.5	C/VV			

### Notes:

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.

## SUM90N10-8m2P

# Vishay Siliconix



<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, unless otherwise noted)								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{DS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	100			V		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2.5		4.5	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 250	nA		
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μА		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			50			
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			250			
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	70			Α		
D : 0	D	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0067	0.0082	0		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A, T <sub>J</sub> = 125 °C		0.0127	0.0170	Ω		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 20 A		62		S		
Dynamic <sup>b</sup>	•							
Input Capacitance	C <sub>iss</sub>			6290		pF		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 50 \text{ V}, f = 1 \text{ MHz}$		535				
Reverse Transfer Capacitance	C <sub>rss</sub>			182				
Total Gate Charge <sup>c</sup>	$Q_g$			97	150	nC		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 85 \text{ A}$		32				
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			25				
Gate Resistance	$R_{g}$	f = 1 MHz	0.28	1.4	2.8	Ω		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			23	35			
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 0.588 \Omega$		17	26	ns		
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong 85 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		34	52			
Fall Time <sup>c</sup>	t <sub>f</sub>			9	18			
Source-Drain Diode Ratings and Characteristics (T <sub>C</sub> = 25 °C) <sup>b</sup>								
Continuous Current	Is				85			
Pulsed Current	I <sub>SM</sub>				240	Α		
Forward Voltage <sup>a</sup>	$V_{SD}$	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		0.85	1.5	V		
Reverse Recovery Time	t <sub>rr</sub>			61	100	ns		
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 75 A, di/dt = 100 A/μs		3	4.5	Α		
Reverse Recovery Charge	Q <sub>rr</sub>			91	130	μC		

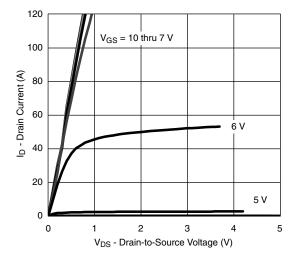
### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

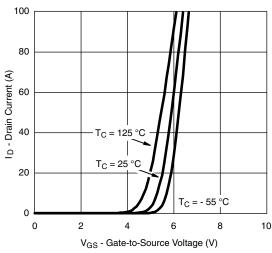
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



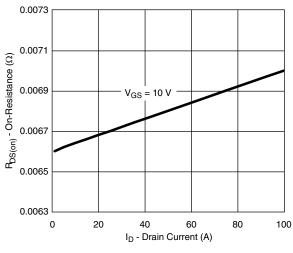
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



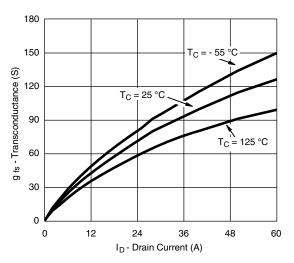
### **Output Characteristics**



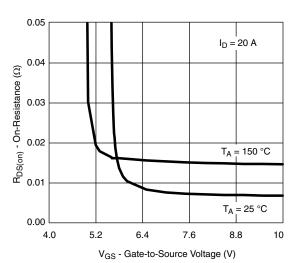
**Transfer Characteristics** 



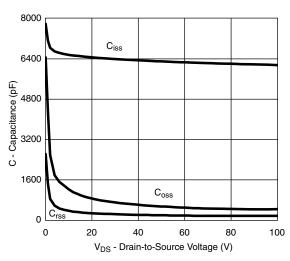
On-Resistance vs. Drain Current



Transconductance



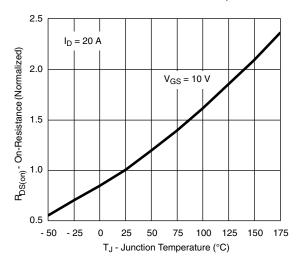
On-resistance vs. Gate-to-Source Voltage



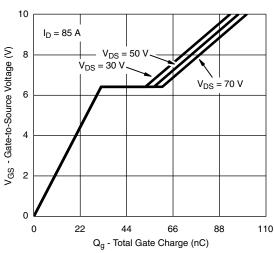
Capacitance

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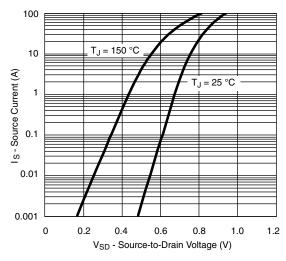
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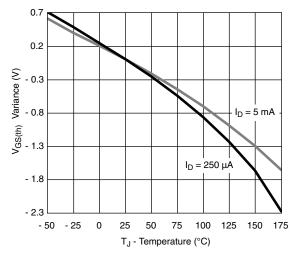
On-Resistance vs. Junction Temperature



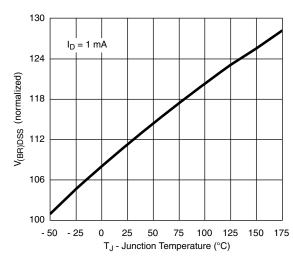
**Gate Charge** 



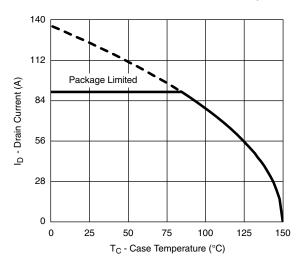
Source-Drain Diode Forward Voltage



**Threshold Voltage** 



Drain Source Breakdown vs. Junction Temperature

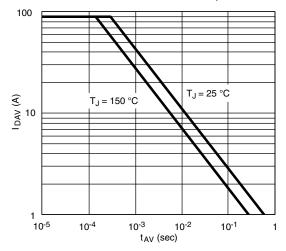


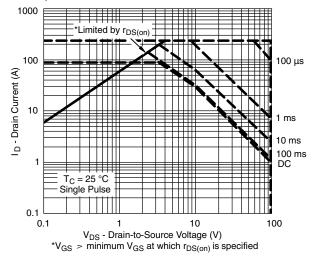
**Maximum Drain Current vs. Case Temperature** 



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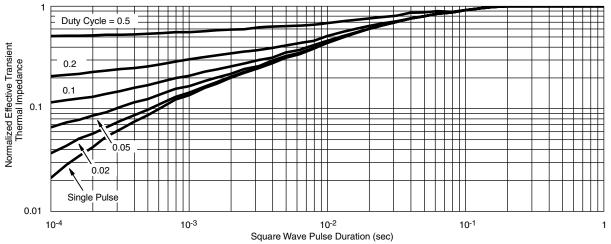
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Single Pulse Avalanche Current Capability vs. Time



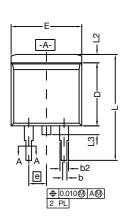


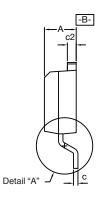
Normalized Thermal Transient Impedance, Junction-to-Case

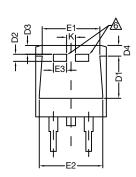
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# TO-263 (D<sup>2</sup>PAK): 3-LEAD









DETAIL A (ROTATED 90°)



_	,	—b <del>-</del> -b	 			1
2	T			C	_ (	<u>-</u>
	SE	^TIC	M	ا م		1

- 1. Plane B includes maximum features of heat sink tab and plastic.
- 2. No more than 25 % of L1 can fall above seating plane by max. 8 mils.
- 3. Pin-to-pin coplanarity max. 4 mils.
- 4. \*: Thin lead is for SUB, SYB. Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

6 This feature is for thick lead.

		INCHES		MILLIMETERS			
	DIM.	MIN.	MAX.	MIN.	MAX.		
Α		0.160	0.190	4.064	4.826		
	b	0.020	0.039	0.508	0.990		
	b1	0.020	0.035	0.508	0.889		
	b2	0.045	0.055	1.143	1.397		
c*	Thin lead	0.013	0.018	0.330	0.457		
	Thick lead	0.023	0.028	0.584	0.711		
c1	Thin lead	0.013	0.017	0.330	0.431		
CI	Thick lead	0.023	0.027	0.584	0.685		
	c2	0.045	0.055	1.143	1.397		
	D	0.340	0.380	8.636	9.652		
	D1	0.220	0.240	5.588	6.096		
	D2	0.038	0.042	0.965	1.067		
	D3	0.045	0.055	1.143	1.397		
	D4	0.044	0.052	1.118	1.321		
	Е	0.380	0.410	9.652	10.414		
	E1	0.245	-	6.223	-		
	E2	0.355	0.375	9.017	9.525		
	E3	0.072	0.078	1.829	1.981		
	е	0.100	BSC	2.54 BSC			
	K	0.045	0.055	1.143	1.397		
	L	0.575	0.625	14.605	15.875		
	L1	0.090	0.110	2.286	2.794		
	L2	0.040	0.055	1.016	1.397		
	L3	0.050	0.070	1.270	1.778		
	L4	0.010 BSC		0.254 BSC			
	М	-	0.002	-	0.050		
ECN: T13-0707-Rev. K, 30-Sep-13							

DWG: 5843





### RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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